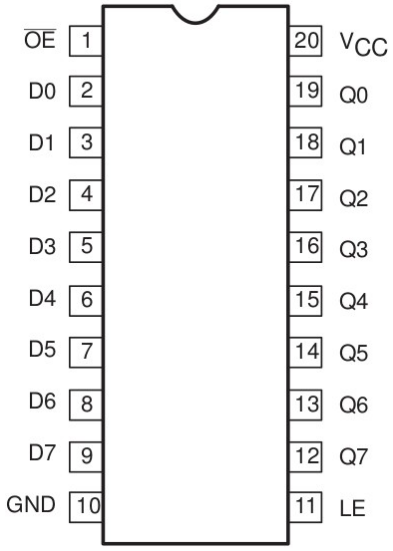
# RB411GL

| GPIO | Connections |
|---|---|
| 0 | NAND I/O1 & **LVC573A** D0 |
| 1 | NAND I/O2 & **LVC573A** D1 |
| 2 | NAND I/O3 & **LVC573A** D2 |
| 3 | NAND I/O4 & **LVC573A** D3 |
| 4 | NAND I/O5 & **LVC573A** D4 |
| 5 | NAND I/O6 & **LVC573A** D5 & **NAND** RY/BY |
| 6 | NAND I/O7 & **LVC573A** D6 & reset TP309 |
| 7 | NAND I/O8 & **LVC573A** D7 & reset S301 |
| 8 | NAND R/W? (C5K) |
| 9 | **BUZZER** & serial RX |
| 10 | TL555C OUT (input voltage meter) & serial TX |
| 11 | LVC573A LE |
| 12 | *(LOW)* |
| 13 | *(LOW)* |
| 14 | *(LOW)* |
| 15 | *(LOW)* |

GPIOs 12-15 are always listed as low in /sys/kernel/debug/gpio regardless of a value set in /sys/class/gpioX/value

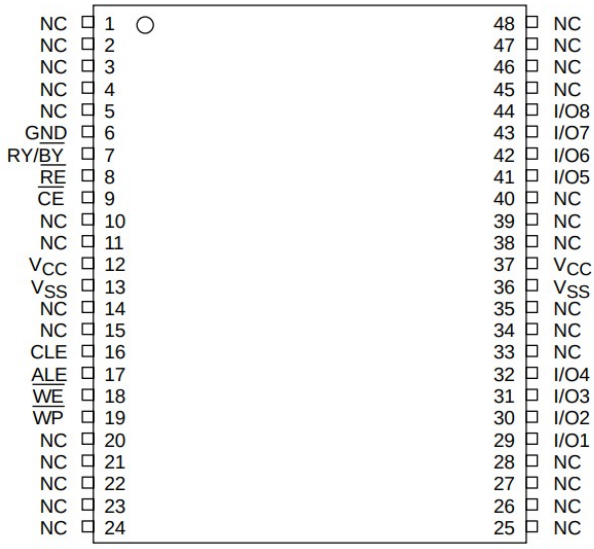
| **HC595** | | 74HC595 | | |
|---|---|---|---|---|
| LD303 | Q1 | 1 Q1 | VCC 16 | Vcc | Vcc |
| LD306 | Q2 | 2 Q2 | Q0 15 | Q0 | LD304 |
| LD307 | Q3 | 3 Q3 | DS 14 | DS | SPI DI |
| LD305 | Q4 | 4 Q4 | $\overline{OE}$ 13 | OE | GND |
| *(LOW)* | Q5 | 5 Q5 | ST_CP 12 | ST_CP | **LVC573A** Q6 |
| *(HIGH)* | Q6 | 6 Q6 | SH_CP 11 | SH_CP | SPI CLK |
| LSP5502 (USB power) | Q7 | 7 Q7 | $\overline{MR}$ 10 | MR | (with NAND WP) HIGH |
| GND | GND | 8 GND | Q7' 9 | Q7' | |

| **LVC573A** | | | | | |
|---|---|---|---|---|---|
| GND | OE |  | Vcc | Vcc |
| NAND I/O1 **GPIO0** | D0 | | Q0 | NAND CE |
| NAND I/O2 **GPIO1** | D1 | | Q1 | NAND READ? (C5K) |
| NAND I/O3 **GPIO2** | D2 | | Q2 | NAND ALE |
| NAND I/O4 **GPIO3** | D3 | | Q3 | NAND CLE |
| NAND I/O5 **GPIO4** | D4 | | Q4 | LD302 |
| NAND I/O6 **GPIO5** | D5 | | Q5 | |
| NAND I/O7 **GPIO6** | D6 | | Q6 | **HC595** ST_CP |
| NAND I/O8 **GPIO7** | D7 | | Q7 | |
| GND | GND | | LE | **GPIO11** |

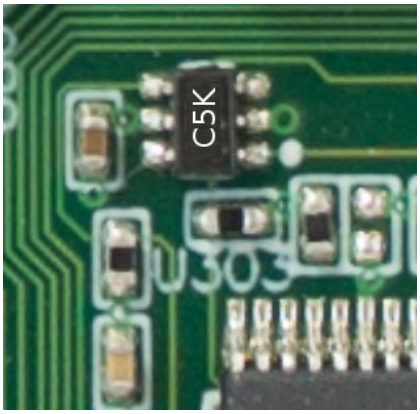


**Toshiba TC58DVG02D5TA00 NAND Flash**

This is actually a pinout from **TC58DVG02**A1FT00 as I could not find a **TC58DVG02**D5TA00 datasheet…




| I/O1 to I/O8 | I/O port |
|---|---|
| $\overline{CE}$ | Chip enable |
| $\overline{WE}$ | Write enable |
| $\overline{RE}$ | Read enable |
| CLE | Command latch enable |
| ALE | Address latch enable |
| $\overline{WP}$ | Write protect |
| RY/$\overline{BY}$ | Ready/Busy |
| GND | Ground input |
| $V_{CC}$ | Power supply |
| $V_{SS}$ | Ground |

| **U303 (C5K)**<br>also marked as „AF” there | | |
|---|---|---|
| **GPIO8** [PULL-UP] |  | **NAND** WE [PULL-UP] |
| Vcc | | GND |
| **LVC573A** Q1 | | **NAND** RE [PULL-UP] |

| Input | | Output | |
|---|---|---|---|
| **GPIO8** | LVC Q1 | WE | RE |
| 0 | 0 | **0** | 1 |
| 0 | 1 | 1 | **0** |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 1 | 1 |