

10V Drive Nch MOSFET

RCJ330N25

Structure

Silicon N-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage V_{GSS} garanteed to be $\pm 30 \text{V}$.
- 4) High package power.

Application

Switching

Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	1000
RCJ330N2	5	0

●Absolute maximum ratings (Ta = 25°C)

Paramete	er	Symbol	Limits	Unit
Drain-source voltage	V_{DSS}	250	V	
Gate-source voltage	V_{GSS}	±30	V	
Drain current	Continuous	I _D *3	±33	Α
Diain current	Pulsed	I _{DP} *1	±132	Α
Source current	Continuous	s *3	26	Α
(Body Diode)	Pulsed	I _{SP} *1	104	Α
Avalanche current		I _{AS} *2	16.5	Α
Avalanche energy		E _{AS} *2	74.8	mJ
Power dissipation (Tc=2	P_{D}	211	W	
Channel temperature	Tch	150	°C	
Range of storage temper	Tstg	-55 to +150	°C	

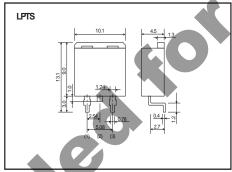
^{*1} Pw≤10µs, Duty cycle≤1%

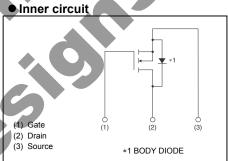
• Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	Rth(j-c) *	0.59	°C / W

^{*} T_C=25°C

Dimensions (Unit : mm)





^{*2} Lidots500μH, V_{DD}=50V, Rg=25Ω, starting Tch=25°C

^{*3} Limited only by maximum temperature allowed.

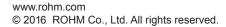
● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	-	-	±100	nA	V_{GS} =±30V, V_{DS} =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	250	-	-	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	-	1	μА	V _{DS} =250V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	3	-	5	٧	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS (on} *	-	77	105	mΩ	I _D =16.5A, V _{GS} =10V
Forward transfer admittance	I Y _{fs} I*	10	20	-	S	I _D =16.5A, V _{DS} =10V
Input capacitance	C _{iss}	1	4500	-	pF	V _{DS} =25V
Output capacitance	C _{oss}	1	220	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	130	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	50	-	ns	I _D =16.5A, V _{DD} ≒125V
Rise time	t _r *	1	200	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	1	120	-	ns	$R_L=7.6\Omega$
Fall time	t _f *	-	140	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	-	80	-	nC	I _D =33A,
Gate-source charge	Q _{gs} *	-	25	-	nC	V _{DD} ≒125V
Gate-drain charge	Q _{gd} *	-	27	-	nC	V _{GS} =10V

^{*}Pulsed

●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	- ,	-	1.5	٧	I _s =33A, V _{GS} =0V
*Pulsed				O'	9	
40						



●Electrical characteristic curves (Ta=25°C)

Fig.1 Typical Output Characteristics (I)

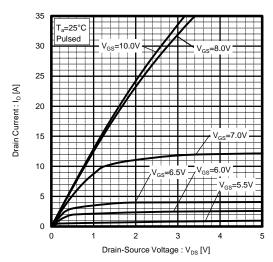


Fig.3 Typical Transfer Characteristics

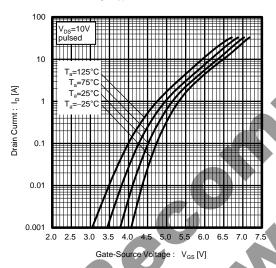


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

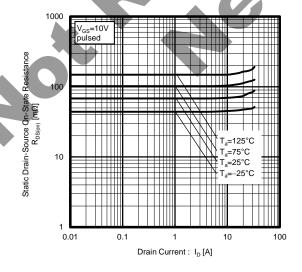


Fig.2 Typical Output Characteristics (${\rm I\hspace{-.1em}I}$)

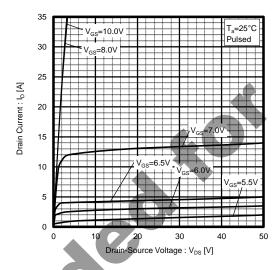


Fig.4 Gate Threshold Voltage vs. Channel Temperature

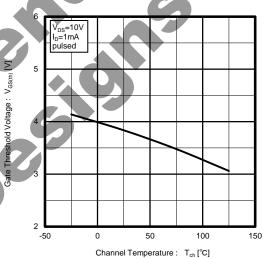
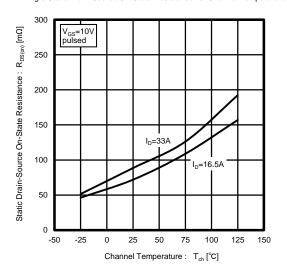
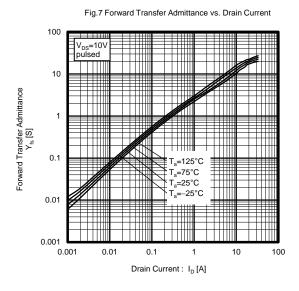
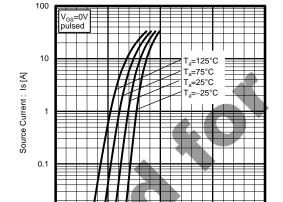


Fig.6 Static Drain-Source On-State Resistance vs. Channel Temperature



2.0



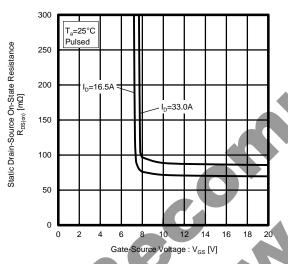


0.01

0.0

Fig.8 Source Current vs. Source-Drain Voltage







Source-Drain Voltage: V_{SD} [V]

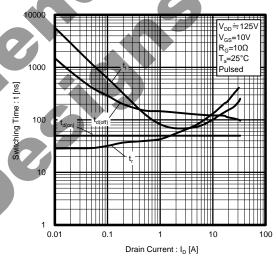


Fig.11 Dynamic Input Characteristics

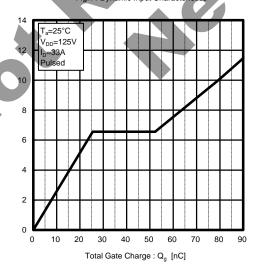
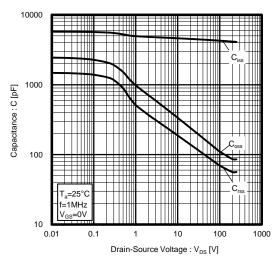
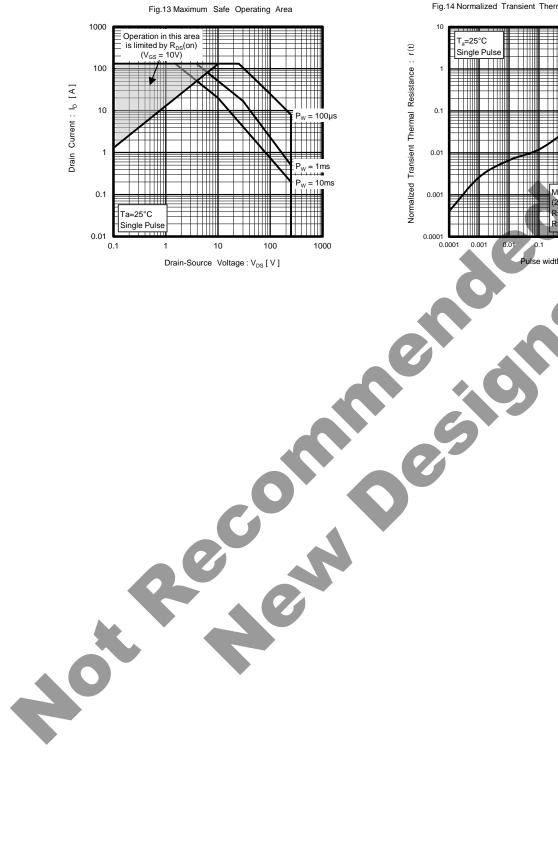
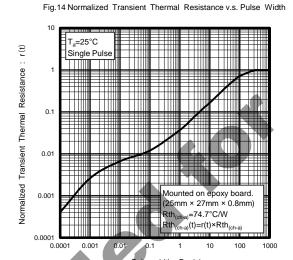


Fig.12 Typical Capacitance vs. Drain-Source Voltage







• Measurement circuits

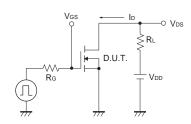


Fig.1-1 Switching Time Measurement Circuit

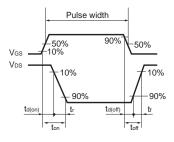


Fig.1-2 Switching Waveforms

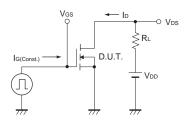


Fig.2-1 Gate Charge Measurement Circuit

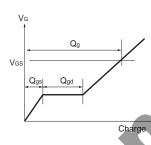


Fig.2-2 Gate Charge Waveform

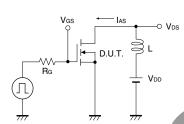


Fig.3-1 Avalanche Measurement Circu

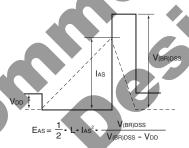


Fig.3-2 Avalanche Waveform

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 1	1		
JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCTI	CLASS II b	CLASSIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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