

RCJ700N20

Nch 200V 70A Power MOSFET

V_{DSS}	200V
R _{DS(on)} (Max.)	42.7m $Ω$
I _D	70A
P_D	297W

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free lead plating; RoHS compliant
- 6) 100% Avalanche tested

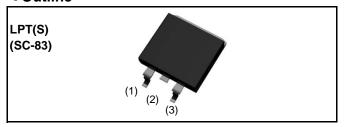
Application

Switching Power Supply

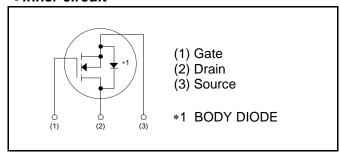
Automotive Motor Drive

Automotive Solenoid Drive

Outline



•Inner circuit



Packaging specifications

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	Packaging	Taping		
	Reel size (mm)	330		
Tuno	Tape width (mm)	24		
Туре	Basic ordering unit (pcs)	1,000		
	Taping code	TL		
	Marking	RCJ700N20		

●Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	200	V	
Continuous drain current	T _c = 25°C	I _D *1	±70	А
Continuous drain current	T _c = 100°C	I _D *1	±38	А
Pulsed drain current	I _{D,pulse} *2	±140	А	
Gate - Source voltage	V_{GSS}	±30	V	
Avalanche energy, single pulse	E _{AS} *3	396	mJ	
Avalanche current		I _{AR} *3	35	А
$T_c = 25$ °C		P _D	297	W
Power dissipation $T_a = 25^{\circ}C^{*4}$		P _D	1.56	W
Junction temperature	T _j	150	°C	
Range of storage temperature	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter	Symbol	Values			Unit	
- Farameter	Symbol	Min.	Тур.	Max.	Offic	
Thermal resistance, junction - case	R_{thJC}	-	-	0.42	°C/W	
Thermal resistance, junction - ambient *4	R_{thJA}	-	-	80	°C/W	
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C	

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.		
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	200	-	-	V	
		$V_{DS} = 200V, V_{GS} = 0V$	-	-	25		
Zero gate voltage drain current	I _{DSS}	$T_j = 25^{\circ}C$				μΑ	
		$V_{DS} = 200V, V_{GS} = 0V$ $T_j = 125^{\circ}C$	-	-	100		
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA	
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	3.0	-	5.0	V	
		$V_{GS} = 10V, I_D = 35A$	-	30.5	42.7		
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 10V, I_D = 35A$	_	62.0	87.0	mΩ	
		T _j = 125°C		02.0	07.0		
Forward transfer admittance	g_{fs}	$V_{DS} = 10V, I_{D} = 35A$	15.3	30.6	-	S	

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions		Unit		
r ai ai ii e lei	Symbol	Symbol Conditions -		Тур.	Max.	Offic
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	6900	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	400	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	230	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 100 \text{V}, V_{GS} = 10 \text{V}$	-	70	-	
Rise time	t _r *5	$I_D = 35A$	-	340	-	nc
Turn - off delay time	t _{d(off)} *5	$R_L = 2.8\Omega$	-	160	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	160	-	

● Gate Charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Symbol Conditions -		Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 100V$	-	125	-	
Gate - Source charge	Q _{gs} *5	I _D = 70A	-	40	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	50	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 100 V, I_D = 70 A$	-	7.0	-	V

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Cymbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous source current	l _S *1	T _c = 25°C	-	1	70	Α
Pulsed source current	I _{SM} *2	1 c = 25 C	-	-	140	Α
Forward voltage	V_{SD}^{*5}	$V_{GS} = 0V, I_{S} = 70A$	-	-	1.5	V
Reverse recovery time	t _{rr} *5	I _S = 35A	-	130	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	600	-	nC

^{*1} Limited only by maximum temperature allowed.

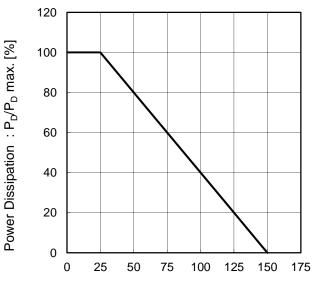
*5 Pulsed

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L \simeq 500 μ H, V_{DD} = 50V, Rg = 25 Ω , starting T_j = 25°C

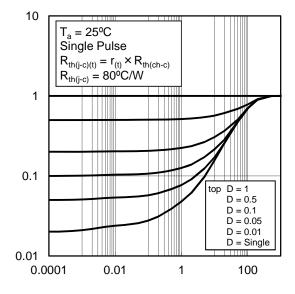
^{*4} Mounted a epoxy PCB FR4 (25×27×0.8mm)

Fig.1 Power Dissipation Derating Curve



Junction Temperature : T_i [°C]

Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: P_W [s]

Normalized Transient Thermal Resistance: r_(t)

Fig.3 Avalanche Current vs Inductive Load

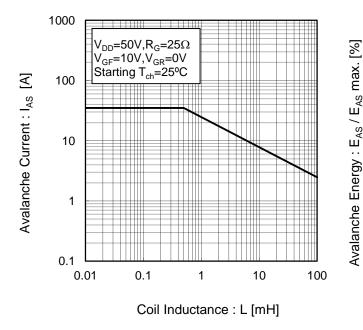


Fig.4 Avalanche Energy Derating Curve vs Junction Temperature

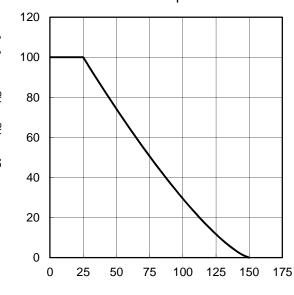
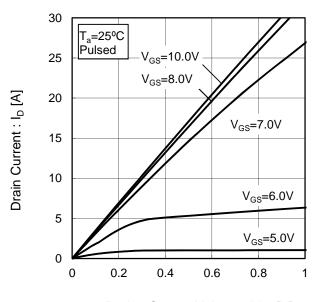


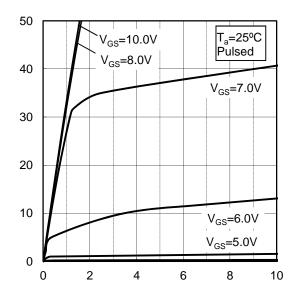
Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)

Junction Temperature : T_i [°C]



Drain - Source Voltage : V_{DS} [V]

Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs. Junction Temperature 280 Normarize Drain - Source Breakdown Voltage $V_{GS} = 0V$ 270 $I_D = 1mA$ 260 250 240 230 220 210 200 190 180 -50 50 100 150 Junction Temperature : T_i [°C]

100 V_{DS}= 10V 10 Drain Current: I_D [A] 1 0.1 $T_a = 125^{\circ}C$ T_a= 75°C 0.01 T_a= 25°C $T_a = -25^{\circ}C$ 0.001 4 0 3 5 6 8 9 10

Gate - Source Voltage : V_{GS} [V]

Fig.10 Transconductance vs. Drain Current

Fig.8 Typical Transfer Characteristics

vs. Junction Temperature 5.0 $V_{DS} = 10V$ $I_D = 1 \text{mA}$ Gate Threshold Voltage: V_{GS(th)} [V] 4.5 4.0 3.5 3.0 2.5 -50 -25 0 25 50 75 100 125 150

Junction Temperature : T_i [°C]

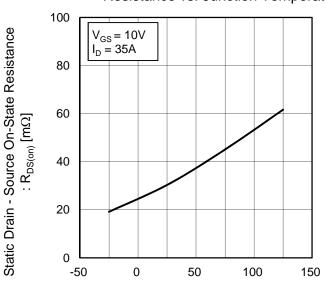
Fig.9 Gate Threshold Voltage

100 V_{DS}= 10V 10 Fransconductance: g_{fs} [S] $T_a = -25^{\circ}C$ T_a=25°C _=75°C 0.1 T_a=125ºC 0.01 0.01 0.1 1 10 100 Drain Current : I_D [A]

Fig.11 Static Drain - Source On - State Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage Resistance vs. Drain Current(I) 100 1000 Static Drain - Source On-State Resistance T_a=25°C T_a=25°C Static Drain - Source On-State Resistance 80 I_D = 35A 100 60 $:R_{DS(on)}\left[m\Omega \right]$ $I_D = 50A$ $:R_{\text{DS(on)}}\left[\text{m}\Omega \right]$ 40 10 20 0 20 0 5 10 15 0.01 0.1

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Gate - Source Voltage : V_{GS} [V]



1

Drain Current : I_D [A]

10

100

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

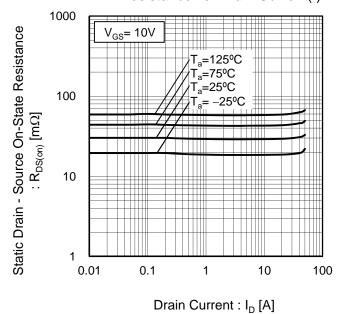
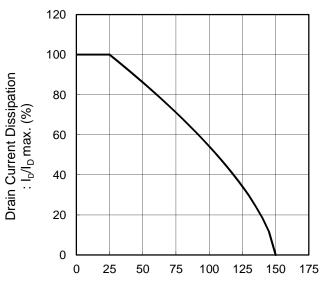


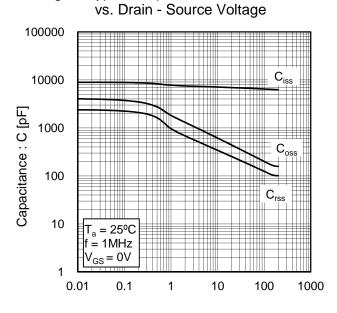
Fig.15 Drain Current Derating Curve



Junction Temperature : T_i [°C]

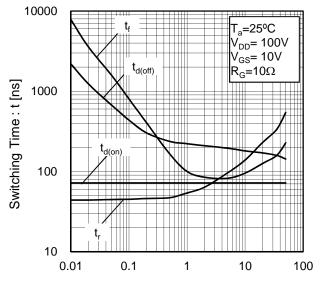


Fig.16 Typical Capacitance



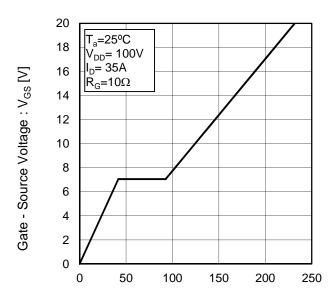
Drain - Source Voltage : V_{DS} [V]

Fig.17 Switching Characteristics



Drain Current : I_D [A]

Fig.18 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]

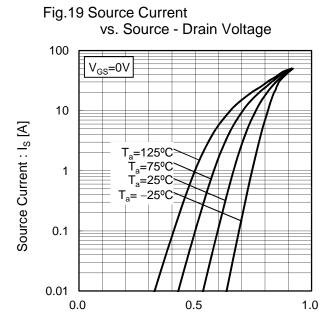


Fig20 Reverse Recovery Time
vs. Source Current

1000

Ta=25°C
di / dt = 100A / \(\mu \)
V_{GS} = 0V

10

0.1

1 10 100

Source Current : I_S [A]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

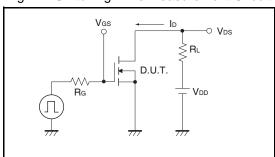


Fig.2-1 Gate Charge Measurement Circuit

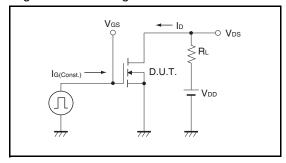


Fig.3-1 Avalanche Measurement Circuit

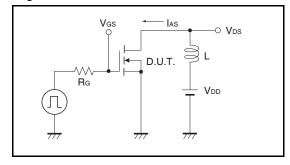


Fig.1-2 Switching Waveforms

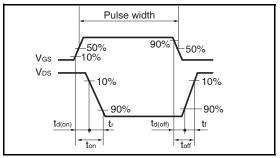


Fig.2-2 Gate Charge Waveform

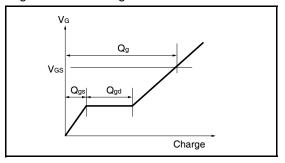
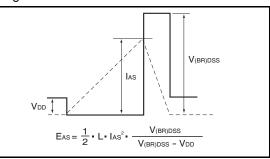
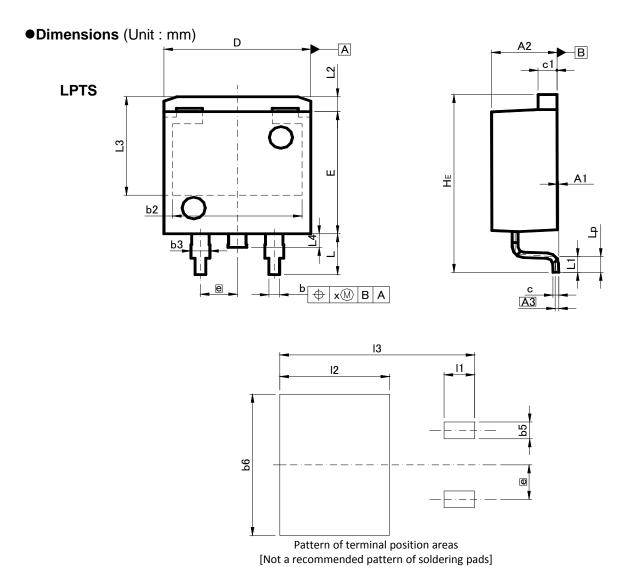


Fig.3-2 Avalanche Waveform





DIM	MILIM	ETERS	INC	HES
ואונט	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.	90	0.3	50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	0.90	1.50	0.035	0.059
L2	1.	10	0.043	
L3	7.	7.25 0.28		285
L4	1.	00	0.0	139
Lp	0.90	1.50	0.035	0.059
Х	_	0.25	_	0.010

DIM	MILIMETERS IN		INC	CHES	
DIM	MIN	MAX	MIN	MAX	
bb	_	1.23	-	0.049	
b6	_	10.40	-	0.409	
	_	2.10	-	0.083	
12	_	7.55	-	0.297	
13	_	13 40	_	0.528	

Dimension in mm / inches

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CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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