

RClamp7002M RailClamp<sup>®</sup> Low Capacitance TVS Diode Array

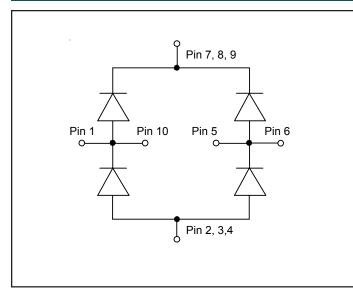
#### PROTECTION PRODUCTS - RailClamp<sup>®</sup> Description

RailClamps are surge rated diode arrays designed to protect high speed data interfaces. The RClamp series has been specifically designed to protect sensitive components from overvoltage caused by **ESD** (electrostatic discharge), **CDE** (cable discharge events), **EFT** (electrical fast transients), and **lightning**. The RClamp<sup>™</sup>7002M has been optimized for use on the next generation of ADSL interfaces. Their high surge capability makes them particularly useful in systems that utilize gas tubes as the primary protection component.

This device integrates surge rated, low capacitance steering diodes in a small MSOP-10L package. Each protection diode features a large area junction to minimize clamping voltage and increase reliability during high current surges. It has a maximum capacitance of only 10pF and may be used to protect two high-speed lines without sacrificing signal integrity. The RClamp7002M may be used on systems operating from 3.3 to 70 volts. This means that they may be used in applications that use the Central Office battery as the main supply.

The RClamp7002M is also designed for easy board layout by allowing the signal traces to run straight through the device. The high surge capability, ease of layout coupled with the low capacitance and clamping voltage of the RClamp7002M makes it the superior choice for protecting two high speed lines against high energy telecommunication surges.

#### Circuit Diagram



#### Features

- ◆ 1000 Watts peak pulse power (8/20µs)
- Protection for high-speed data lines to
  IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
  IEC 61000-4-4 (EFT) 40A (5/50ns)
  IEC 61000-4-5 (Lightning) 1kV, 24A (1.2/50µs)
- Low clamping voltage: 10V at lpp = 40A (8/20µs)
- Array of surge rated suppression diodes
- Protects two high speed lines
- Low capacitance: 10pF (I/O to Ground)
- Operating voltage range: 3.3 70V
- Small package saves board space
- Solid-state silicon-avalanche technology

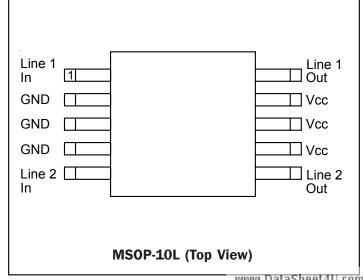
#### **Mechanical Characteristics**

- ◆ JEDEC MSOP-10L package
- Molding compound flammability rating: UL 94V-0
- Marking: Marking code and date code
- ◆ Packaging: Tape and Reel per EIA 481
- Lead Finish: Matte tin
- RoHS/WEEE Compliant

#### Applications

- ADSL Lines
- Central Office Equipment
- ◆ T1/E1 secondary IC Side Protection
- Small Office Home Office (SOHO) Applications
- WAN/LAN Equipment
- ♦ ISDN S/T Interface
- ◆ ATM Interfaces

#### **PIN Configuration**



## RClamp7002M



#### **PROTECTION PRODUCTS**

#### Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p = 8/20\mu s$ )	P <sub>PP</sub>	1000	W
Peak Pulse Current ( $t_p = 8/20\mu s$ )	I <sub>pp</sub>	70	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	$V_{ESD}$	15 8	kV
Operating Temperature	T,	-55 to +125	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

#### Electrical Characteristics (T=25°C)

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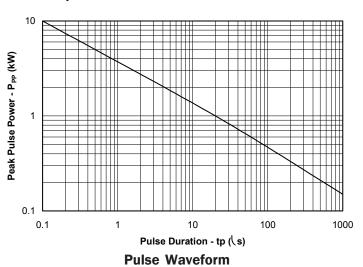
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				70	V
Reverse Breakdown Voltage	V <sub>BR</sub>	Ι <sub>t</sub> = 50μΑ	85			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 70V, T=25°C			5	μA
Forward Voltage	V <sub>f</sub>	I <sub>r</sub> = 15mA			1.2	V
Clamping Voltage	V <sub>c</sub>	I <sub>pp</sub> = 1A, tp = 8/20µs Any I/O pin to Ground			1.5	V
Clamping Voltage	V <sub>c</sub>	I <sub>PP</sub> = 40A, tp = 8/20µs Any I/O pin to Ground			10	V
Clamping Voltage	V <sub>c</sub>	I <sub>pp</sub> = 70A, tp = 8/20µs Any I/O pin to Ground			14.3	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> = 0V, f = 1MHz Any I/O pin to Ground			10	pF
		V <sub>R</sub> = OV, f = 1MHz Between I/O pins		3		pF

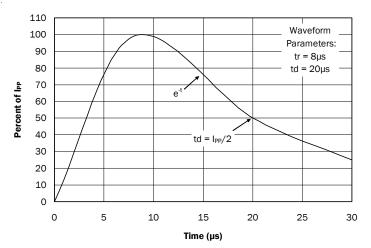


### PROTECTION PRODUCTS

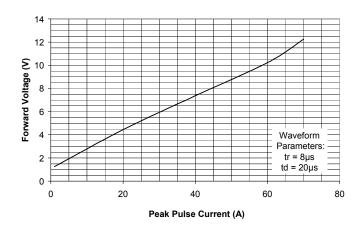
#### Typical Characteristics

#### Non-Repetitive Peak Pulse Power vs. Pulse Time

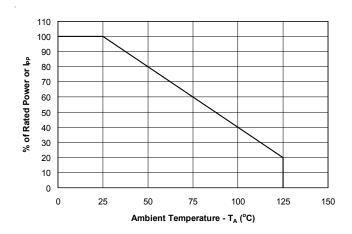




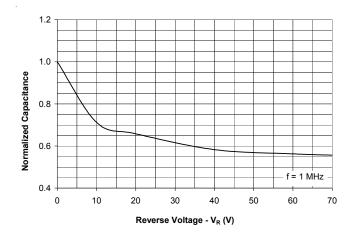
Forward Voltage vs. Forward Current



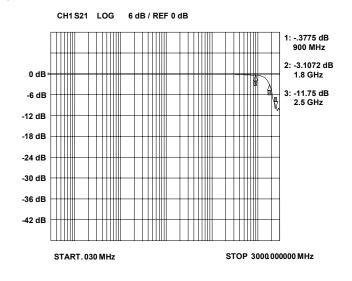
**Power Derating Curve** 



**Capacitance vs. Reverse Voltage** 







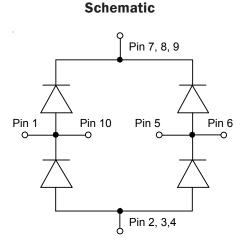


#### PROTECTION PRODUCTS Applications

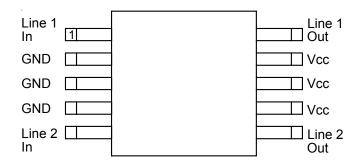
#### Device Connection Options for Protection of Two High-Speed Data Lines

This device is designed to protect two data lines from transient over-voltages by clamping them to a fixed reference. When the voltage on the protected line exceeds the reference voltage (plus diode  $V_F$ ) the steering diodes are forward biased, conducting the transient current away from the sensitive circuitry. Data line inputs are connected at pins 1 and 5. The outputs are connected at pins 10 and 6 respectively. The negative reference is connected at pins 2, 3, and 4. These pins should be connected directly to a ground plane on the board for best results. The path length is kept as short as possible to minimize parasitic inductance. The positive reference is connecting the positive reference are as follows:

- 1. To protect data lines and the power line, connect pins 7, 8 and 9 directly to the positive supply rail  $(V_{cc})$ . In this configuration the data lines are referenced to the supply voltage. An external TVS diode may be added between the supply rail and ground in order to prevent over-voltage on the supply rail.
- In applications where no positive supply reference is available, or complete supply isolation is desired, an external TVS diode may be used as the reference. The steering diodes will begin to conduct when the voltage on the protected line exceeds the working voltage of the TVS (plus one diode drop).









#### **PROTECTION PRODUCTS**

Applications Information - Spice Model

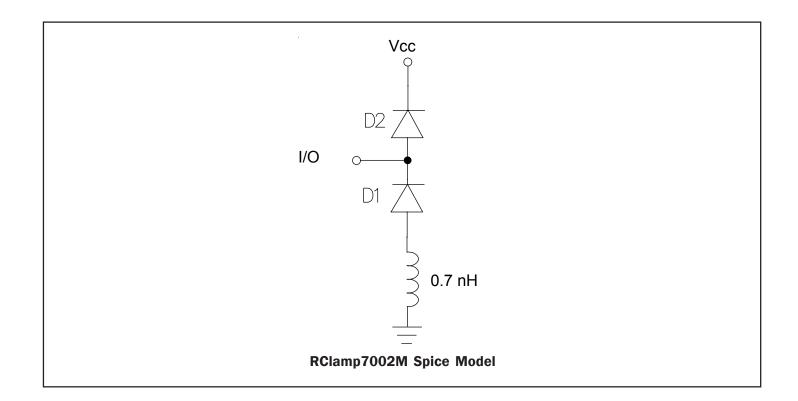


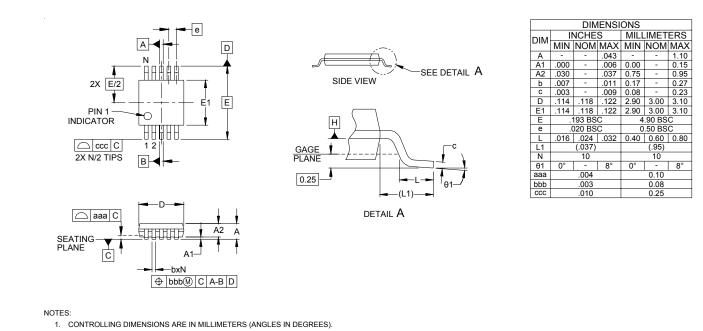
Table 1 - RClamp7002M Spice Parameters				
Parameter	Unit	D1 (LCRD)	D2 (LCRD)	
IS	Amp	1.0E-20	1.0E-20	
BV	Volt	200	200	
۲۷	Volt	0.53	0.53	
RS	Ohm	0.037	0.14	
IBV	Amp	1E-3	1E-3	
CJO	Farad	3.5E-12	3.5E-12	
TT	sec	2.541E-9	2.541E-9	
М		0.142	0.142	
N		1.1	1.1	
EG	eV	1.11	1.11	

# SEMTECH

# RClamp7002M

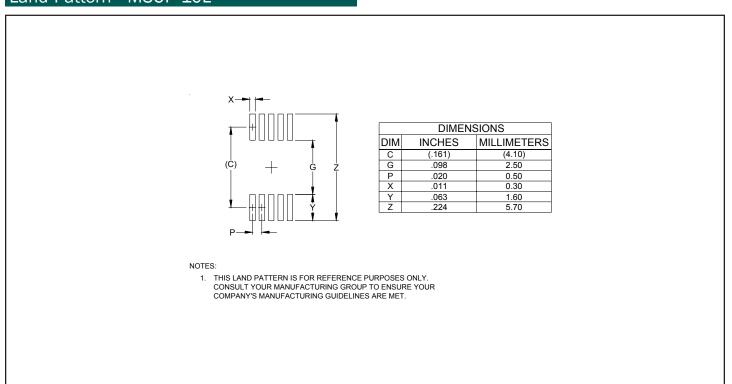
#### PROTECTION PRODUCTS

#### Outline Drawing MSOP 10L



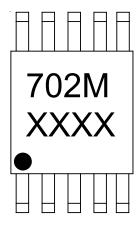
- 2. DATUMS -A- AND -B- TO BE DETERMINED AT DATUM PLANE -H-
- DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 4. REFERENCE JEDEC STD MO-187, VARIATION BA.

#### Land Pattern - MSOP 10L





#### Marking Codes



\* XXXX = Date Code

\*\* Dot indicates Pin 1

#### **Ordering Information**

Part Number	Working Voltage	Qty per Reel	Reel Size
RClamp7002M.TBT	5V	500	7 Inch

Note: Lead finish is matte tin

RailClamp and RClamp are marks of Semtech Corporation

#### **Contact Information**

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