

PROTECTION PRODUCTS

Description

RClamp®01511ZC is an ultra low capacitance FemtoClamp™ ESD protection device specifically designed to protect high-speed differential lines. It offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp01511ZC features excellent ESD protection characteristics highlighted by low peak ESD clamping voltage, and high ESD withstand voltage per IEC 61000-4-2 ($\pm 10\text{kV}$ contact). RClamp01511ZC has a maximum capacitance of only 0.2pF and extremely low insertion loss allowing it to be used on high speed lines such as Thunderbolt 3 and USB 4.

RClamp01511ZC is in a DFN 0.60x0.30x0.25mm 2-Lead package. The small package gives the designer the flexibility to protect single lines in applications where arrays are not practical.

Features

- ESD withstand voltage
 - ♦ IEC 61000-4-2 (ESD) $\pm 10\text{kV}$ (contact)
- Low ESD Clamping
- Ultra-Low capacitance: 0.2pF Maximum
- Protects one high-speed data line
- Working voltage: 1.5V
- Low reverse leakage current: 100nA max at $V_R=1.5\text{V}$
- Solid-state silicon-avalanche technology

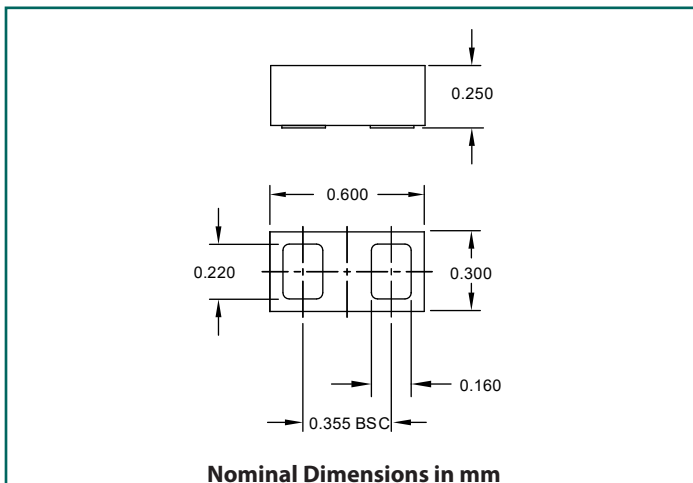
Mechanical Characteristics

- Package: DFN 0.60x0.30x0.25mm 2-Lead
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Lead Finish: Pb-Free
- Marking : Marking Code
- Packaging : Tape and Reel

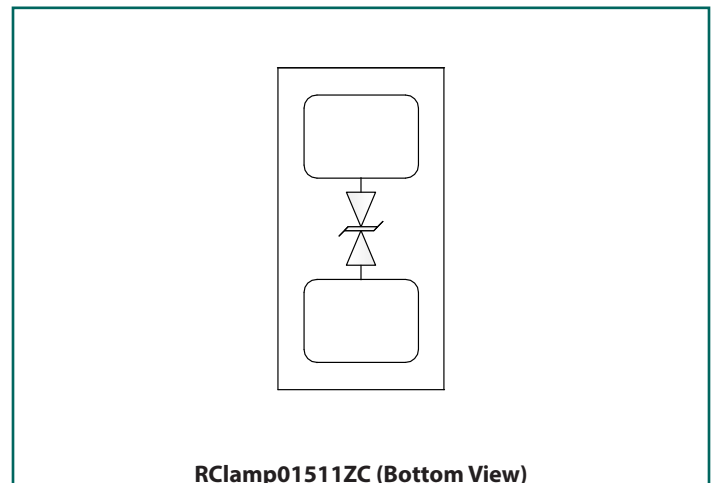
Applications

- Thunderbolt 3
- USB4
- USB3.2 Gen 1/Gen 2/Gen 2x2

Nominal Dimensions



Schematic and Pin Configuration



Absolute Maximum Ratings

Rating	Symbol	Value	Units
Peak Pulse Current (tp = 8/20μs)	I _{PP}	2.5	A
ESD per IEC 61000-4-2 (Contact) ⁽¹⁾ ESD per IEC 61000-4-2 (Air) ⁽¹⁾	V _{ESD}	±10 ±15	kV
Operating Temperature	T _{OP}	-40 to +85	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}	-40°C to 85°C			1.5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 10mA	1.8	2.5	3.1	V
Reverse Leakage Current	I _R	V _{RWM} = 1.5V		<10	100	nA
Clamping Voltage ⁽²⁾	V _C	I _{PP} = 2.5A, tp = 1.2/50μs (Voltage), 8/20μs (Current) Combination Waveform, R _s = 2Ω		4.2		V
ESD Clamping Voltage ⁽³⁾	V _C	tp = 0.2/100ns (TLP)	I _{PP} = 4A	4.8		V
			I _{PP} = 16A	10		
Dynamic Resistance ⁽³⁾⁽⁴⁾	R _{DYN}	tp = 0.2/100ns (TLP)		0.42		Ohms
Junction Capacitance	C _J	V _R = 0V, f = 1MHz		0.17	0.2	pF
Insertion Loss	S ₂₁	f = 5 GHz		0.10		dB
		f = 10 GHz		0.25		

Notes:

(1): ESD gun return path connected to Ground Reference Plane (GRP)

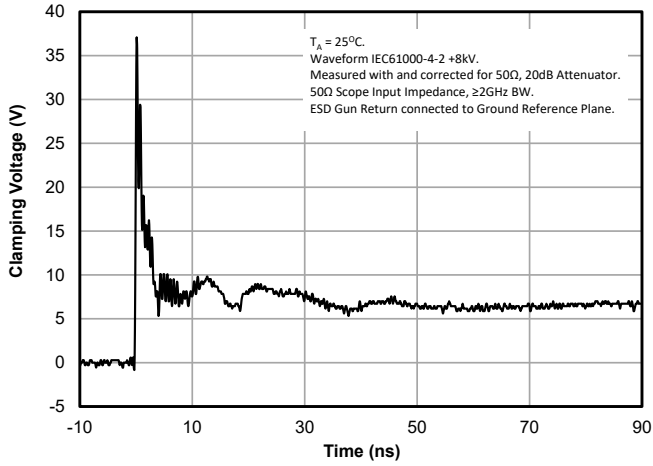
(2): Measured using a 1.2/50μs voltage, 8/20μs current combination waveform, R_s=2 Ohms. Clamping is defined as the peak voltage across the device after the device snaps back to a conducting state.

(3): Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns, I_{TLP} and V_{TLP} averaging window: t₁ = 70ns to t₂ = 90ns.

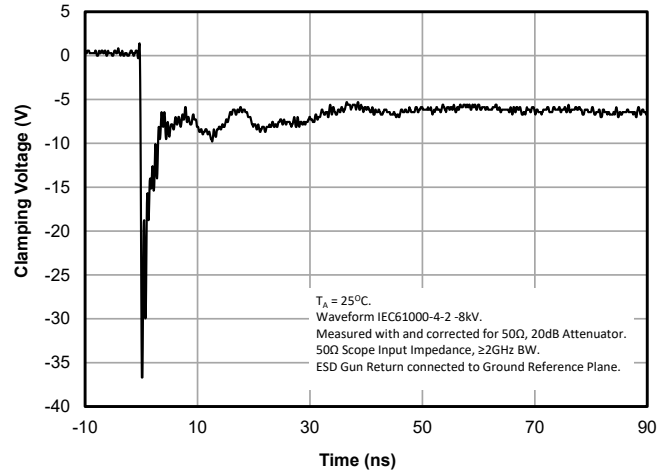
(4): Dynamic resistance calculated from I_{TLP} = 4A to I_{TLP} = 16A

Typical Characteristics

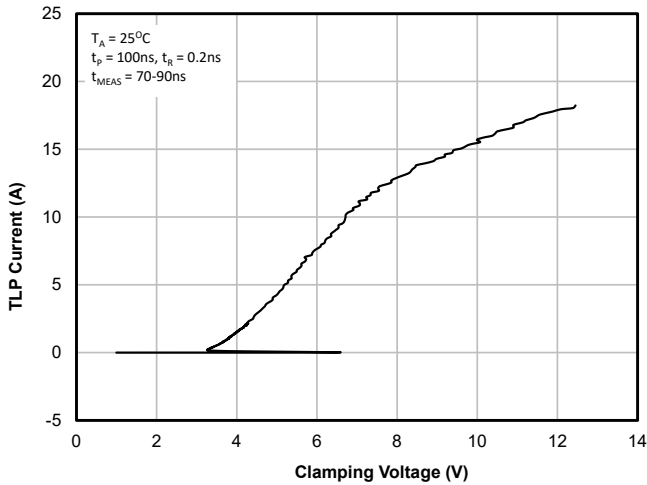
ESD Clamping (+8kV Contact per IEC 61000-4-2)



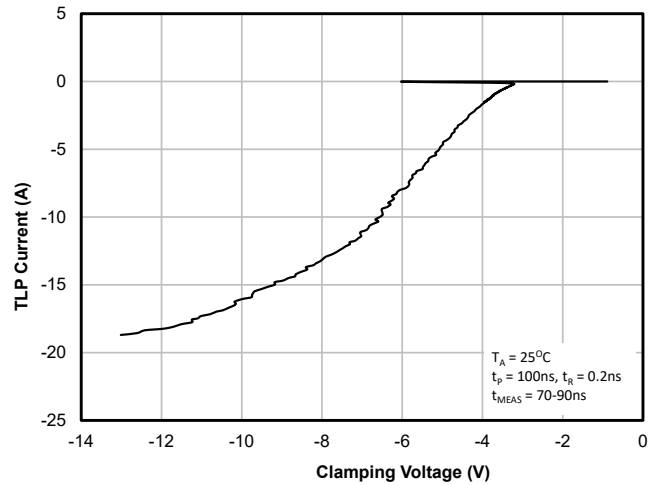
ESD Clamping (-8kV Contact per IEC 61000-4-2)



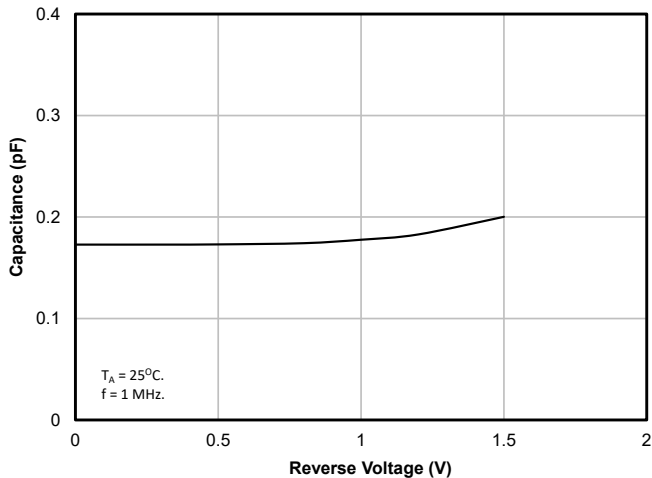
TLP Characteristic (Positive Pulse)



TLP Characteristic (Negative Pulse)

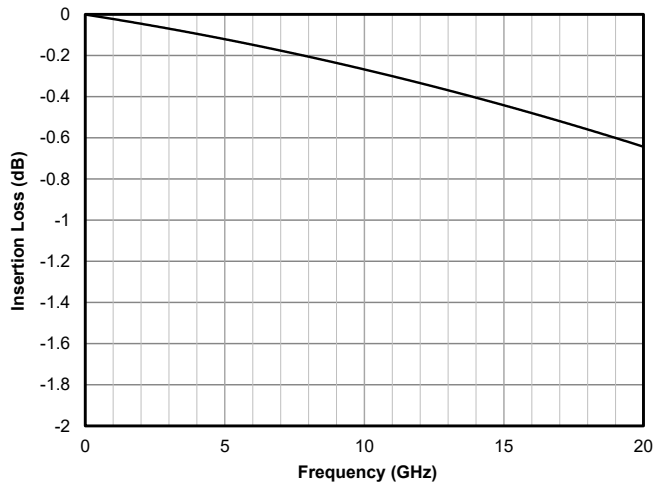


Capacitance vs. Reverse Voltage

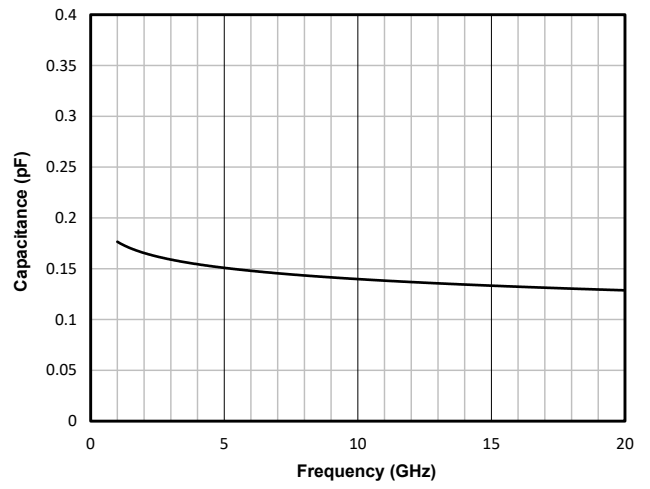


Typical Characteristics

Insertion Loss (S21)



Capacitance vs. Frequency

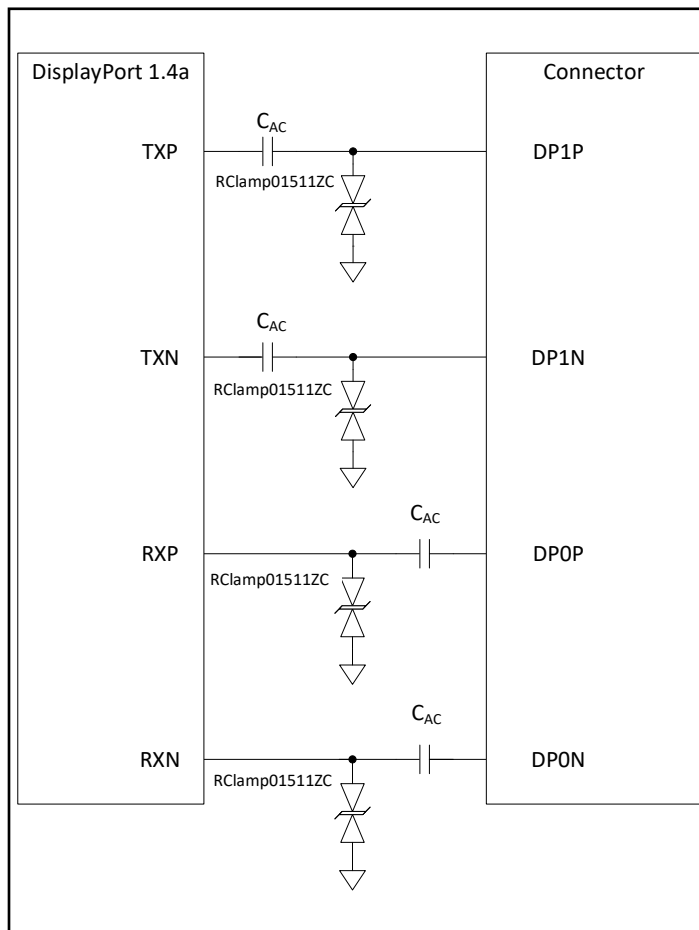


Application Information Continue

DisplayPort Line Protection

When RClamp01511ZC is used to protect a dedicated DisplayPort signal line, AC coupling capacitors should be used on both TX and RX lines as shown in Figure 3. In this case, the TVS should be placed on the connector side of the AC coupling capacitor to block the common mode voltage of the TX signal. On the RX line the TVS should be placed on the transceiver side of the AC coupling capacitor to block the common mode voltages of incoming signals.

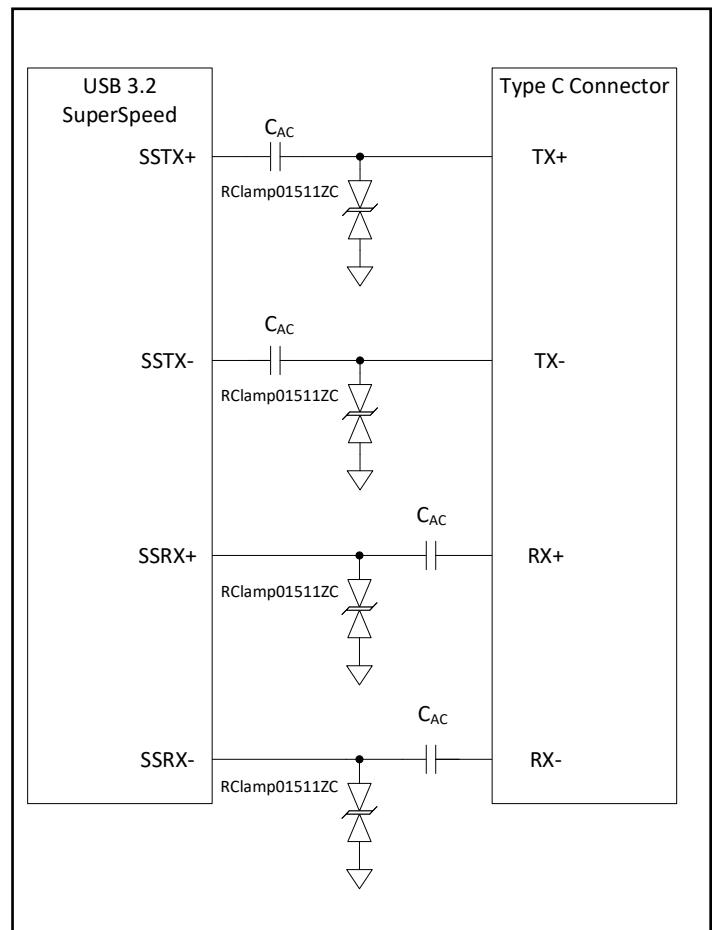
Figure 3 - DisplayPort Line Protection



USB 3.2 and USB4 protection

RClamp01511ZC can also be used to protect USB SuperSpeed lines. Figure 4 shows an example of four SuperSpeed lines which are protected by RClamp01511ZC. As per the USB specifications, AC coupling capacitors are required on TX lines. RClamp01511ZC should be placed on the connector side of the AC coupling capacitor as shown in Figure 3. It is recommended that AC coupling capacitors are used for RX lines. In that case, the TVS should be placed on the transceiver side of the capacitor.

Figure 4 - USB3.2 Protection

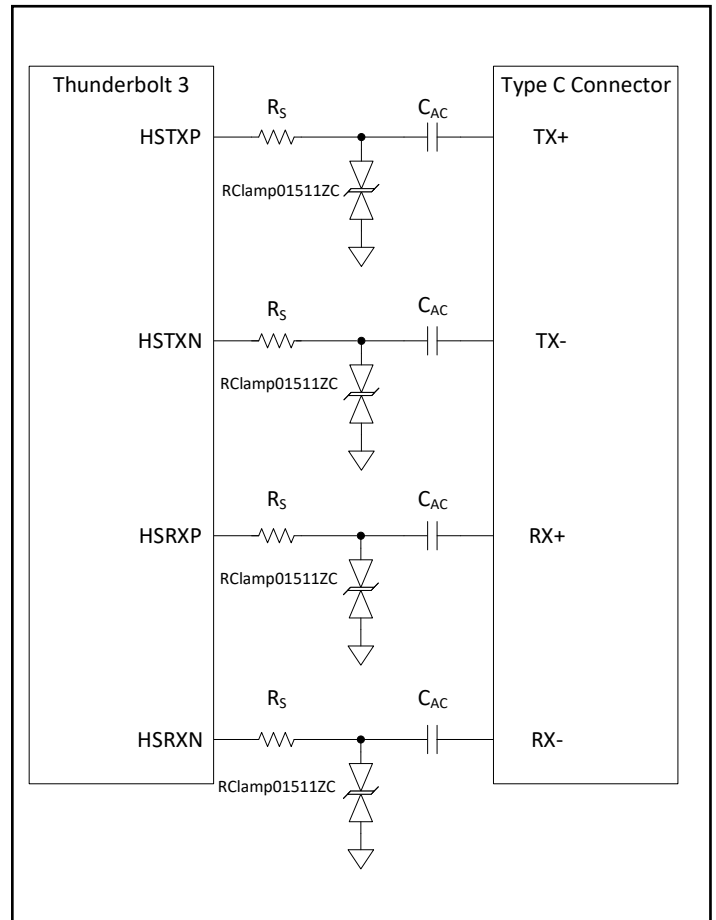


Application Information

Thunderbolt 3 Protection

A typical protection scheme for a differential pair of Thunderbolt 3 high speed data lines is shown in Figure 2. In this configuration the high speed Thunderbolt 3 lines are transmitting with a maximum output voltage of less than 1.5V, therefore the TVS can be placed on the transceiver side of the AC coupling capacitor. By placing RClamp01511ZC on the transceiver side of AC coupling capacitors on the RX line, the voltage across the TVS is restricted to the differential signal voltage, even when operating in alternate modes interfacing to DisplayPort 1.4a and HDMI. These interfaces transmit at higher common mode voltages, and their maximum differential signal voltages are within the operating range of the RClamp01511ZC .

Figure 2 - Thunderbolt 3 Protection



Application Information

Assembly Guidelines

The figure at the right details Semtech's recommended mounting pattern. Recommended assembly guidelines are shown in Table 1. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. Exact manufacturing parameters will require some experimentation to get the desired solder application.

Solder Stencil

Stencil design is one of the key factors which will determine the volume of solder paste which is deposited onto the land pad. The area ratio of the stencil aperture will determine how well the stencil will print. The area ratio takes into account the aperture shape, aperture size, and stencil thickness. A minimum area ratio of 0.66 is preferred for the subject package. The area ratio of a rectangular aperture is given as:

$$\text{Area Ratio} = (L * W) / (2 * (L + W) * T)$$

Where:

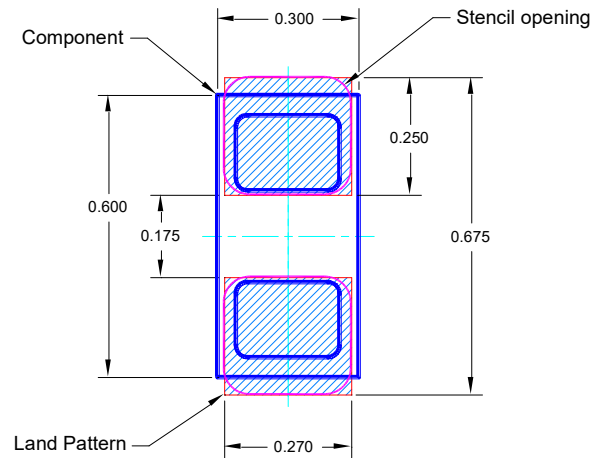
L = Aperture Length

W = Aperture Width

T = Stencil Thickness

Semtech recommends a stencil with square aperture and rounded corners for consistent solder release. The stencil should be laser cut with electro-polished finish. A stencil thickness of 0.075mm (0.003") is recommended. A 0.100mm (0.004") stencil may be used, however the stencil opening may need to be increased slightly to achieve the desired area ratio to ensure proper solder coverage on the pad.

Recommended Mounting Pattern



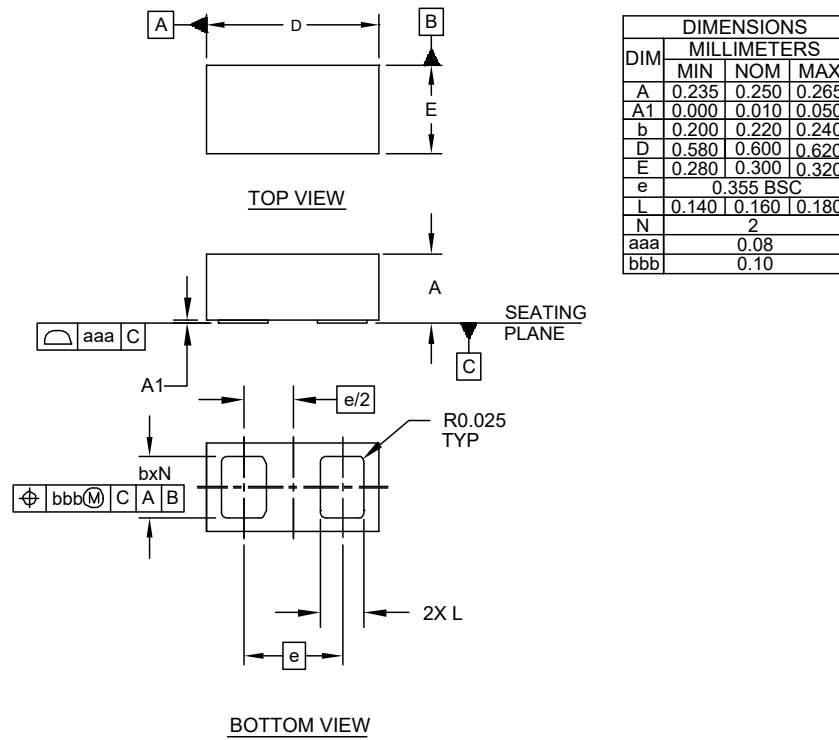
All Dimensions are in mm.

Land Pad.
 Stencil opening
 Component

Table 1 - Assembly Guidelines

Assembly Parameter	Recommendation
Solder Stencil Design	Laser Cut, Electro-Polished
Aperture Shape	Rectangular with Rounded Corners
Solder Stencil Thickness	0.075mm (0.003") or 0.100mm (0.004")
Solder Paste Type	Type 4 Size Sphere or Smaller
Solder Reflow Profile	Per JEDEC J-STD-020
PCB Solder Pad Design	Solder Mask Defined or Non Solder Mask Defined
PCB Pad Finish	OSP or NiAu

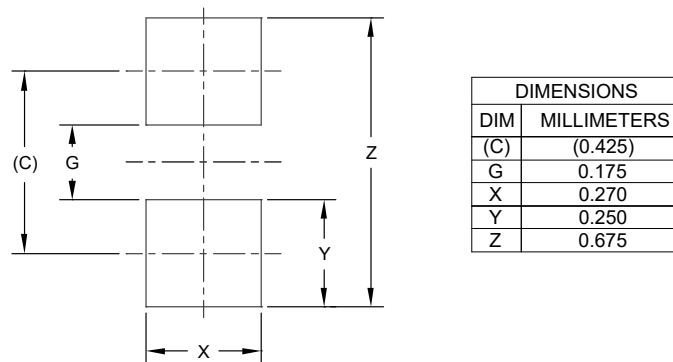
Outline Drawing - DFN 0.60x0.30x0.25mm 2-Lead



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Land Pattern - DFN 0.60x0.30x0.25mm 2-Lead



NOTES:

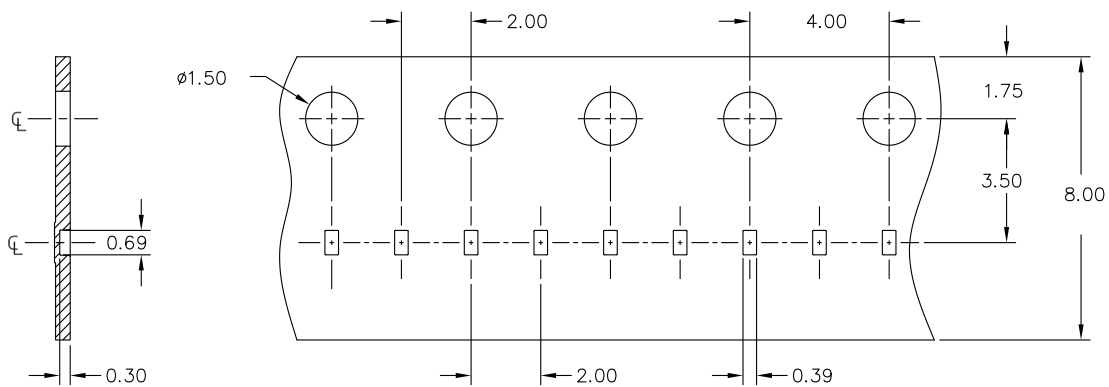
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.
CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Marking Code

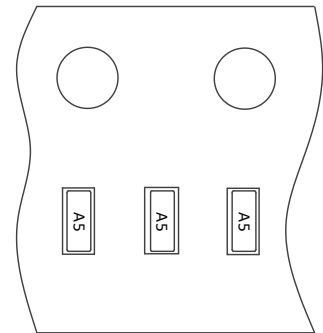
A5

Notes: Device is electrically symmetrical.

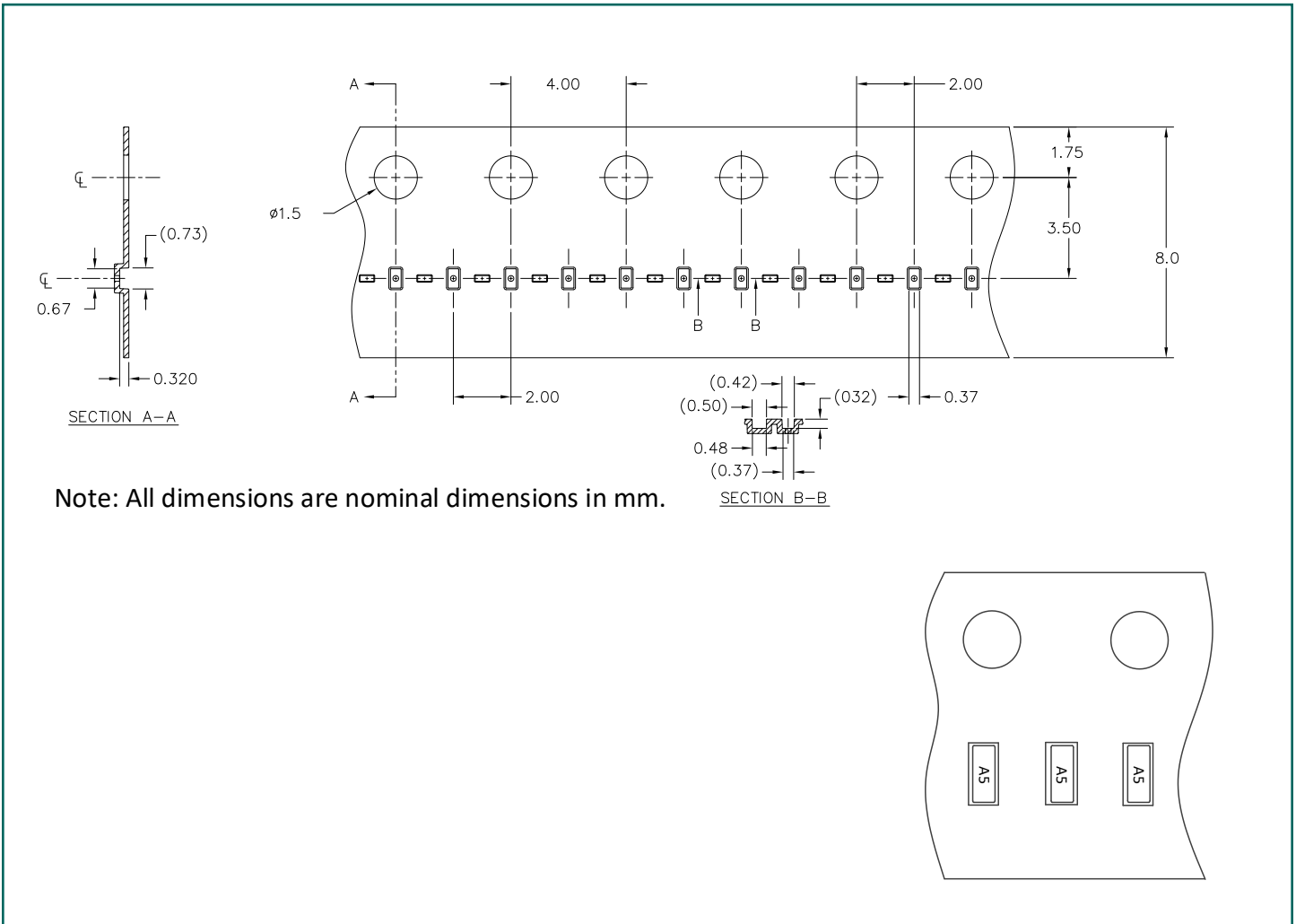
Tape and Reel Specification - Paper Tape



Note: All dimensions are nominal dimensions in mm.



Tape and Reel Specification - Plastic Tape



Ordering Information

Part Number	Tape Material	Qty per Reel	Reel Size
RClamp01511ZC.F	Paper	15000	7 Inch
RClamp01511ZC.N	Plastic	10000	7 Inch

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