16-bit Buffers / Line Drivers with 3-state Outputs

REJ03D0526-0100 Rev.1.00 Mar. 14, 2005

Description

The RD74LVC16240B has sixteen inverter drivers with three state outputs in a 48 pin package. This device is a inverting buffer and has two active low enables ($1\overline{G}$ to $4\overline{G}$). Each enable independently controls four buffers. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 1.65 \text{ V}$ to 5.5 V
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
- All outputs V_{OUT} (Max.) = 5.5 V (@V_{CC} = 0 V or output off state)
- Typical V_{OL} ground bounce < 0.8 V (@V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@V_{CC} = 3.3 V, Ta = 25°C)
- High output current $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$

 $\begin{array}{l} \pm 8 \mbox{ mA } (@V_{CC} = 2.3 \mbox{ V}) \\ \pm 12 \mbox{ mA } (@V_{CC} = 2.7 \mbox{ V}) \\ \pm 24 \mbox{ mA } (@V_{CC} = 3.0 \mbox{ V to } 5.5 \mbox{ V}) \end{array}$

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
RD74LVC16240BTEL	TSSOP-48 pin	PTSP0048KA–A (TTP–48DBV)	Т	EL (1,000 pcs/reel)

Function Table

Inp	outs	
G	A	Output Y
Н	Х	Z
L	Н	L
L	L	Н

H: High level

L: Low level

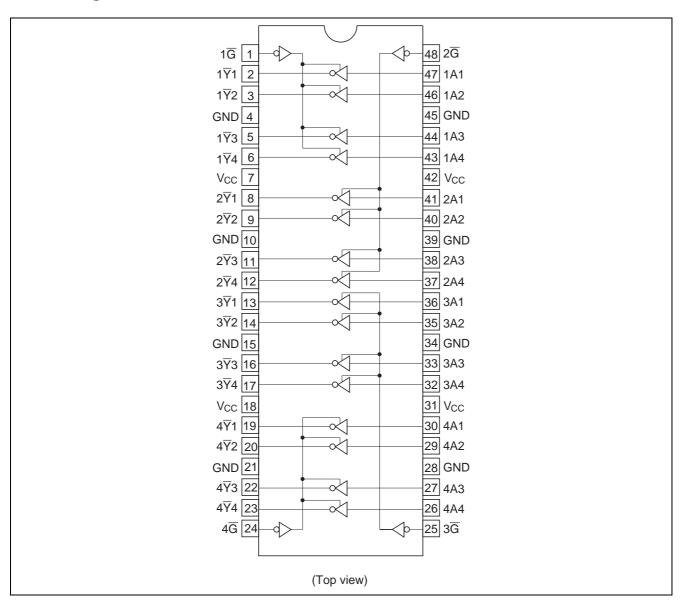
X: Immaterial

Z: High impedance



Pin Arrangement

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Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	-0.5 to 7.0	V	
Input diode current	I _{IK}	-50	mA	V ₁ = -0.5 V
Input voltage	VI	-0.5 to 7.0	V	
Output diode current	Ι _{ΟΚ}	-50	mA	V _O = -0.5 V
		50		$V_{\rm O} = V_{\rm CC} + 0.5 \rm V$
Output voltage	Vo	–0.5 to V _{CC} +0.5	V	Output "H" or "L"
		-0.5 to 7.0		Output "Z" or V _{CC} :OFF
Output current	Io	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



Recommended Operating Conditions

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Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	1.5 to 5.5	V	Data hold
		1.65 to 5.5		At operation
Input / Output voltage	VI	0 to 5.5	V	G, A
	Vo	0 to V _{CC}		Output "H" or "L"
		0 to 5.5		Output "Z" or V _{CC} : OFF
Operating temperature	Та	-40 to 85	°C	
Output current	I _{ОН}	-4	mA	V _{CC} = 1.65 V
		-8		V _{CC} = 2.3 V
		-12		V _{CC} = 2.7 V
		-24		$V_{CC} = 3.0 \text{ V} \text{ to } 5.5 \text{ V}$
	I _{OL}	4	mA	V _{CC} = 1.65 V
		8		V _{CC} = 2.3 V
		12		V _{CC} = 2.7 V
		24		$V_{CC} = 3.0 \text{ V} \text{ to } 5.5 \text{ V}$
Input rise / fall time ^{*1}	t _r , t _f	20	ns/V	V_{CC} = 1.65 V to 2.7 V
	l l	10		$V_{CC} = 3.0 \text{ V} \text{ to } 5.5 \text{ V}$

Notes: 1. This item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.



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Electrical Characteristics

			Ta = -40) to 85°C		
Item	Symbol	V _{cc} (V)	Min	Max	Unit	Test Conditions
Input voltage	VIH	1.65 to 1.95	V _{CC} ×0.65	—	V	
		2.3 to 2.7	1.7	_		
		2.7 to 3.6	2.0	_		
		4.5 to 5.5	V _{CC} ×0.7	_		
	VIL	1.65 to 1.95	_	V _{CC} ×0.35	V	-
		2.3 to 2.7	_	0.7		
		2.7 to 3.6	_	0.8		
		4.5 to 5.5	_	V _{CC} ×0.3		
Output voltage	V _{OH}	1.65 to 5.5	V _{CC} -0.2	—	V	I _{OH} = −100 μA
		1.65	1.2	_		$I_{OH} = -4 \text{ mA}$
		2.3	1.7	_		I _{OH} = -8 mA
		2.7	2.2	—		I _{OH} = -12 mA
		3.0	2.4	—		
		3.0	2.2	_		I _{OH} = -24 mA
		4.5	3.8	—		
	V _{OL}	1.65 to 5.5	_	0.2	V	I _{OL} = 100 μA
		1.65	_	0.45		I _{OL} = 4 mA
		2.3	—	0.7		I _{OL} = 8 mA
		2.7	—	0.4		I _{OL} = 12 mA
		3.0	_	0.55		I _{OL} = 24 mA
		4.5	_	0.55		
Input current	I _{IN}	0 to 5.5	_	±5.0	μA	$V_{IN} = 5.5 V \text{ or GND}$
Output leak current	I _{OFF}	0	_	±5.0	μA	$V_{IN}/V_{OUT} = 5.5 V$
Off state output current	I _{OZ}	2.7 to 5.5	—	±5.0	μA	$V_{IN} = V_{CC} \text{ or } GND$ $V_O = 5.5 \text{ V or } GND$
Quiescent supply	I _{CC}	2.7 to 3.6	_	±10	μA	V _{IN} = 3.6 V to 5.5 V
current		2.7 to 5.5	_	10	1	V _{IN} = V _{CC} or GND
	Δl _{CC}	2.7 to 3.6	_	500	μA	V_{IN} = one input at (V_{CC} -0.6)V, other inputs at V_{CC} or GND

Switching Characteristics

			Ta	= -40 to 8	85°C		From	То
Item	Symbol	V _{cc} (V)	Min	Тур	Мах	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	1.8±0.15	1.0	_	10.9	ns	А	Ϋ́
	t _{PHL}	2.5±0.2	1.0	_	7.9			
		2.7	1.0	—	6.2			
		3.3±0.3	1.5	—	5.5			
		5.0±0.5	1.0	_	4.5			
Output enable time	t _{ZH}	1.8±0.15	1.0	—	12.6	ns	G	Ϋ́
	t _{ZL}	2.5±0.2	1.0	_	9.6			
		2.7	1.0	_	7.7			
		3.3±0.3	1.5	_	7.0			
		5.0±0.5	1.0	_	6.0			
Output disable time	t _{HZ}	1.8±0.15	1.0	_	12.1	ns	G	Ϋ́
	t _{LZ}	2.5±0.2	1.0	—	7.8			
		2.7	1.0	—	7.7			
		3.3±0.3	1.5	—	7.0			
		5.0±0.5	1.0	—	6.0			
Between output pins skew*1	t _{OSLH}	1.8±0.15	—	—	—	ns		
	t _{OSHL}	2.5±0.2	_	_	_			
		2.7	—	—	—			
		3.3±0.3	—	_	1.0			
		5.0±0.5			1.0			
Input capacitance	CIN	3.3	_	4.0		pF		
Output capacitance	Co	3.3		8.0		pF		

Note: 1. This parameter is characterized but not tested.

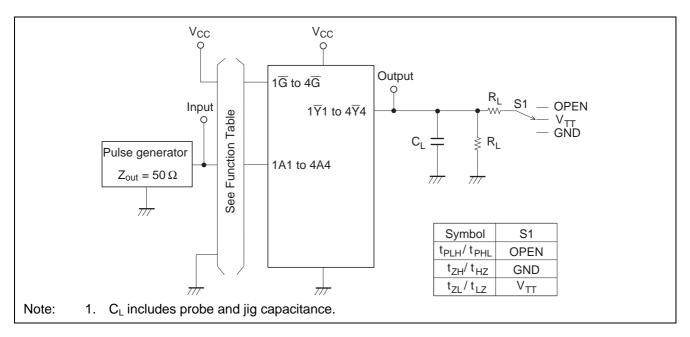
 $t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|$

Operating Characteristics

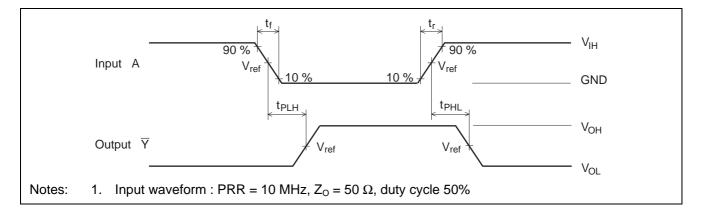
				Ta = 25°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	C _{PD}	1.8		25.0	—	pF	f = 10 MHz
Capacitance		2.5	_	25.0	_		
		3.3	_	27.0			
		5.0	_	32.0	_		

Test Circuit

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Waveforms - 1





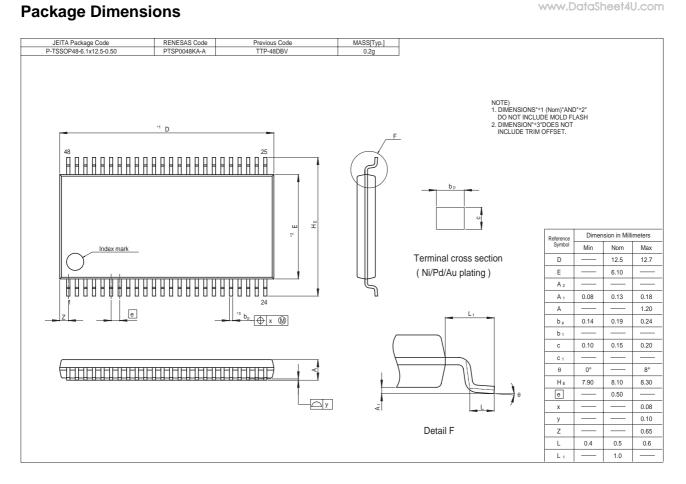
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Waveforms – 2

I	Input G 90 %				90 % V _{ref}				- V _{IH}
	10 %	6 ¥		/ 1	0 % t _{LZ}				- GND
				<		\rightarrow			$\sim 1/2V_{TT}$
١	Waveform - A	V _{ref} \					$_{\rm DL}$ + Δ V		- V _{OL}
		t _{ZH}		<	t _{HZ}	<u> </u>			
١	Waveform - B	V _{ref}	[ار مر	$_{\rm DH}$ – Δ V		- V _{OH}
		/							- ≈ GND
			INPUT	S					
		Vcc (V)	Vi	tr/tf	Vref	VTT	CL	RL	ΔV
		Vcc = 1.8±0.15 V	Vcc	≤ 2 ns	1/2 Vcc	2×Vcc	30 pF	1.0 kΩ	0.15 V
		Vcc = 2.5±0.2 V	Vcc	≤ 2 ns	1/2 Vcc	2×Vcc	30 pF	500 Ω	0.15 V
		Vcc = 2.7 V	2.7 V	≤ 2.5 ns	1.5 V	6 V	50 pF	500 Ω	0.3 V
		Vcc = 3.3±0.3 V	2.7 V	≤ 2.5 ns	1.5 V	6 V	50 pF	500 Ω	0.3 V
		Vcc = 5.0±0.5 V	Vcc	≤ 2.5 ns	1/2 Vcc	2× Vcc	50 pF	500 Ω	0.3 V



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