

**RF3377** 

#### **GENERAL PURPOSE AMPLIFIER**

# RoHS Compliant & Pb-Free Product

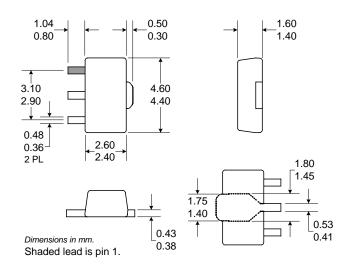
## **Typical Applications**

- Basestation Applications
- Broadband, Low-Noise Gain Blocks
- IF or RF Buffer Amplifiers

- Driver Stage for Power Amplifiers
- Final PA for Low-Power Applications
- High Reliability Applications

## **Product Description**

The RF3377 is a general purpose, low-cost RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily-cascadable  $50\Omega$  gain block. Applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to  $6000\,\text{MHz}.$  The device is self-contained with  $50\Omega$  input and output impedances and requires only two external DC-biasing elements to operate as specified.



#### **Optimum Technology Matching® Applied**

- ☐ Si BJT ☐ GaAs MESFET☐ Si Bi-CMOS☐ SiGe HBT☐ Si CMOS☐ InGaP/HBT☐ GaN HEMT☐ SiGe Bi-CMOS☐

**Functional Block Diagram** 

### Package Style: SOT89

#### **Features**

- DC to >6000MHz Operation
- Internally Matched Input and Output
- 15.5dB Small Signal Gain
- +25.5dBm Output IP3
- +13dBm Output P1dB

#### Ordering Information

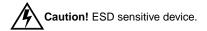
RF3377 General Purpose Amplifier RF337XPCBA-410Fully Assembled Evaluation Board

RF Micro Devices, Inc. 7628 Thorndike Road Greensboro, NC 27409, USA Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com

# **RF3377**

**Absolute Maximum Ratings** 

Parameter	Rating	Unit				
Input RF Power	+13	dBm				
Operating Ambient Temperature	-40 to +85	°C				
Storage Temperature	-60 to +150	°C				
I <sub>CC</sub>	60	mA				



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Davamatav	Specification		l locit	Condition		
Parameter	Min.	Тур.	Max.	Unit	Condition	
Overall					T=25 °C, I <sub>CC</sub> =40mA (See Note 1.)	
Frequency Range		DC to >6000		MHz		
3dB Bandwidth		4.4		GHz		
Gain	15.5	16.7		dB	Freq=500MHz	
	15.0	16.5		dB	Freq=1000MHz	
	14.5	15.5		dB	Freq=2000MHz	
		14.5		dB	Freq=3000MHz	
		14.0			Freq=4000MHz	
		13.0			Freq=6000MHz	
Noise Figure		3.0		dB	Freq=2000MHz	
Input VSWR		<2:1			In a 50Ω system, DC to 5000MHz	
Output VSWR		<1.8:1			In a $50\Omega$ system, DC to $6000MHz$	
Output IP3	24.5	25.5		dBm	Freq=1000MHz	
	+24.5	+25.5		dBm	Freq=2000MHz	
Output P1dB	+12.0	+13.5		dBm	Freq=1000MHz	
	+11.5	+13.0		dBm	Freq=2000MHz	
Reverse Isolation		19		dB	Freq=2000MHz	
Thermal					I <sub>CC</sub> =40mA, P <sub>DISS</sub> =135mW. (See Note 3.)	
Theta <sub>JC</sub>		122		°C/W	V <sub>PIN</sub> =3.38V	
Maximum Measured Junction Temperature at DC Bias Con- ditions		102		°C	T <sub>CASE</sub> =+85°C	
Mean Time To Failures		77,000		years	T <sub>CASE</sub> =+85°C	
Power Supply				-	With 22Ω bias resistor	
Device Operating Voltage		3.65	3.7	V	At pin 8 with I <sub>CC</sub> =40mA	
		4.5	4.8	V	At evaluation board connectors, I <sub>CC</sub> =40mA	
Operating Current		40	60	mA	See Note 2.	

Note 1: All specification and characterization data has been gathered on standard FR-4 evaluation boards. These evaluation boards are not optimized for frequencies above 2.5GHz. Performance above 2.5GHz may improve if a high performance PCB is used.

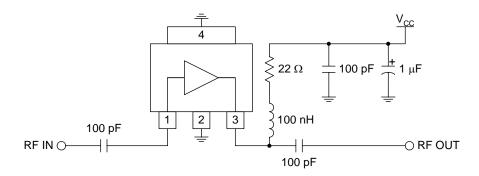
Note 2: The RF3377 must be operated at or below 60mA in order to achieve the thermal performance stated above. Operating at 40mA will ensure the best possible combination of reliability and electrical performance.

Note 3: Because of process variations from part to part, the current resulting from a fixed bias voltage will vary. As a result, caution should be used in designing fixed voltage bias circuits to ensure the worst case bias current does not exceed 60 mA over all intended operating conditions.

4-608 Rev A6 050524

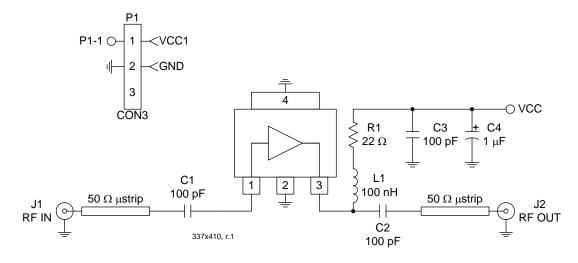
Pin	Function	Description	Interface Schematic
1	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.	
2	GND	Ground connection.	
3	RF OUT	RF output and bias pin. Biasing is accomplished with an external series resistor and choke inductor to $V_{CC}$ . The resistor is selected to set the DC current into this pin to a desired level. The resistor value is determined by the following equation: $R = \frac{(V_{SUPPLY} - V_{DEVICE})}{I_{CC}}$ Care should also be taken in the resistor selection to <b>ensure that the current into the part never exceeds 60 mA over the planned operating temperature</b> . This means that a resistor between the supply and this pin is always required, even if a supply near 3.6V is available, to provide DC feedback to prevent thermal runaway. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. The supply side of the bias network should also be well bypassed.	RF IN O
4	GND	Ground connection.	

# **Application Schematic**



## **Evaluation Board Schematic**

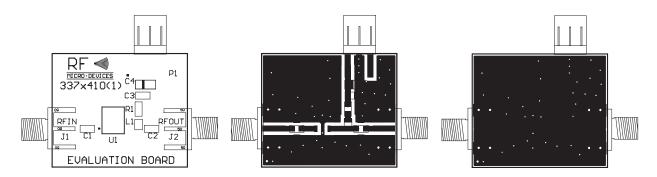
(Download Bill of Materials from www.rfmd.com.)

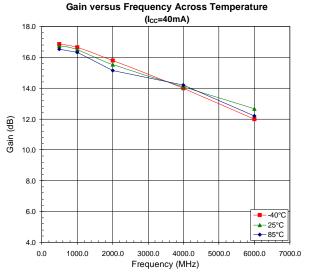


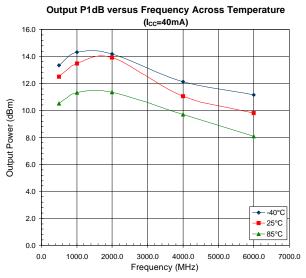
4-610 Rev A6 050524

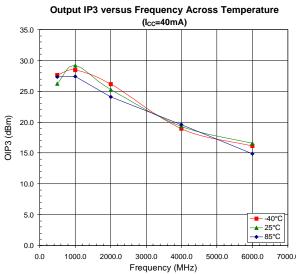
# **Evaluation Board Layout Board Size 1.195" x 1.000"**

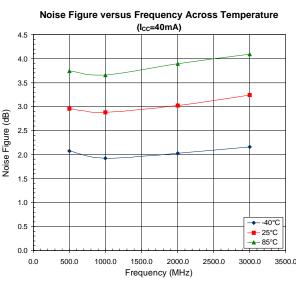
Board Thickness 0.033", Board Material FR-4

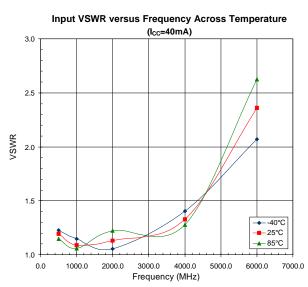


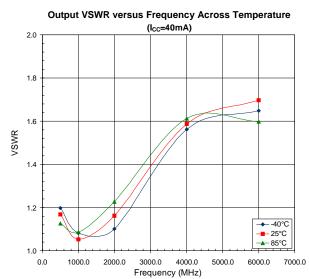




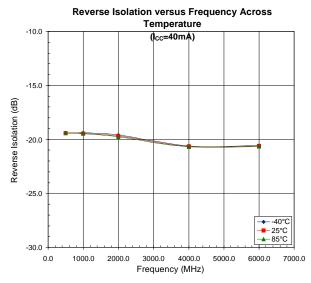


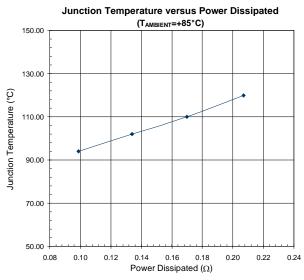


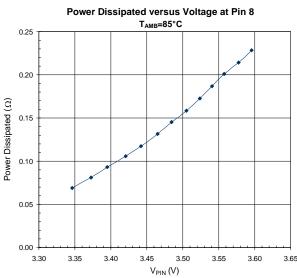


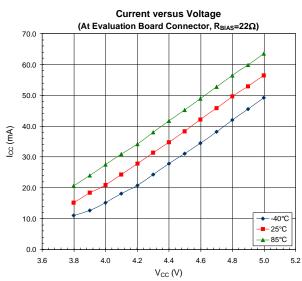


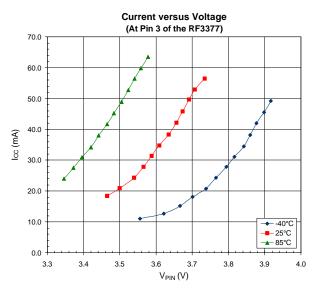
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# RF3377

4-614 Rev A6 050524