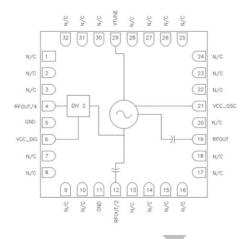


RFVC1843

9.8GHz to 11.3GHz MMIC VCO with Fo/2 and Fo/4 Outputs

RFMD's RFVC1843 is a 5V InGaP MMIC VCO with an integrated frequency divider providing additional Fo/2 and Fo/4 outputs. With an Fo frequency range of 9.8GHz to 11.3GHz its monolithic structure provides excellent temperature, shock, and vibration performance. Output power (Fo) is +7dBm and is flat across the tuning voltage range of 1.5V to 14.5V. Phase noise is typically -114dBc/Hz at 100kHz offset. The device operates from a low supply current of 275mA which can be further reduced to 215mA by disabling the divider functions if not required. The RFVC1843 is available in a low cost 5mm x 5mm surface mount plastic overmolded QFN outline.





Ordering Information

RFVC1843S2	Sample bag with 2 pieces
RFVC1843SB	Sample bag with 5 pieces
RFVC1843SQ	Bag with 25 pieces
RFVC1843SR	Bag with 100 pieces
RFVC1843TR7	7" Reel with 750 pieces
RFVC1843PCBA-410	Evaluation Board



Package: Plastic QFN, 32-pin, 5mm x 5mm x 0.85mm

Features

- Multiple Frequency Outputs
 - Fo: 9.80GHz to 11.3GHz
 - Fo/2: 4.90GHz to 5.65GHz
 Fo/4: 2.45GHz to 2.83GHz
- No External Resonator Required
- Integrated Frequency Divider
- Phase Noise: -114dBc/Hz at 100kHz Offset
- Flat Output Power Over Frequency Tuning Range 1.5V to 14.5V
 - Fo: 7dBm
 - Fo/2: 8dBm
 - Fo/4: -2dBm
- Low Power Consumption
 - 5V/275mA (Divider On)
 - 5V/215mA (Divider Off)

32-Lead 5mm x 5mm Plastic Overmolded QFN

Applications

- Point-to-Point Radio
- Point-to-Multipoint Radio
- Satellite Communications

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- Test Equipment
- Military
- Aerospace

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Absolute Maximum Ratings

Parameter	Rating	Unit
V _{CC_OSC} , V _{CC_DIG}	+5.5	V
V _{TUNE}	0 to +15	V
Junction Temperature (T _J)	135	°C
Continuous P_{DISS} (T _A = 85°C) (derate 37mW/°C above T _A = 85°C)	1.65	W
Junction to Case, Thermal Resistance $(R_{\theta(j-a)})$	30	°C/W
Storage Temperature	-65 to +150	°C
Operating Temperature	-40 to +85	°C
ESD Sensitivity (HBM)	Class 1A	





RFMD Green: RoHS compliant per EU Directive 2011/65/EU, halogen free per IEC 61249-2-21, <1000ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony solder.

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

Nominal Operating Parameters

Dourousston	Specification				Condition	
Parameter	Min	Тур	Max	Unit	Condition	
Electrical Specifications					V _{CC} = 5V, T _A = +25°C	
Operating Frequency						
Fo	9.80		11.3	GHz		
Fo/2	4.90		5.65	GHz		
Fo/4	2.45		2.83	GHz		
Output Power						
Fo		7		dBm		
Fo/2		8		dBm		
Fo/4		-2		dBm		
SSB Phase Noise						
10kHz offset at RF _{out}		-90		dBc/Hz	$V_{TUNE} = 5V$	
100kHz offset at RF _{OUT}		-114		dBc/Hz		
Tune Voltage	1.5		14.5	V		
Supply Current						
V _{cc_osc}		215		mA		
V _{CC_DIG}		60		mA		
Tune Port Leakage Current		10		μA		
Output Return Loss		5		dB		

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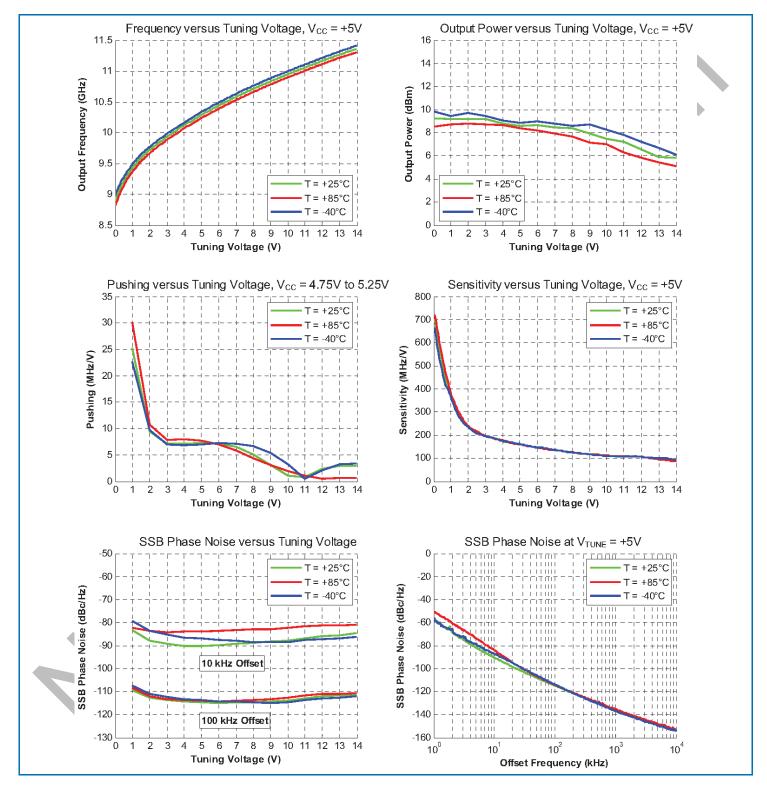
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Parameter	Specification			Unit	Condition
Farameter	Min	Тур	Max	Onit	
Electrical Specifications (continued)					V _{CC} = 5V, T _A = +25°C
Harmonics/Sub-harmonics					Measured with RF probes at package, not at SMA connections on EVB
1/2		50		dBc	
3/2		40		dBc	*. (
2 nd		10		dBc	
3 rd		18		dBc	
Pulling (into a 2.0:1 VSWR)		5		MHz pp	
Pushing		10		MHz/V	
Frequency Drift Rate		0.9		MHz/°C	

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Typical Performance

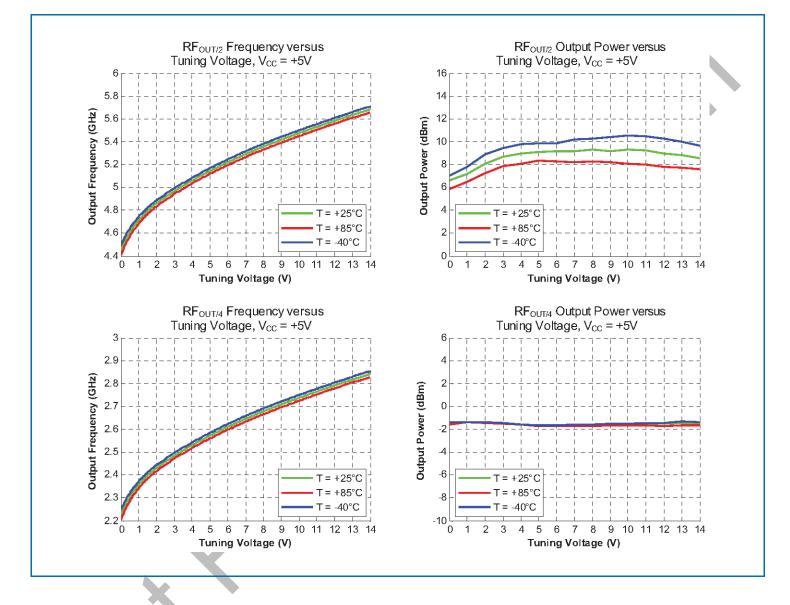


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Typical Performance (continued)



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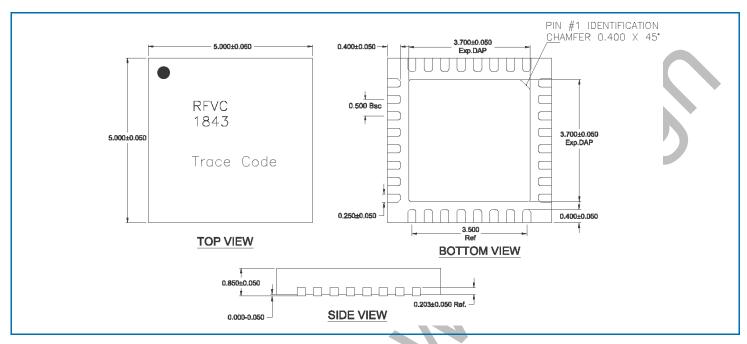
Pin Names and Descriptions

Pin	Name	Description	Interface Schematic
1-3	N/C	No internal connection.	
4	RFOUT/4	VCO RF output at Fo/4. Externally DC-blocked.	S V RFOUT/4
5	GND	Connect to PCB ground.	
6	VCC_DIG	Supply voltage input for the integrated frequency divider. Typical +5V. Ground this pin to disable digital divider and reduce current consumption by 60mA.	
7-10	N/C	No internal connection.	
11	GND	Connect to PCB ground.	See Pin 5 interface schematic
12	RFOUT/2	VCO RF output at Fo/2. Internally DC-blocked.	
13-18	N/C	No internal connection.	
19	RFOUT	VCO RF output at Fo. Internally DC-blocked.	
20	N/C	No internal connection.	
21	VCC_OSC	Supply voltage input for the VCO. Typical +5V.	

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Pin	Name	Description	Interface Schematic
22-28	N/C	No internal connection.	
29	VTUNE	VCO control voltage input.	
30-32	N/C	No internal connection.	
PKG BASE	GND	Connect to PCB ground.	See Pin 5 interface schematic

Pin Names and Descriptions (continued)

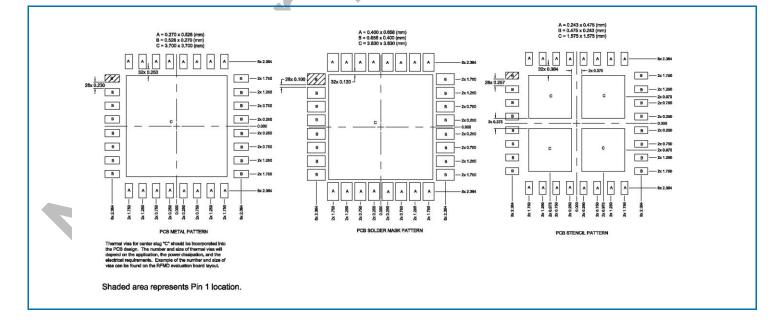


Package Drawing (all dimensions in millimeters)

Notes:

- 1. Dimensions are for reference only.
- 2. Package body material: Plastic.
- 3. Lead and paddle plating: 8µm minimum of Sn over Cu leadframe.

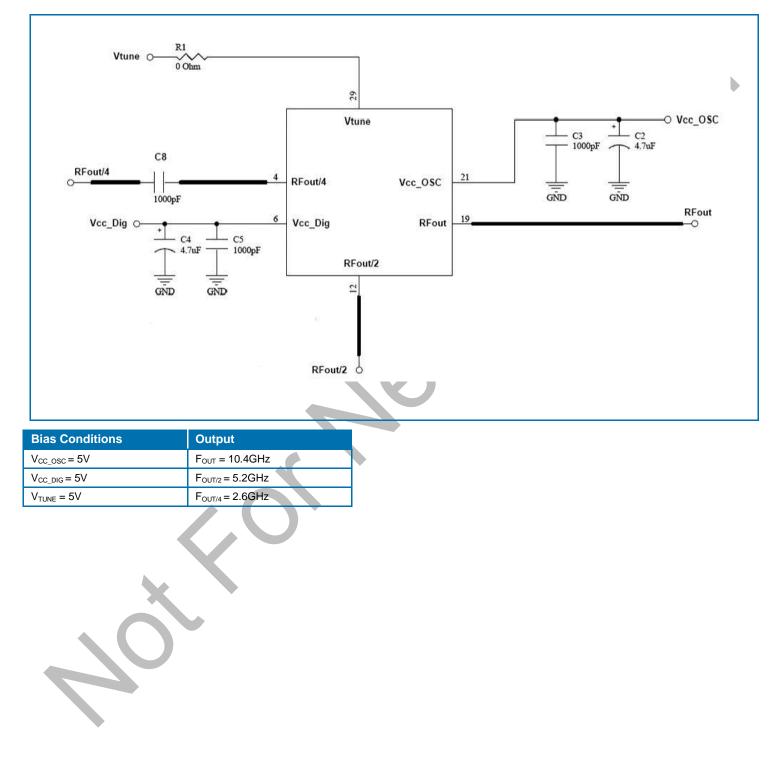
Recommended PCB Layout



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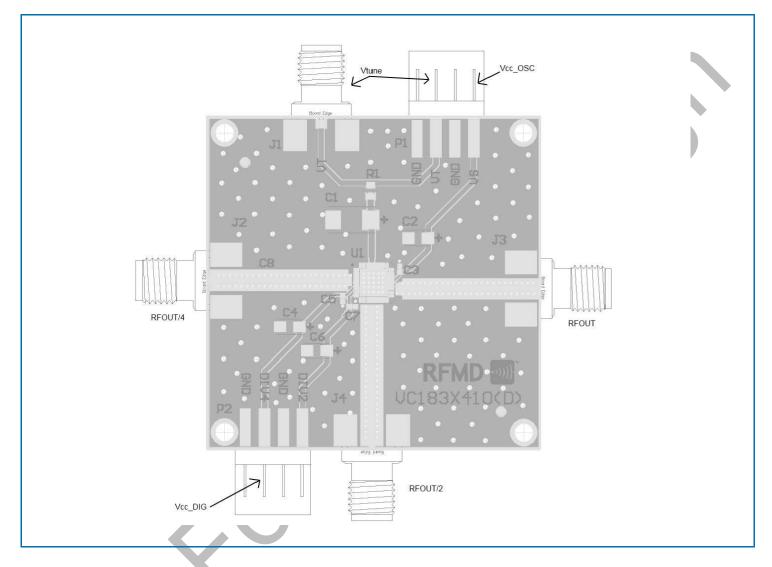
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Sample Application Circuit Schematic

Evaluation Board Layout



Evaluation Board Bill of Materials (BOM)

Item	Description
U1	RFVC1843 VCO
C3, C5, C7, C8	1000pF Capacitor, 0402 Package
C2, C4, C6	4.7µF Tantalum Capacitor
C1	68µF Tantalum Capacitor
R1	0Ω Resistor, 0603 Package
P1, P2	4-PIN DC connector
J1, J2, J3, J4	PCB mount SMA connector
РСВ	VC183x410(D)

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