OKAYA Electric America, Inc.

SPECIFICATIONS

DRAWING CODE _____

SAMPLE CODE _____

(This Code will be changed while mass production)

MASS PRODUCTION CODE **RH128128T-1x44WN-B2**

Customer Approved

Date:

Sales Sign	QC Confirmed	Checked By	Designer

Approval for Specifications Only

This specification is subject to change without notice

Approval for Specifications and Sample

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History of Version

Date	Ver.	Edi.	Description	Page	Design by
03/04/2011	01	001	NEW DRAWING	-	WUZHIJUN
05/26/2011	01	002	NEW SAMPLE	-	WUZHIJUN
06/09/2011	01	003	ADD THE TAPE OF LCM	-	WUZHIJUN

Total: 23 Pages

OKAYA

Contents

1. SPECIFICATIONS

1.1 Features

Main LCD Panel

Item	Standard Value
Display Type	128(R、G、B) * 128 Dots
LCD Type	Normally white TN, Transmissive type
Screen size(inch)	1.4'' inch
Viewing Direction	12 O'clock
Color configuration	R.G.B. vertical stripe
Backlight	White LED
Interface	8Bits data bus (8080 MCU Parallel System interface)
Other(controller / driver IC)	Sitronix: ST7715R
ROHS	THIS PRODUCT CONFORMS THE ROHS OF OKAYA

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	31.1 (W)*36.9 (L) *2.85(H)	mm

TFT LCD Panel

Item	Standard Value	Unit
Viewing Area	26.5 (W) *27.5 (L)	mm
Active Area	25.5 (W) * 26.5 (L)	mm

Note : For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
Supply Voltage	VDD	-	-0.3	+4.8	V
Supply Voltage(Logic)	VDDI	-	-0.3	+4.6	V
Supply Voltage（Digital）	VCC	-	-0.3	+1.95	V
Driver supply voltage	VGH-VGL	-	-0.3	+30	V
Logic input voltage range	VIN	-	-0.3	VDDI+0.3	V
Logic output voltage range	VO		-0.3	VDDI+0.3	
Operating Temperature	TOP	-	-20	+70	°C
Storage Temperature	TST	-	-30	+80	°C
Storage Humidity	HD	Ta ≦ 60 °C	0	90	%RH

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
System voltage	VDD	-	2.7	3.0	3.3	V
Interface operation voltage	VDDI	-	1.65	1.8	3.7	
Logic-high input voltage	VIH	-	0.7VDDI	-	VDDI	V
Logic-low input voltage	VIL	-	VSS	-	0.3VDDI	V
Logic-high output voltage	VOH	IOH=-1.0mA	0.8VDDI	-	VDDI	V
Logic-low output voltage	VOL	IOL=+1.0mA	VSS	-	0.2VDDI	V
Supply Current	IDD	VDD=3.0V, Pattern=Word*1	-	0.80	1.20	mA

Note1 : Maximum current display.

1.5 Optical Characteristics

TFT LCD Panel VDD =3.0V, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	unit	
Response time		Tr + Tf	Ta = 25°C θX, θY = 0°	-	30	45	ms	Note2
Viewing angle	Top	θY+	CR≥10	-	45	-	Deg.	Note4
	Bottom	θY-		-	15	-		
	Left	θX-		-	45	-		
	Right	θX+		-	45	-		
Contrast ratio		CR	Ta = 25°C θX , θY = 0°	150	200	-	-	Note3
Color of CIE Coordinate (With B/L)	White	X		0.23	0.28	0.33	-	Note1
		Y		0.24	0.29	0.34		
	Red	X		0.54	0.59	0.64		
		Y		0.26	0.31	0.36		
	Green	X		0.26	0.31	0.36		
		Y		0.51	0.56	0.61		
	Blue	X		0.09	0.14	0.19		
		Y		0.03	0.08	0.13		
Average Brightness Pattern=white display (With B/L)		IV	IF=20mA	240	350	-	cd/m2	Note1
Uniformity (With B/L)		△B	IF=20mA	80	-	-	%	Note1

Note1:

1：△B= B(min) / B(max) × 100%.

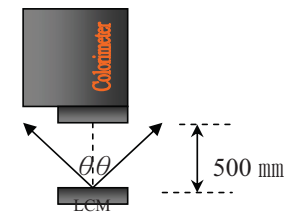
2：Measurement Condition for Optical Characteristics:

a：Environment: 25℃±5℃ / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b：Measurement Distance: 500 ± 50 ㎜, (θ= 0°).

c：Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d：The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%.

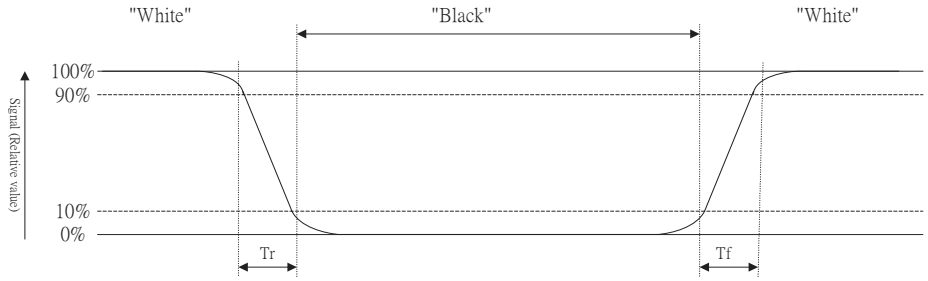


Colorimeter=BM-7 fast

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



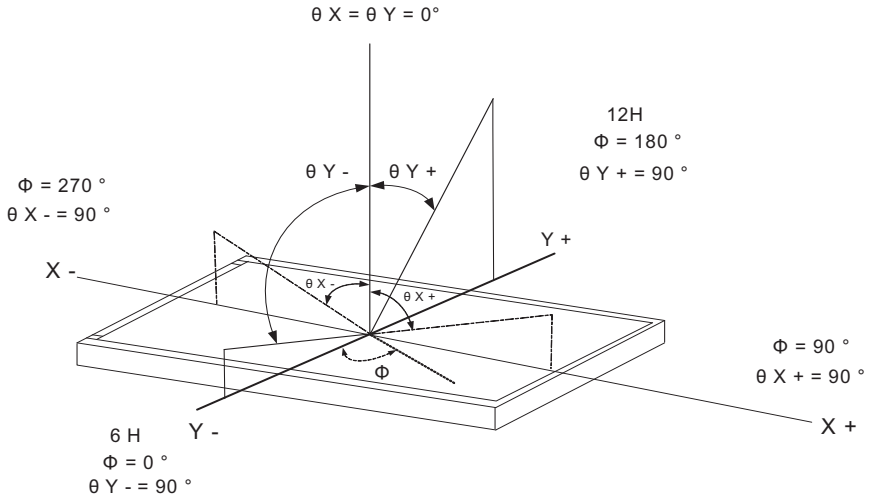
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector output when LCD is at “Black” state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25℃	-	30	mA
Reverse Voltage	VR	Ta =25℃	-	5.0	V
Power Dissipation	PD	Ta =25℃	-	90	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 20mA	-	3.2	3.6	V
Average Brightness (with LCD)	IV	IF= 20mA	1700	2100	-	cd/m^2
Color of CIE Coordinate (with LCD)	X	IF= 20mA	0.26	0.29	0.315	-
	Y		0.26	0.29	0.315	
Color	White					

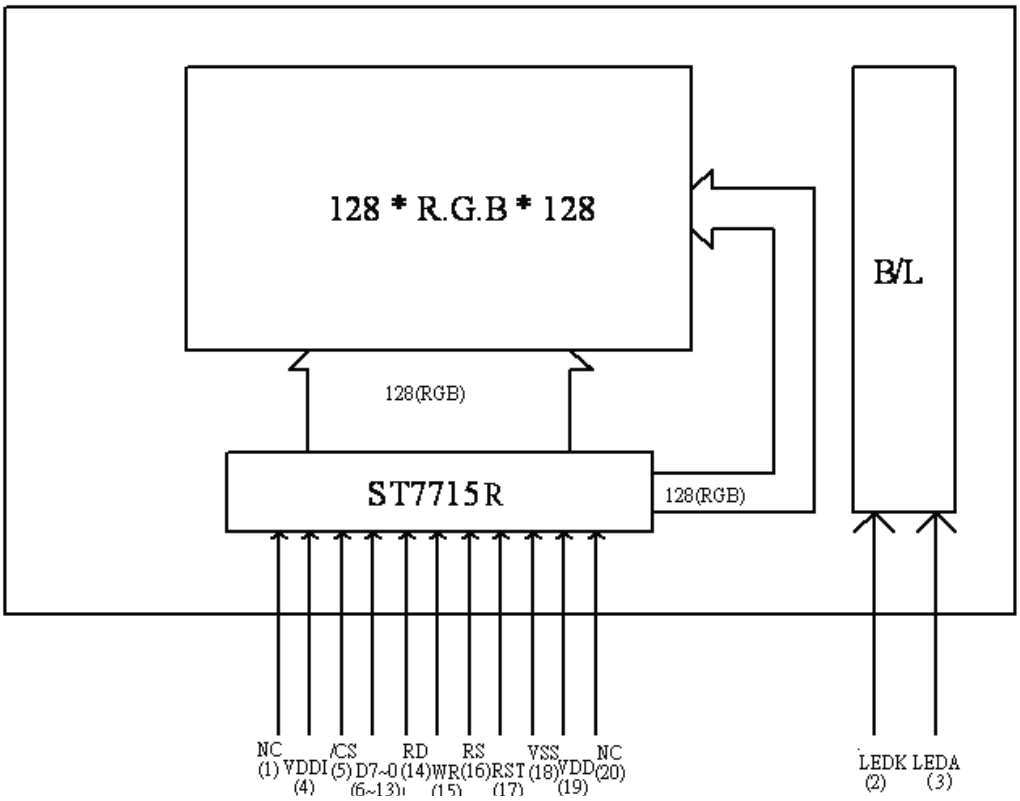
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



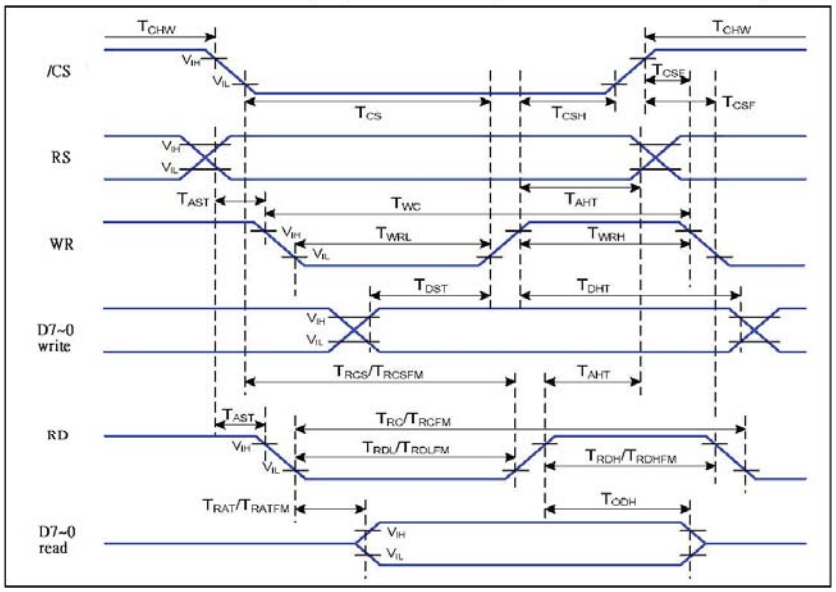
2.2 Interface Pin Description

Pin NO	Symbol	Function
1,20	NC	No Connection
2	LEDK	Negative Power Supply for LED
3	LEDA	Positive Power Supply for LED
4	VDDI	Power supply for I/O system
5	/CS	Chip select for booster circuit
6~13	D7~0	8-Bit data bus,8080series
14	RD	Read Enable input pin
15	WR	Write enable input pin
16	RS	Data/Command select input pin
17	RST	Reset control pin
18	VSS	Ground
19	VDD	Power supply for LCD

Note: VDDI is I/O system power source,the reference range is 1.65 to 3.7V,and it's usually connected with VDD for power supply.

2.3 Timing Characteristics

Parallel interface characteristics: 8-bit bus (8080 series MCU interface)



Parallel interface timing characteristics (8080 series MCU interface)

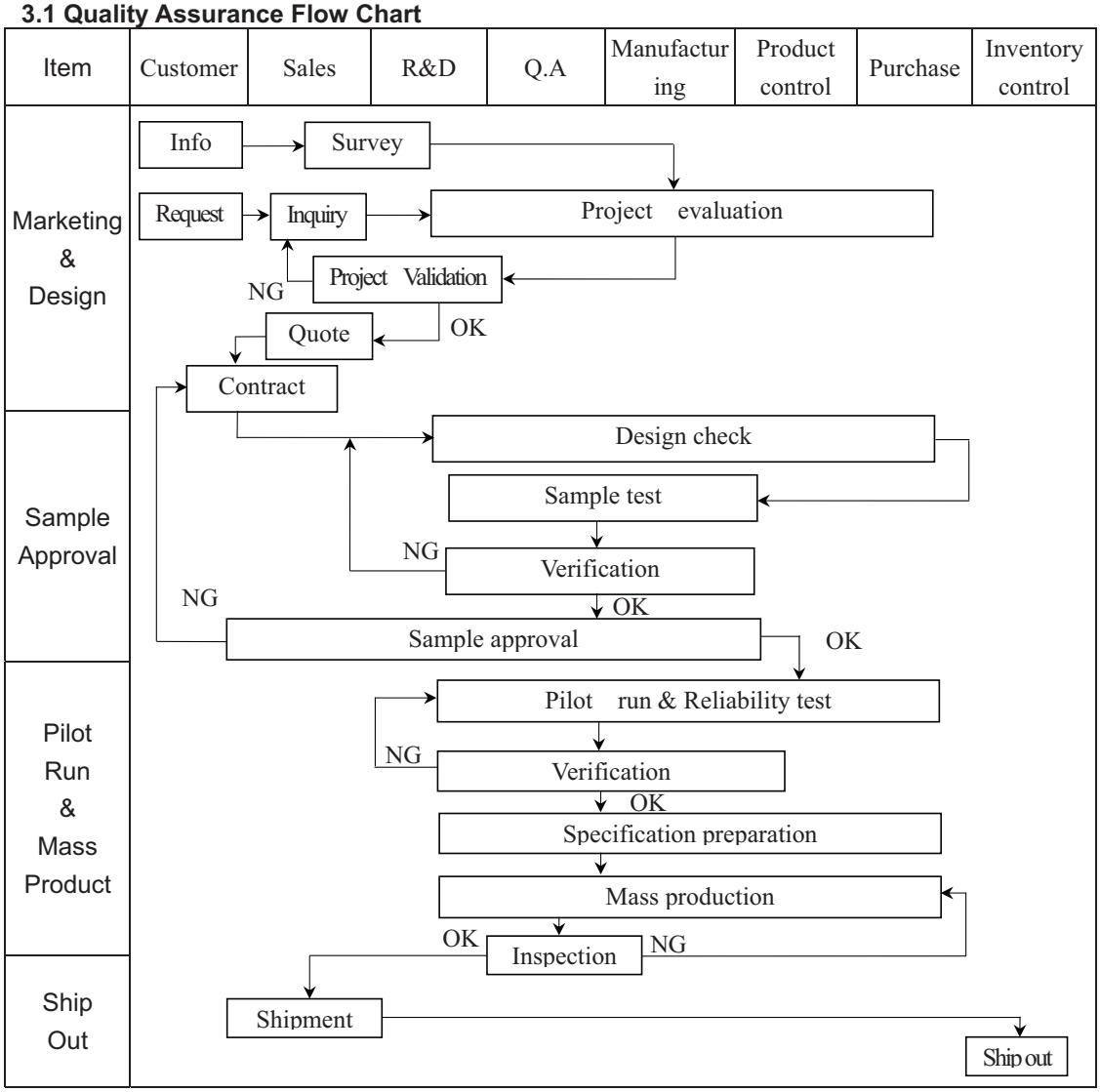
Ta=25 ℃, VDDI=1.65~3.7V, VDD=2.7~3.3V

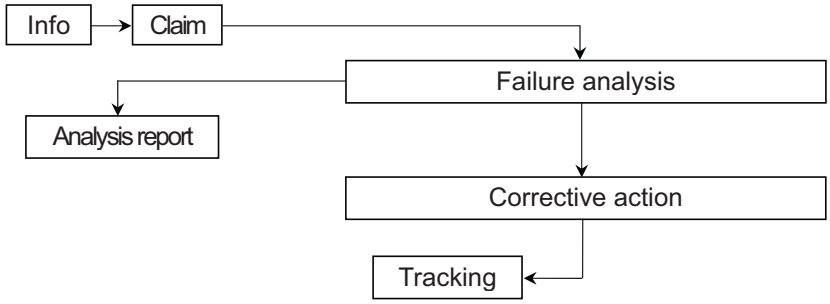
Signal	Symbol	Parameter	Min	Max	Unit	Description
RS	TAST	Address setup time	0		ns	-
	TAHT	Address hold time (Write/Read)	10		ns	
/CS	TCHW	Chip select "H" pulse width	0		ns	-
	TCS	Chip select setup time (Write)	15		ns	
	TRCS	Chip select setup time (Read ID)	45		ns	
	TRCSFM	Chip select setup time (Read FM)	355		ns	
	TCSF	Chip select wait time (Write/Read)	10		ns	
	TCSH	Chip select hold time	10		ns	
WR	TWC	Write cycle	66		ns	
	TWRH	Control pulse "H" duration	15		ns	
	TWRL	Control pulse "L" duration	15		ns	
RD[ID]	TRC	Read cycle (ID)	160		ns	When read ID data
	TRDH	Control pulse "H" duration (ID)	90		ns	
	TRDL	Control pulse "L" duration (ID)	45		ns	
RD[FM]	TRCFM	Read cycle (FM)	450		ns	When read from frame memory
	TRDHFM	Control pulse "H" duration (FM)	90		ns	
	TRDLFM	Control pulse "L" duration (FM)	355		ns	
D7~0	TDST	Data setup time	10		ns	For CL=30pF
	TDHT	Data hold time	10		ns	
	TRAT	Read access time (ID)		40	ns	
	TRATFM	Read access time (FM)		340	ns	
	TODH	Output disable time	20	80	ns	

8080 parallel Interface Characteristics

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2. Inspection Specification

◆Scope ：The document shall be applied to TFT-LCD Module for less than 3.5″ (Ver.B01).

◆Inspection Standard：MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment：Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level：Major Defect AQL： 0.4 ； Minor Defect AQL： 1.5

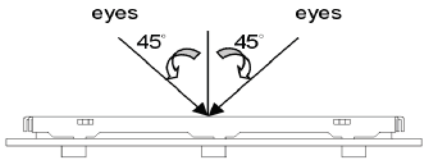
◆OUT Going Defect Level：Sampling.

◆Standard of the product appearance test：

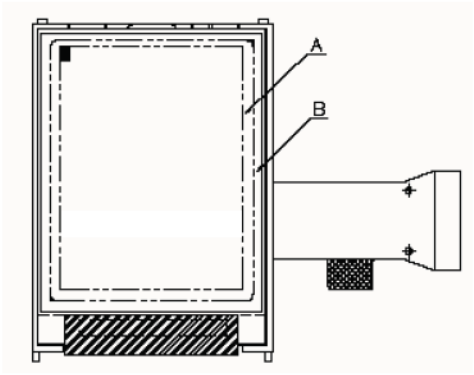
a. Manner of appearance test：

(1). The test best be under 20W×2 fluorescent light，and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection： (Unit：mm)

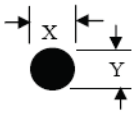
◆Specification For TFT-LCD Module Less Than 3.5″： (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="3">01</td><td rowspan="3">Product condition</td><td>1.1 The part number is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>1.2 Mixed product types.</td><td>Major</td></tr>
<tr><td>1.3 Assembled in inverse direction.</td><td>Major</td></tr>
<tr><td>02</td><td>Quantity</td><td>2.1 The quantity is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>03</td><td>Outline dimension</td><td>3.1 Product dimension and structure must conform to structure diagram.</td><td>Major</td></tr>
<tr><td rowspan="5">04</td><td rowspan="5">Electrical Testing</td><td>4.1 Missing line character and icon.</td><td>Major</td></tr>
<tr><td>4.2 No function or no display.</td><td>Major</td></tr>
<tr><td>4.3 Display malfunction.</td><td>Major</td></tr>
<tr><td>4.4 LCD viewing angle defect.</td><td>Major</td></tr>
<tr><td>4.5 Current consumption exceeds product specifications.</td><td>Major</td></tr>
<tr><td>05</td><td>Dot defect
(Bright dot、Dark dot)
On -display</td><td>
<table>
<tr><th colspan="2">Item</th><th>Acceptance (Q'ty)</th></tr>
<tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≦ 2</td></tr>
<tr><td>Dark Dot</td><td>≦ 3</td></tr>
<tr><td>Joint Dot</td><td>≦ 2</td></tr>
<tr><td>Total</td><td>≦ 3</td></tr>
</table>
5.1 Inspection pattern：full white , full black , Red , Green and blue screens.
5.2 It is defined as dot defect if defect area ＞1/2 dot.
5.3 The distance between two dot defect ≧5 mm.
</td><td>Minor</td></tr>
</table>

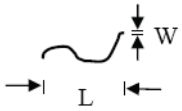
◆Specification For TFT-LCD Module Less Than 3.5″：

(Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>06</td><td>Black or white dot、scratch、contamination

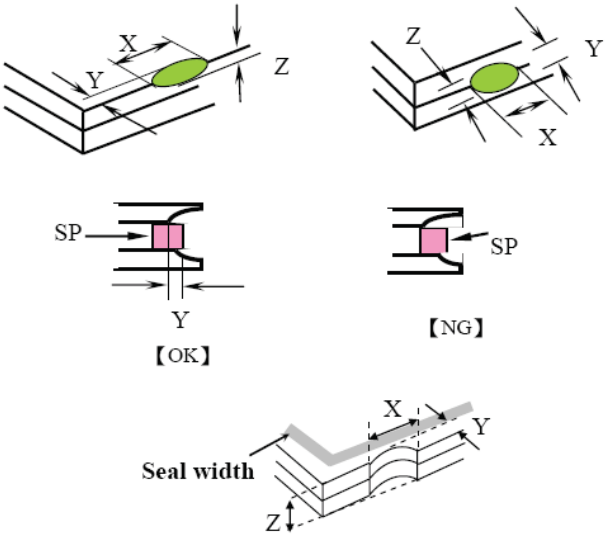
Round type
 

Φ=(x+y) / 2

Line type
 
</td><td>6.1 Round type (Non-display or display)：
<table><tr><th rowspan="2">Dimension
(diameter：Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.15</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.15 ＜ Φ ≦ 0.20</td><td>2</td></tr><tr><td>0.20 ＜ Φ ≦ 0.30</td><td>2</td></tr><tr><td>Φ ＞ 0.30</td><td>0</td></tr><tr><td>Total</td><td>3</td></tr></table>
6.2 Line type(Non-display or display)：
<table><tr><th colspan="2">Dimension</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>Length (L)</th><th>Width (W)</th><th>A area</th><th>B area</th></tr><tr><td>---</td><td>W ≦ 0.03</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>L ≦5.0</td><td>0.03 ＜W ≦ 0.05</td><td>3</td></tr><tr><td>---</td><td>W ＞0.05</td><td>As round type</td></tr><tr><td colspan="2">Total</td><td>3</td></tr></table></td><td>Minor</td></tr>
<tr><td>07</td><td>Polarizer Bubble</td><td><table><tr><th rowspan="2">Dimension
(diameter：Φ)</th><th colspan="2">Acceptance (Q'ty)</th></tr><tr><th>A area</th><th>B area</th></tr><tr><td>Φ ≦ 0.20</td><td>Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.20 ＜ Φ ≦ 0.50</td><td>3</td></tr><tr><td>Φ ＞ 0.50</td><td>0</td></tr><tr><td>Total</td><td>3</td></tr></table></td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module Less Than 3.5″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>08</td><td>The crack of glass</td><td>
Symbols :
X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

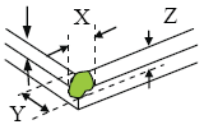
8.1 General glass chip :
8.1.1 Chip on panel surface and crack between panels:


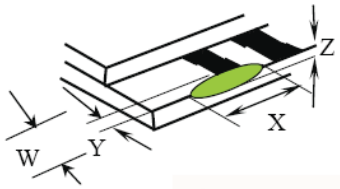
<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ a</td><td>Crack can’t enter viewing area</td><td>≦1/2 t</td></tr>
<tr><td>≦ a</td><td>Crack can’t exceed the half of SP width.</td><td>1/2 t ＜ Z ≦2 t</td></tr>
</table>
</td><td>Minor</td></tr>
</table>

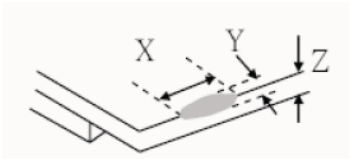
◆Specification For TFT-LCD Module Less Than 3.5″ : (Ver.B01)

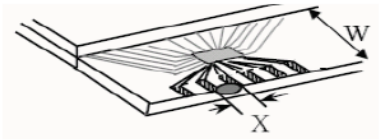
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>08</td><td>The crack of glass</td><td>
Symbols :

X : The length of crack　　Y : The width of crack.
Z : The thickness of crack　　W : terminal length
t : The thickness of glass　　a : LCD side length

8.1.2 Corner crack :


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can't enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can't exceed the half of SP width.</td><td>1/2 t < Z ≦ 2 t</td></tr>
</table>
8.2 Protrusion over terminal :
8.2.1 Chip on electrode pad :




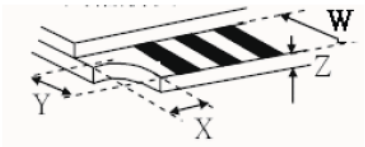


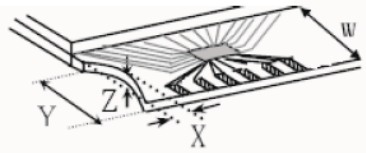
<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td>≦ a</td><td>≦ W</td><td>≦ 1/2 t</td></tr>
</table>
</td><td>Minor</td></tr>
</table>

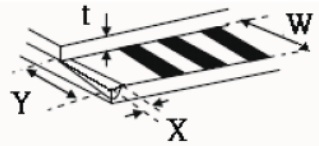
◆Specification For TFT-LCD Module Less Than 3.5″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>08</td><td>The crack of glass</td><td>
Symbols :

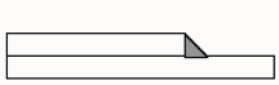
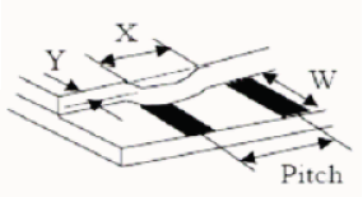
X : The length of crack Y : The width of crack.
Z : The thickness of crack W : terminal length
t : The thickness of glass a : LCD side length

8.2.2 Non-conductive portion :






<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ 1/3 a</td><td>≦W</td><td>≦t</td></tr>
</table>
⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.

8.2.3 Glass remain :



<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ a</td><td>≦ 1/3 W</td><td>≦t</td></tr>
</table>
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module Less Than 3.5″ : (Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type、quantity、dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC .	Major
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≦1.5 mm.	Minor

4. RELIABILITY TEST

4.1 Reliability Test Condition (Ver.B01)

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature Storage Test</td><td>Keep in +80℃ ±2℃ 96 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>2</td><td>Low Temperature Storage Test</td><td>Keep in -30℃ ±2℃ 96 hrs
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td>3</td><td>High Temperature / High Humidity Storage Test</td><td>Keep in +60 ℃ / 90% R.H duration for 96 hrs
Surrounding temperature, then storage at normal condition 4hrs.
(Excluding the polarizer)</td></tr>
<tr><td>4</td><td>Temperature Cycling Storage Test</td><td>-30℃ → +25℃ → +80℃ → +25℃
(30mins) (5mins) (30mins) (5mins)
10 Cycle
Surrounding temperature, then storage at normal condition 4hrs.</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 2 KV with 5 times
Discharge for each polarity +/-</td><td>Contact Discharge:
Apply 250 V with 5 times
discharge for each polarity +/-</td></tr>
<tr><td colspan="2">1. Temperature ambiance : 15℃～35℃
2. Humidity relative : 30%～60%
3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%
4. Discharge Resistance(Rd) : 330Ω±10%
5. Discharge, mode of operation :
Single Discharge (time between successive discharges at least 1 sec)
(Tolerance if the output voltage indication : ±5%)</td></tr>
<tr><td>6</td><td>Vibration Test (Packaged)</td><td colspan="2">1. Sine wave 10～55 Hz frequency (1 min/sweep)
2. The amplitude of vibration :1.5 mm
3. Each direction (X、Y、Z) duration for 2 Hrs</td></tr>
<tr><td>7</td><td>Drop Test (Packaged)</td><td colspan="2"><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop Direction :※1 corner / 3 edges / 6 sides each 1time</td></tr>
</table>

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320 ± 10°C and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

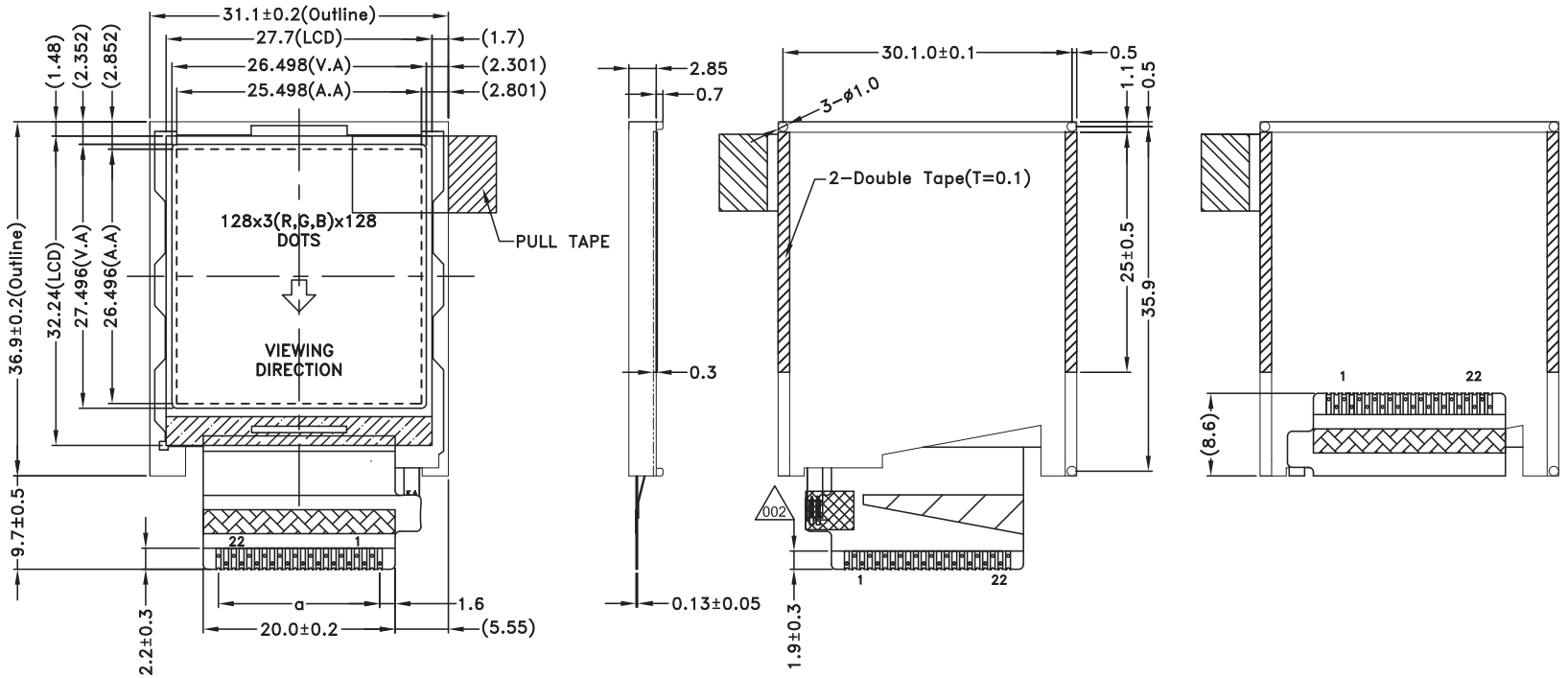
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



NOTE:

1.DRIVING ELEMENT : 65K COLOR a-Si TFT
2.THE TOLERANCE UNLESS CLASSIFIED±0.2mm
3.DRIVER IC:ST7715R
4.a:P0.8X21=16.8±0.1
5.Top: −20~70°C Tst:−30~80°C

REV	REV BY	REVISER	DATE
007			
006			
005			
004			
003			
002	ADD TAPE	Terry	2011/06/09
001	NEW DRAWING	Terry	2011/03/07

PART NO: RH128128T-1x44WN-B2

DRAWING NAME: JLMD-PH128128T-040-L02Q

TITLE: LCD MODULE DRAWING

Design	Terry	(3)	Surface		
Check	Zhou	Unit	MM	Material	
Approve	Ryan	Scale	3:5	Thickness	
		Page	1/1	Quantity	

公差精度等級 / 最小長度 (mm)	精級
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

PT-A-054-01

Ver.001	Documents NO.	JPKG-PH128128T-040-L02Q

LCM包裝規格書
LCM Packaging Specifications (For Tray)

Approve	Check	Contact
Ryan	Eddy	Terry

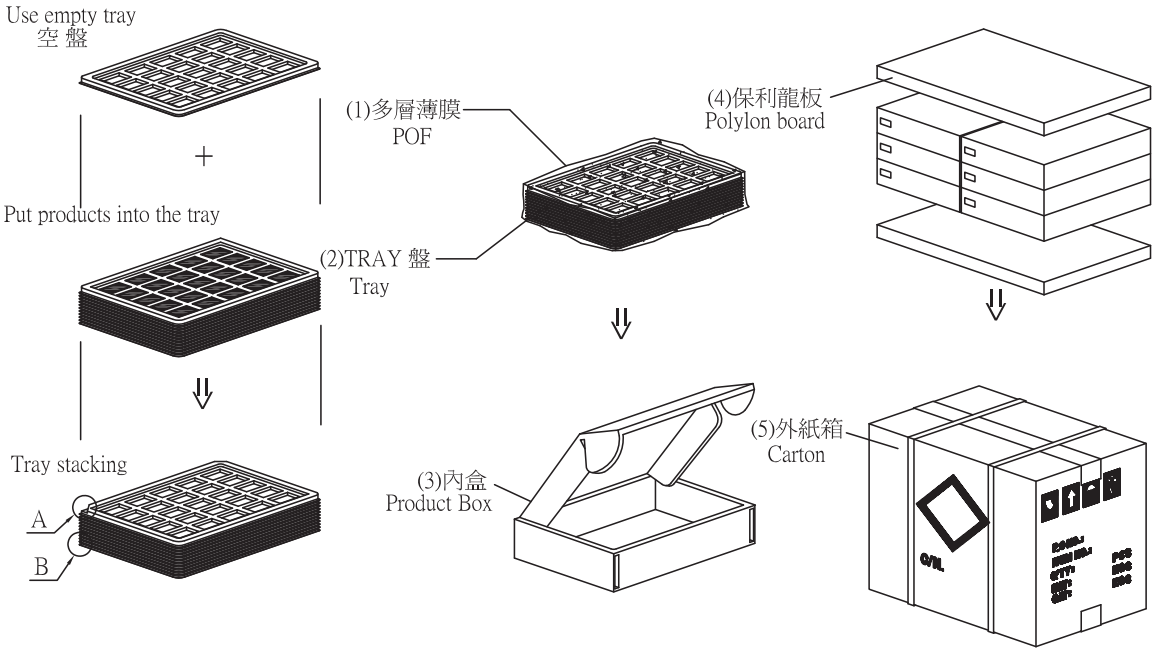
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	RH128128T-1x44WN-B2	31.1 X 36.9	0.0045	1440	6.48
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	——	6	——
3	TRAY 盤 (2)Tray	TY00000000068	352 X 260 X 10.8	0.096	54	5.184
4	內盒(3)Product Box	BX36627063ABBA	393 X 274 X 68	0.2692	6	1.6152
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.4208	1	1.4208
7						
8						
9						

2.一 整箱總重量 (Total LCD Weight in carton) : 14.76 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

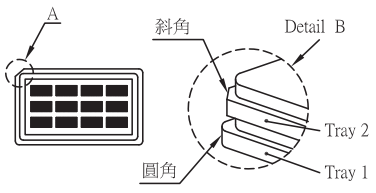
(1)LCM quantity per box : no per tray	30	x no of tray	8	=	240
(2)Total LCM quantity in carton : quantity per box	240	x no of boxes	6	=	1440



特 記 事 項 (REMARK)

1. Label Specifications :

MODEL:
LOT NO:
QUANTITY:
CHECK:



2.TRAY盤相疊時,需旋轉180度,請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.