Standard Products

Datasheet

RadHard-by-Design
RHD5980 Octal Bus Transceiver
Bidirectional Voltage Level Shifter

www.aeroflex.com/RHDseries

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FEATURES

- Bidirectional Voltage translator with two separate supply rails.
- Radiation performance
  - Total dose: >1Mrad(Si); Dose rate = 50 - 300 rads(Si)/s
  - ELDRS Immune
  - SEL Immune >100 MeV-cm^2/mg
  - Neutron Displacement Damage >10^{14} neutrons/cm^2
- Full military temperature range
- Designed for aerospace and high reliability space applications
- Packaging – Hermetic ceramic SOIC
  - 24-pin, .614"L x .299"W x .120"Ht
  - Weight - 2.0 grams max

- **Aeroflex Plainview's Radiation Hardness Assurance Plan is DLA Certified to MIL-PRF-38534, Appendix G.**

GENERAL DESCRIPTION

Aeroflex's RHD5980 is a radiation hardened, Octal Level Shifter in a 24-pin SOIC package. The RHD5980 design uses specific circuit topology and layout methods to mitigate total ionizing dose effects and single event latchup. These characteristics make the RHD5980 especially suited for the harsh environment encountered in Deep Space missions. It is guaranteed operational from -55°C to +125°C. Available screened in accordance with MIL-PRF-38534 Class K, the RHD5980 is ideal for demanding military and space applications.

ORGANIZATION AND APPLICATION

The RHD5980 Octal Level Shifter is a radiation hard replacement for the industry standard Bidirectional Voltage Translators. It is capable of level shifting from the A-to-B or B-to-A input ports for nominal logic voltages on either port of 5.0 or 3.3 volts.

The RHD5980 can level shift from 5.0V to 3.3V or 3.3V to 5.0V, and also buffer from 5.0V to 5.0V or 3.3V to 3.3V. Ports A and B can be inputs or outputs depending on the value of DIR_AB_H.

Control inputs are the standard tri-state enable (OE_L active low) and direction control DIR_AB_H where a HIGH logic steers data from A-to-B and active LOW steers the data from B-to-A.

The control inputs are powered from V_{CCA} and accept inputs at the A bus logic levels (either 3.3V or 5.0V). All delay parameters are less than 30nS over full -55°C to +125°C military temperature range and logic levels. All bus and control inputs have Schmitt trigger buffers to implement low-to-high transition at approximately 60% of the corresponding logic supply and high-to-low transition at approximately 40% providing considerable noise immunity for slow input signals

The devices will not latch with SEU events to above 100 MeV-cm^2/mg. Total dose degradation is minimal to above 1Mrad(Si). Displacement damage environments to neutron fluence equivalents in the mid 10^{14} neutrons per cm^2 range are readily tolerated. There is no sensitivity to low-dose rate (ELDRS) effects. SEU effects are application dependent.

ABSOLUTE MAXIMUM RATINGS

Parameter	Range	Units
Case Operating Temperature Range	-55 to +125	°C
Storage Temperature Range	-65 to +150	°C
Junction Temperature	+150	°C
Supply Voltage +VCCA, +VCCB	+6.0	V
Input Voltage	VCC +0.4 GND -0.4	V V
Lead Temperature (soldering, 10 seconds)	300	°C
ESD Rating (MIL-STD-883, Method 3015, Class 2)	2,000 - 3,999	V
Power @ 25°C	250	mW
Thermal Resistance, Junction-to-Case, ΘJC	5	°C/W

NOTICE: Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress rating only; functional operation beyond the “Operation Conditions” is not recommended and extended exposure beyond the “Operation Conditions” may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Typical	Units
+VCCA, +VCCB	Power Supply Voltage	3.3 to 5.0	V

ELECTRICAL PERFORMANCE CHARACTERISTICS

(TC = -55°C TO +125°C, VCCA = 5.5 V, VCCB = 3.6 V -- UNLESS OTHERWISE SPECIFIED)

Parameter	Symbol	Conditions		Min	Max	Units
PORT A						
Quiescent Supply Current	ICCA	VIN = 5.5 V or GND, No Load			20	uA
Quiescent Supply Current Delta	ΔICCA	One input at 3.4 V, Other inputs at 5.5 V or GND			1.5	mA
High Level Output Voltage	VOH	IOH = -100 uA	VCCA = 4.5 V	4.3		V
			VCCA = 5.5 V	5.3		
		IOH = -12 mA	VCCA = 4.5 V	3.7		
			VCCA = 5.5 V	4.7		
Low Level Output Voltage	VOL	IOL = 100 uA	VCCA = 4.5 V		0.2	V
			VCCA = 5.5 V		0.2	
		IOL = 12 mA	VCCA = 4.5 V		0.55	
			VCCA = 5.5 V		0.55	
Three-state I/O Leakage Current High 3/	IIOH	VIN = 5.5 V		-500	500	nA
Three-state I/O Input Leakage Current Low 3/	IIOL	VIN = GND		-500	500	nA
Input Capacitance 2/	CIN	Control inputs, VIN = VCCA = open or GND		5 TYP		pF
Input/Output Capacitance 2/	CIO	VO = 5.0 V or GND		11 TYP		pF

ELECTRICAL PERFORMANCE CHARACTERISTICS (Cont.)

(T_C = -55°C TO +125°C, V_{CCA} = 5.5 V, V_{CCB} = 3.6 V -- UNLESS OTHERWISE SPECIFIED)

Parameter	Symbol	Conditions		Min	Max	Units
PORT B						
Quiescent Supply Current	I_{CCB}	V_{IN} = 3.6 V or GND, No Load			15	uA
Quiescent Supply Current Delta	ΔI_{CCB}	One input at 2.7 V to 3.6 V - 0.6 V, Other inputs at 2.7 V to 3.6 V or GND			50	uA
High Level Output Voltage	V_{OH}	I_{OH} = -100 uA	V_{CCB} = 2.7 V to 3.6 V	V_{CCB} -0.2		V
		I_{OH} = -12 mA	V_{CCB} = 2.7 V	2.2		
			V_{CCB} = 3.0 V	2.4		
Low Level Output Voltage	V_{OL}	I_{OH} = 100 uA	V_{CCB} = 2.7 V to 3.6 V		0.2	V
		I_{OH} = 12 mA	V_{CCB} = 2.7 V		0.55	
			V_{CCB} = 3.0 V		0.55	
Three-state I/O Leakage Current High 3/	I_{IOH}	V_{IN} = 3.6 V		-500	500	nA
Three-state I/O Input Leakage Current Low 3/	I_{IOL}	V_{IN} = GND		-500	500	nA
Input/Output Capacitance 2/	C_{IO}	V_O = 5.0 V or GND		11 TYP		pF
Switching						
Pro[agation Delay Time A to B	t_{PHL}	V_{CCA} = 4.5 V to 5.5 V, V_{CCB} = 2.7 V to 3.6 V, C_L = 50pF		1	20	ns
	t_{PLH}			1	20	ns
Propagation Delay Time B to A	t_{PHL}			1	20	ns
	t_{PLH}			1	20	ns
Propagation Delay Time, Output Enabled OE_L to A	t_{PZL}			1	30	ns
	t_{PZH}			1	30	ns
Propagation Delay Time, Output Enabled OE_L to B	t_{PZL}			1	30	ns
	t_{PZH}			1	30	ns
Propagation Delay Time, Output Disabled OE_L to A	t_{PLZ}			1	30	ns
	t_{PHZ}			1	30	ns
Propagation Delay Time, Output Disabled OE_L to B	t_{PLZ}			1	30	ns
	t_{PHZ}			1	30	ns

Notes:
1/ Specification derated to reflect Total Dose exposure to 1 Mrad(Si) @ 25°C.
2/ Not tested. Shall be guaranteed by design, characterization or correlation to other test parameters.
3/ These parameters for Tc = -55°C are guaranteed by design, characterization, or correlation to other test parameters.

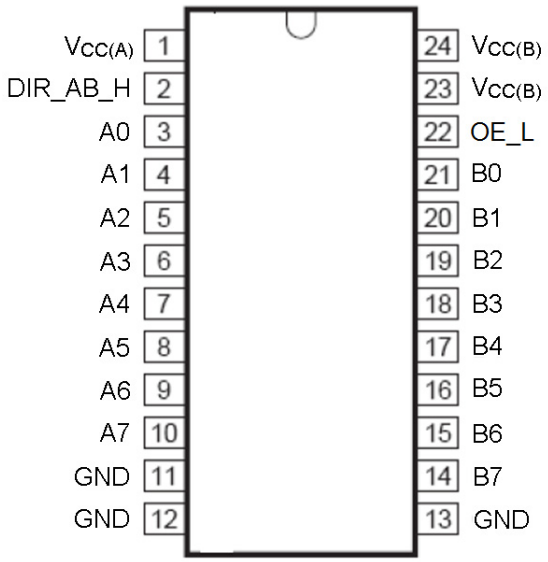

FIGURE 1: PACKAGE PIN-OUT

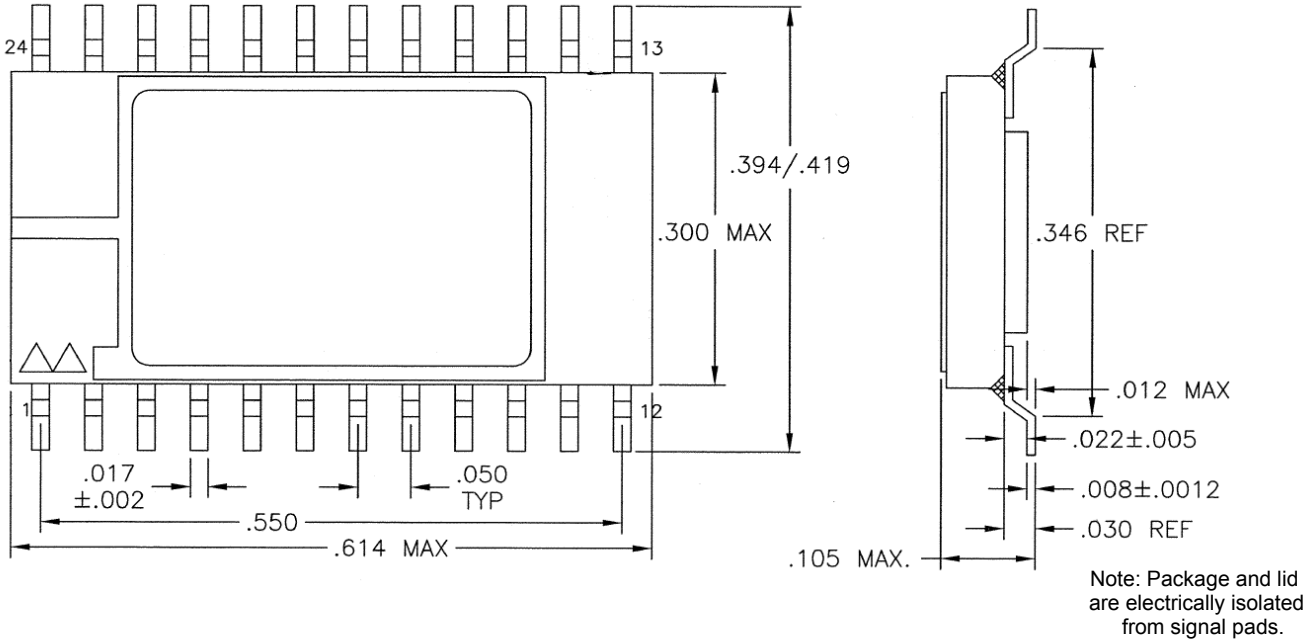

FIGURE 2: PACKAGE OUTLINE

ORDERING INFORMATION

Model	DLA SMD #	Screening	Package
RHD5980-7	-	Commercial Flow, +25°C testing only	24-pin SOIC Package
RHD5980-201-1S	5962-1222601KXC	In accordance with DLA SMD	
RHD5980-201-2S	5962-1222601KXA		
RHD5980-901-1S	5962H1222601KXC	In accordance with DLA Certified RHA Program Plan to RHA Level "H", 1Mrad(Si)	
RHD5980-901-2S	5962H1222601KXA		

EXPORT CONTROL:

This product is controlled for export under the International Traffic in Arms Regulations (ITAR). A license from the U.S. Government is required prior to the export of this product from the United States.

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Datasheet Definitions:

Advanced	*Product in Development*
Preliminary	*Shipping Non-Flight Prototypes*
Datasheet	*Shipping QML and Reduced HiRel*

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