

RJK0215DPA

Silicon N Channel Power MOS FET with Schottky Barrier Diode
High Speed Power Switching

R07DS0207EJ0110
Rev.1.10
Sep 05, 2011

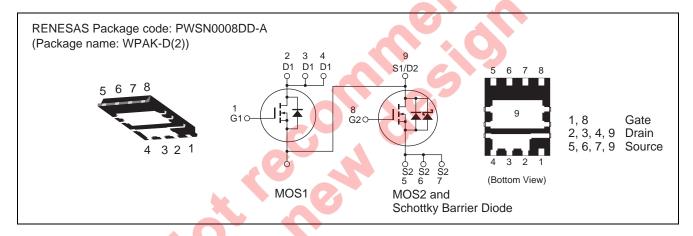
Applications

DC-DC conversion for PC and Server.

Features

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting
- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

		Rat	ings		
Item	Symbol	MOS1	MOS2	Unit	
Drain to source voltage	V _{DSS}	25	25	V	
Gate to source voltage	V _{GSS}	±20	±20	V	
Drain current	I _D	15	40	А	
Drain peak current	I _{D(pulse)} Note1	60	160	А	
Reverse drain current	I _{DR}	15	40	А	
Avalanche current	I _{AP} Note 2	5	14	А	
Avalanche energy	E _{AR} Note 2	3.1	24.5	mJ	
Channel dissipation	Pch Note3	10	25	W	
Channel temperature	Tch	150	150	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. Tc=25°C

Electrical Characteristics

• MOS1

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	25	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I_{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 25 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	7.6	9.2	mΩ	$I_D = 7.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	10.5	13.7	mΩ	$I_D = 7.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	30	_	S	$I_D = 7.5 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	810	1130	pF	V _{DS} = 10 V
Output capacitance	Coss	_	130	_	рF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	74	_	рF	f = 1MHz
Gate Resistance	Rg	_	1.2	2.4	Ω	
Total gate charge	Qg	_	6.2	_	nC	V _{DD} = 10 V
Gate to source charge	Qgs	_	2.8	_	nC	V _{GS} = 4.5 V
Gate to drain charge	Qgd	_	1.9	7	nC	I _D = 15 A
Turn-on delay time	t _{d(on)}	_	7.3		ns	$V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$
Rise time	t _r	_	5.3	(4)	ns	V _{DD} ≈ 10 V
Turn-off delay time	t _{d(off)}	_	33.9		ns	$R_L = 1.33 \Omega$
Fall time	t _f	_	5.4		ns	$R_g = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}		0.84	1.10	V	IF = 15 A, V _{GS} = 0 Note4
Body-drain diode reverse	t _{rr}	=	20		ns	IF =15 A, V _{GS} = 0
recovery time						di _F / dt = 100 A/μs
recovery time Notes: 4. Pulse test						



• MOS2

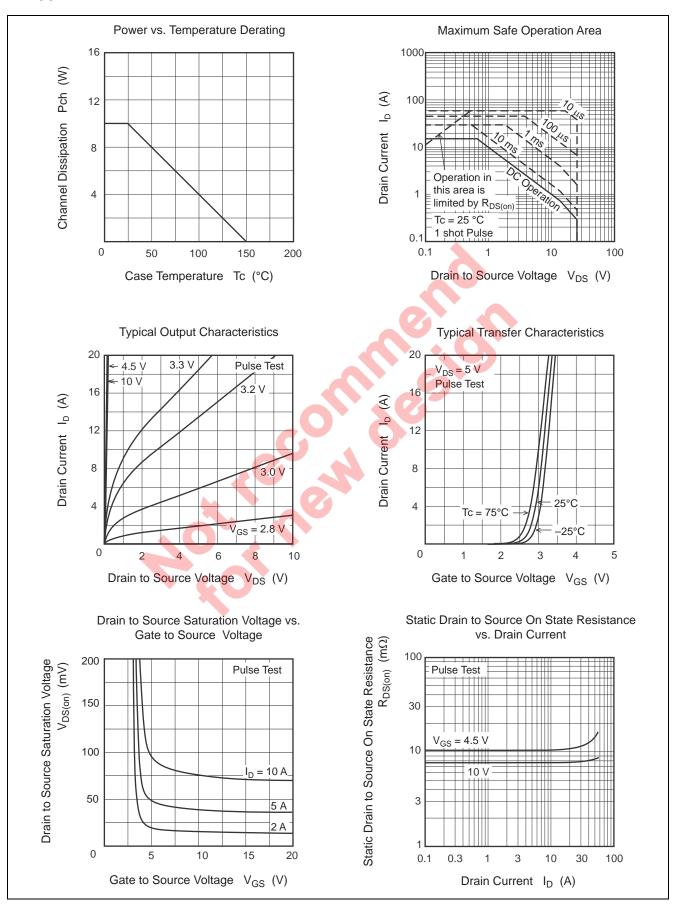
 $(Ta = 25^{\circ}C)$

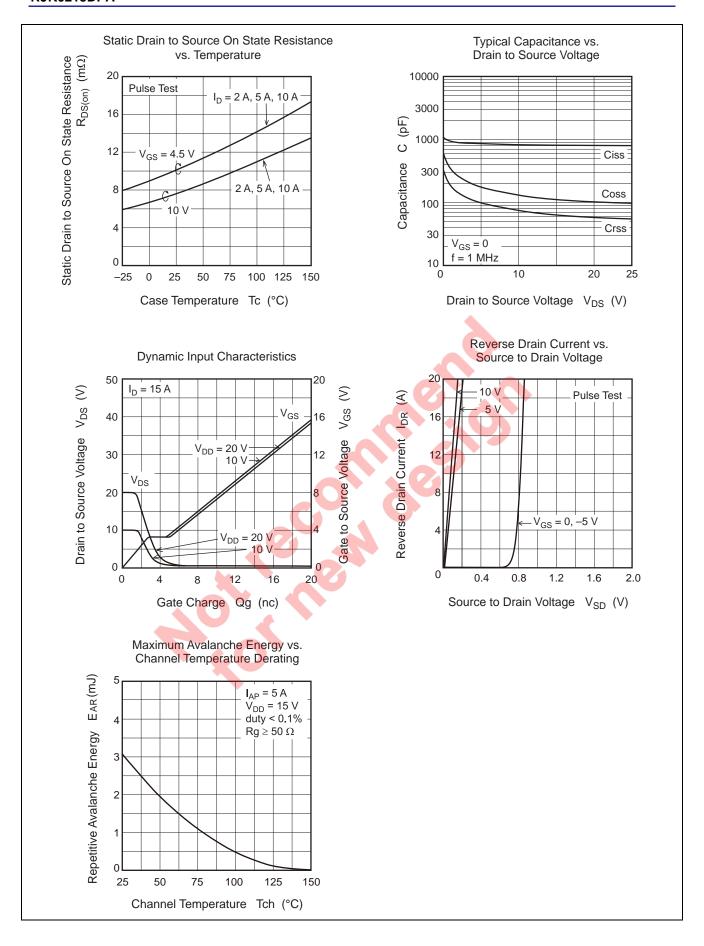
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	25	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	mA	$V_{DS} = 25 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	2.5	3.0	mΩ	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	_	3.7	4.8	mΩ	$I_D = 20 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	77	_	S	$I_D = 20 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	2500	3500	pF	V _{DS} = 10 V
Output capacitance	Coss	_	460	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	260	_	pF	f = 1MHz
Gate Resistance	Rg	_	1.9	3.8	Ω	
Total gate charge	Qg	_	17.5	_	nC	V _{DD} = 10 V
Gate to source charge	Qgs	_	7.6	_	nC	V _{GS} = 4.5 V
Gate to drain charge	Qgd	_	4.6	_	nC	I _D = 40 A
Turn-on delay time	t _{d(on)}	_	11.1	_	ns	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$
Rise time	t _r	_	7.3	- 0	ns	V _{DD} ≈ 10 V
Turn-off delay time	$t_{d(off)}$	_	49.1		ns	$R_L = 0.5 \Omega$
Fall time	t _f	_	7.6		ns	$R_g = 4.7 \Omega$
Schottky Barrier diode forward voltage	V_{F}	_	0.39	—	V	IF = 2 A, V _{GS} = 0 Note4
Body-drain diode reverse	t _{rr}	_	24	_	ns	IF = 40 A, V _{GS} = 0
recovery time					Ť	di _F / dt = 100 A/μs
Notes: 4. Pulse						

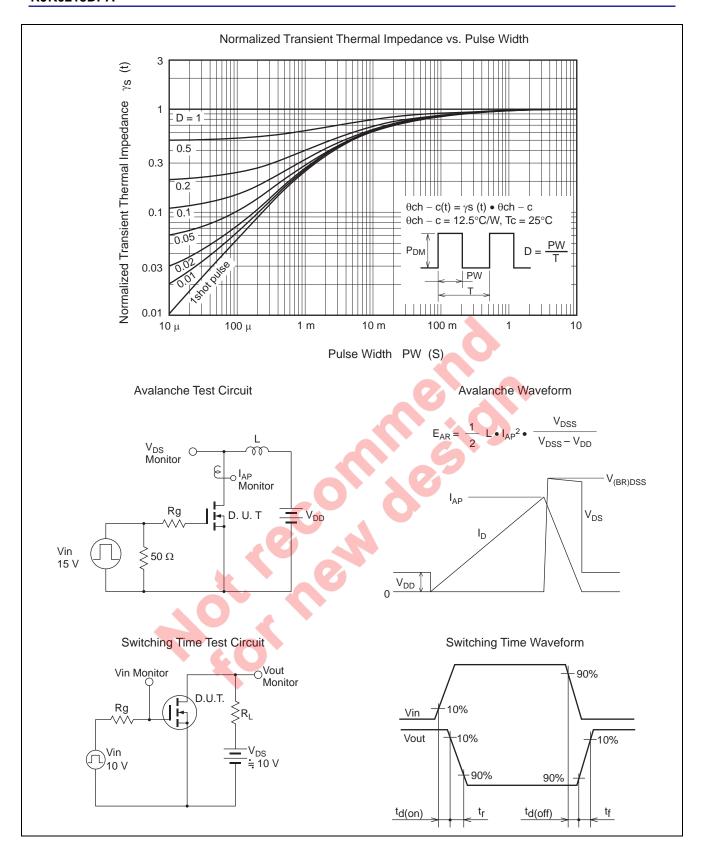


Main Characteristics

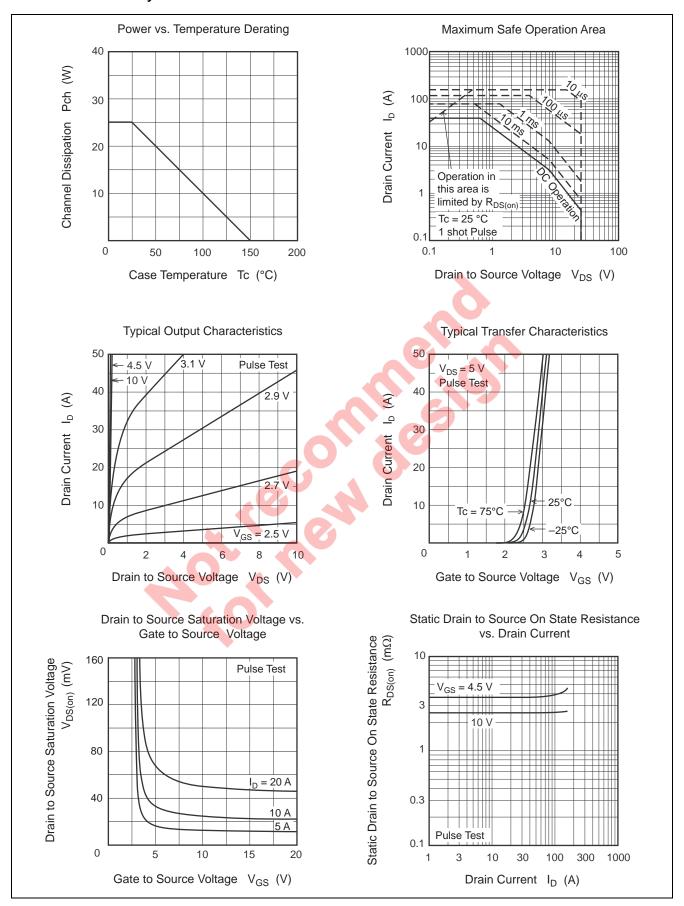
• MOS1

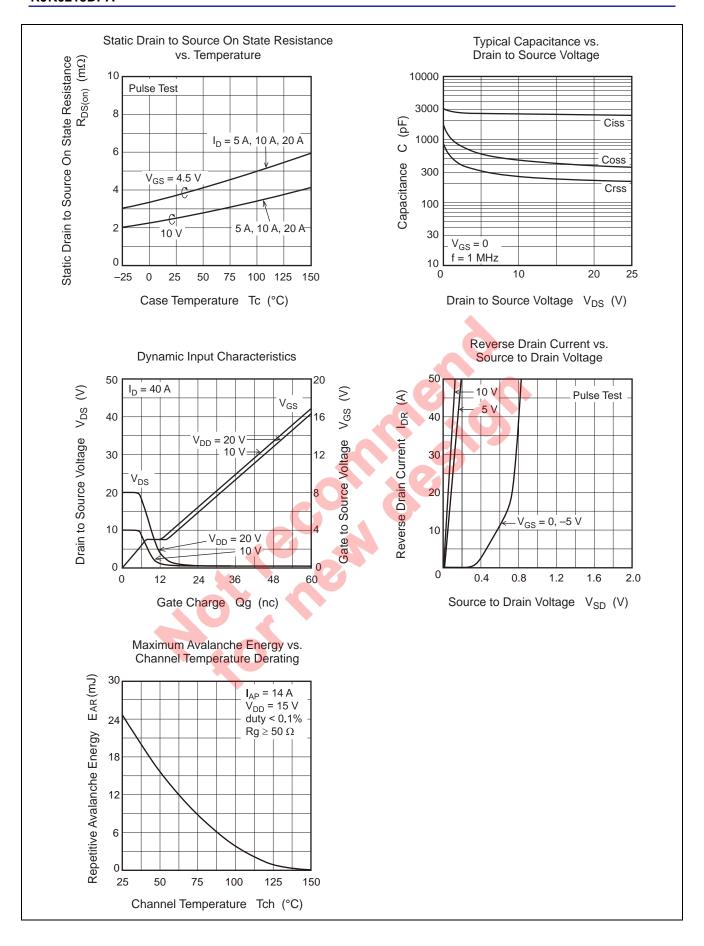


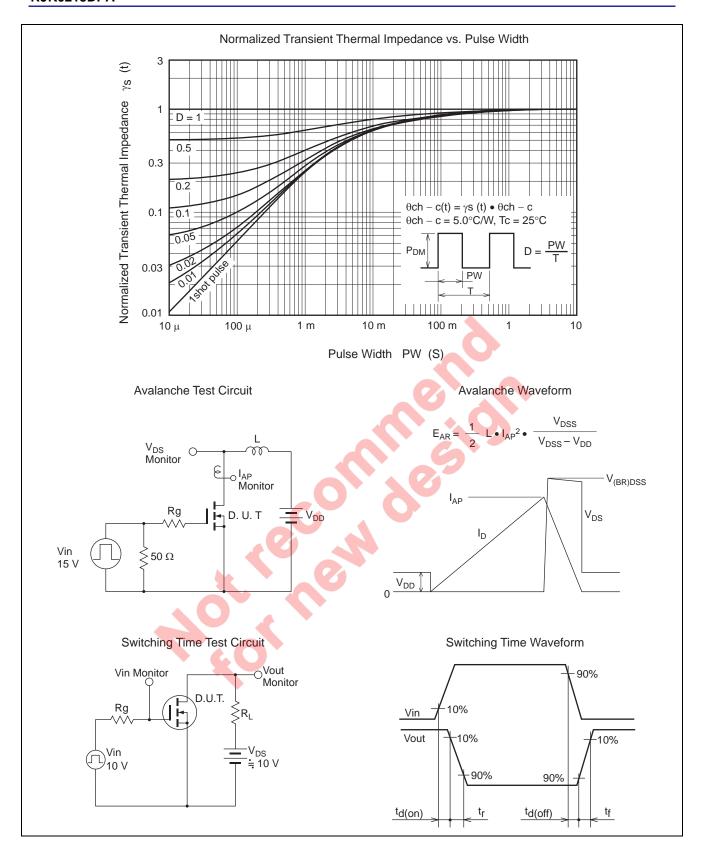




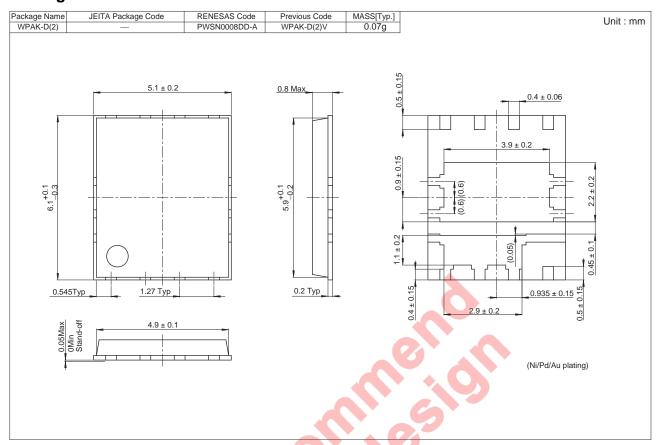
• MOS2 and Schottky Barrier Diode







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK0215DPA-00-J53	3000 pcs	Taping

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