

# RJK0226DNS

## Silicon N Channel Power MOS FET with Schottky Barrier Diode Power Switching

R07DS0260EJ0110

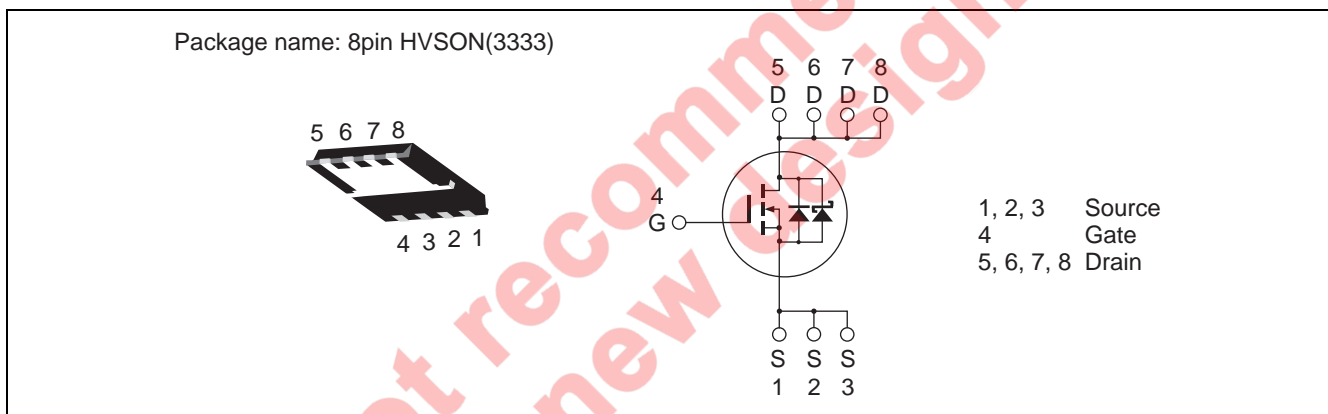
Rev.1.10

Mar 03, 2011

### Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  
 $R_{DS(on)} = 2.3 \text{ m}\Omega$  typ. (at  $V_{GS} = 8 \text{ V}$ )
- Pb-free
- Halogen-free

### Outline



### Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	25	V
Gate to source voltage	$V_{GSS}$	$\pm 12$	V
Drain current	$I_D$	40	A
Drain peak current	$I_{D(pulse)}$ <sup>Note1</sup>	160	A
Body-drain diode reverse drain current	$I_{DR}$	40	A
Avalanche current	$I_{AP}$ <sup>Note 2</sup>	14.7	A
Avalanche energy	$E_{AR}$ <sup>Note 2</sup>	27	mJ
Channel dissipation	$P_{ch}$ <sup>Note3</sup>	30	W
Channel to case thermal impedance	$\theta_{ch-c}$ <sup>Note3</sup>	4.17	$^\circ\text{C}/\text{W}$
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

- Notes: 1.  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$   
 2. Value at  $T_{ch} = 25^\circ\text{C}$ ,  $R_g \geq 50 \Omega$   
 3.  $T_c = 25^\circ\text{C}$

## Electrical Characteristics

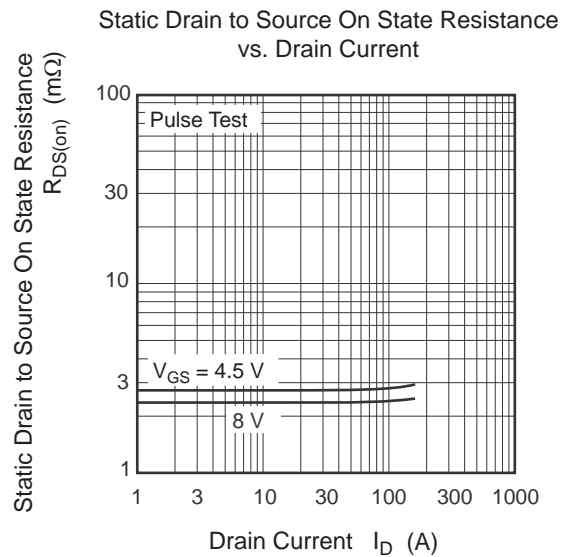
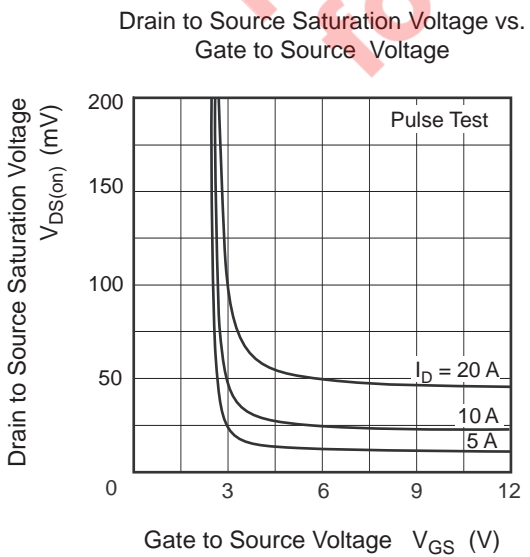
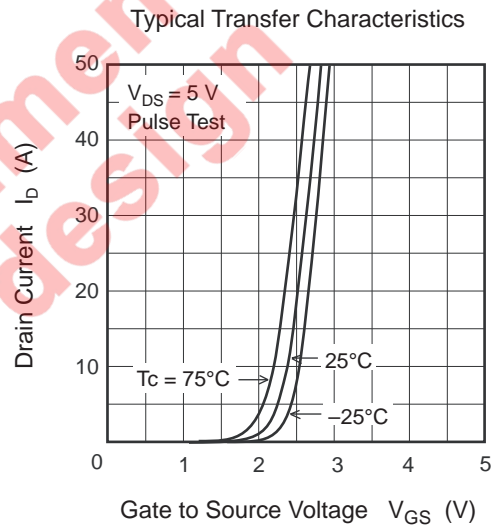
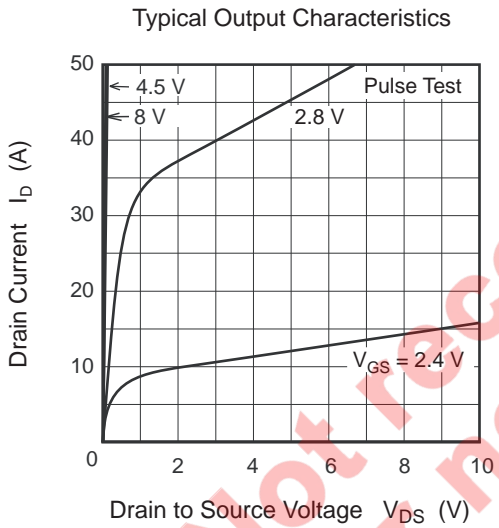
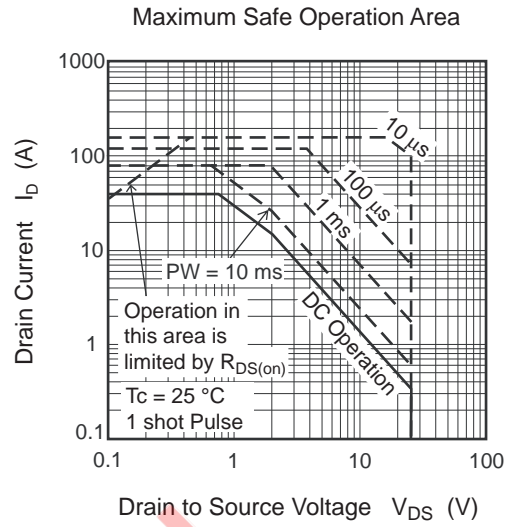
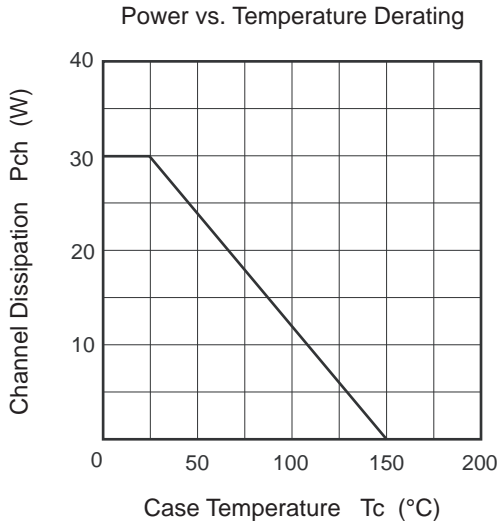
(Ta = 25°C)

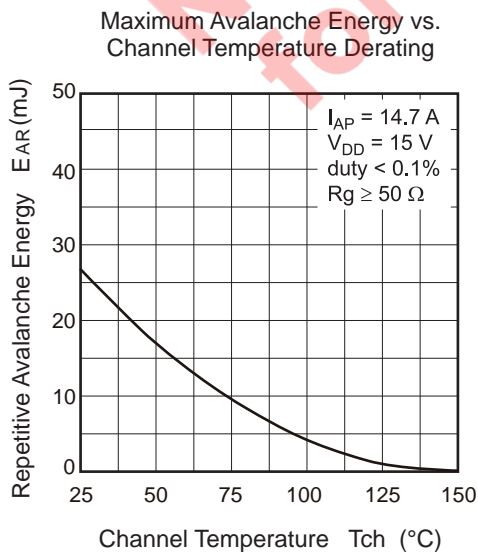
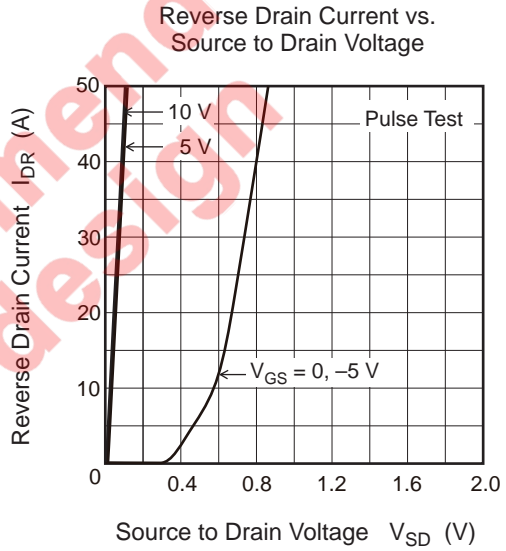
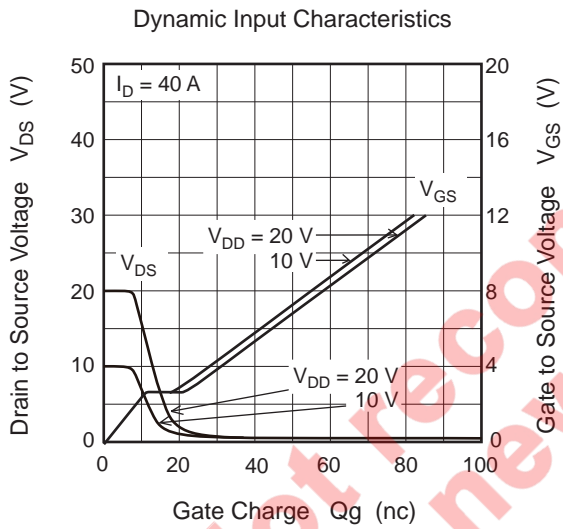
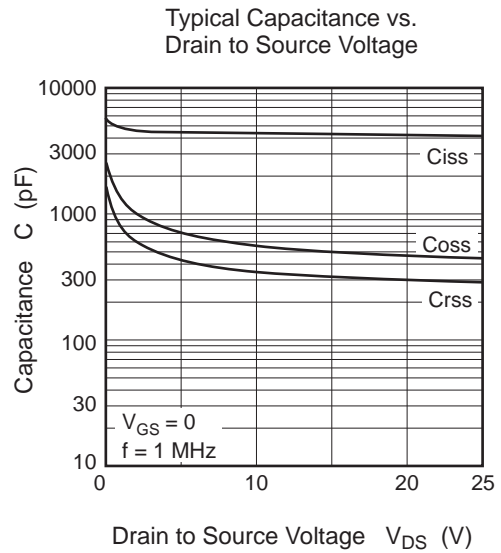
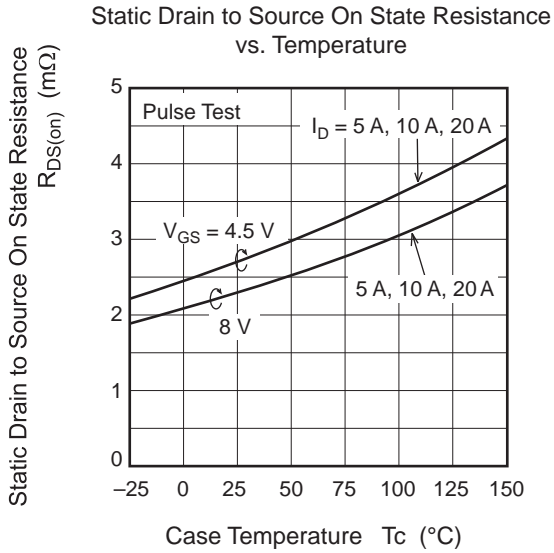
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	25	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	±0.1	μA	$V_{GS} = \pm 12 \text{ V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	1	mA	$V_{DS} = 25 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	2.3	2.8	mΩ	$I_D = 20 \text{ A}$ , $V_{GS} = 8 \text{ V}$ <sup>Note4</sup>
	$R_{DS(on)}$	—	2.7	3.4	mΩ	$I_D = 20 \text{ A}$ , $V_{GS} = 4.5 \text{ V}$ <sup>Note4</sup>
Forward transfer admittance	$ y_{fs} $	—	115	—	S	$I_D = 20 \text{ A}$ , $V_{DS} = 5 \text{ V}$ <sup>Note4</sup>
Input capacitance	$C_{iss}$	—	4300	6020	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	$C_{oss}$	—	565	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	$C_{rss}$	—	340	—	pF	$f = 1 \text{ MHz}$
Gate Resistance	$R_g$	—	2.6	4.5	Ω	
Total gate charge	$Q_g$	—	31	—	nC	$V_{DD} = 10 \text{ V}$
Gate to source charge	$Q_{gs}$	—	11	—	nC	$V_{GS} = 4.5 \text{ V}$
Gate to drain charge	$Q_{gd}$	—	8	—	nC	$I_D = 40 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	18.6	—	ns	$V_{GS} = 8 \text{ V}$ , $I_D = 20 \text{ A}$
Rise time	$t_r$	—	8.7	—	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	65	—	ns	$R_L = 0.5 \text{ Ω}$
Fall time	$t_f$	—	13	—	ns	$R_g = 4.7 \text{ Ω}$
Body-drain diode forward voltage	$V_{DF}$	—	0.39	—	V	$I_F = 2 \text{ A}$ , $V_{GS} = 0$ <sup>Note4</sup>
Body-drain diode reverse recovery time	$t_{rr}$	—	28	—	ns	$I_F = 40 \text{ A}$ , $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

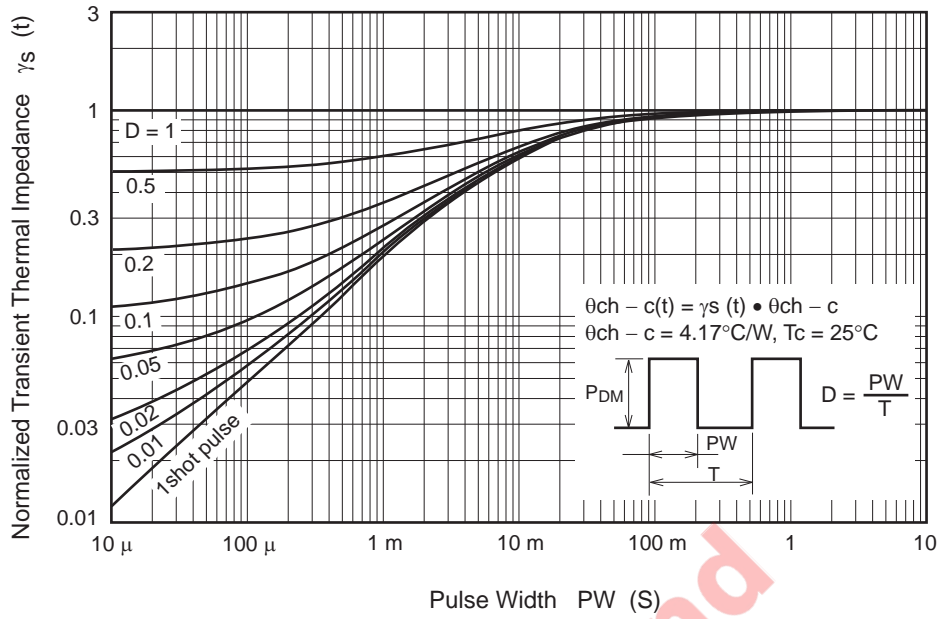
Not recommended for new designs

Main Characteristics

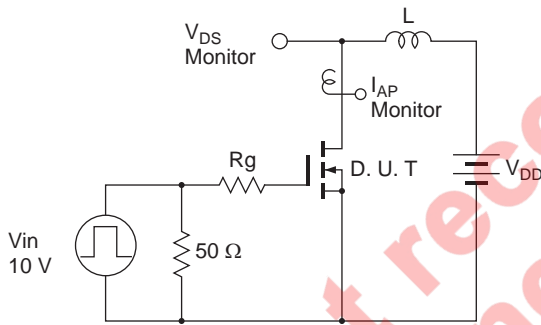




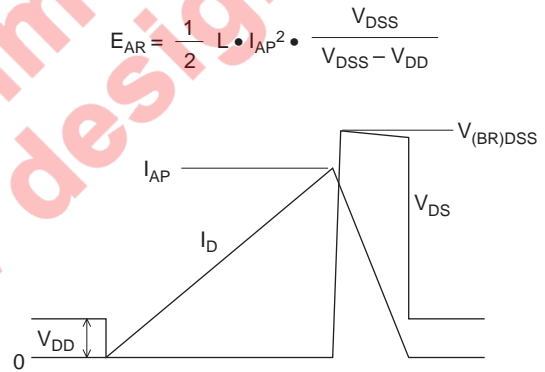
Normalized Transient Thermal Impedance vs. Pulse Width



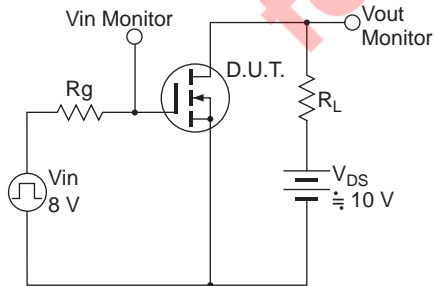
Avalanche Test Circuit



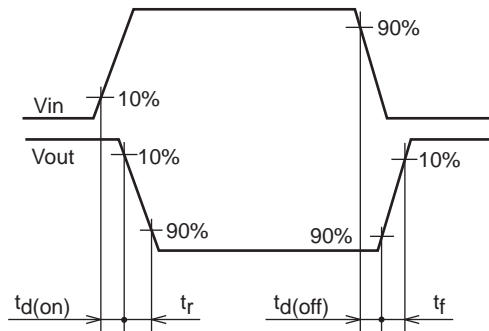
Avalanche Waveform



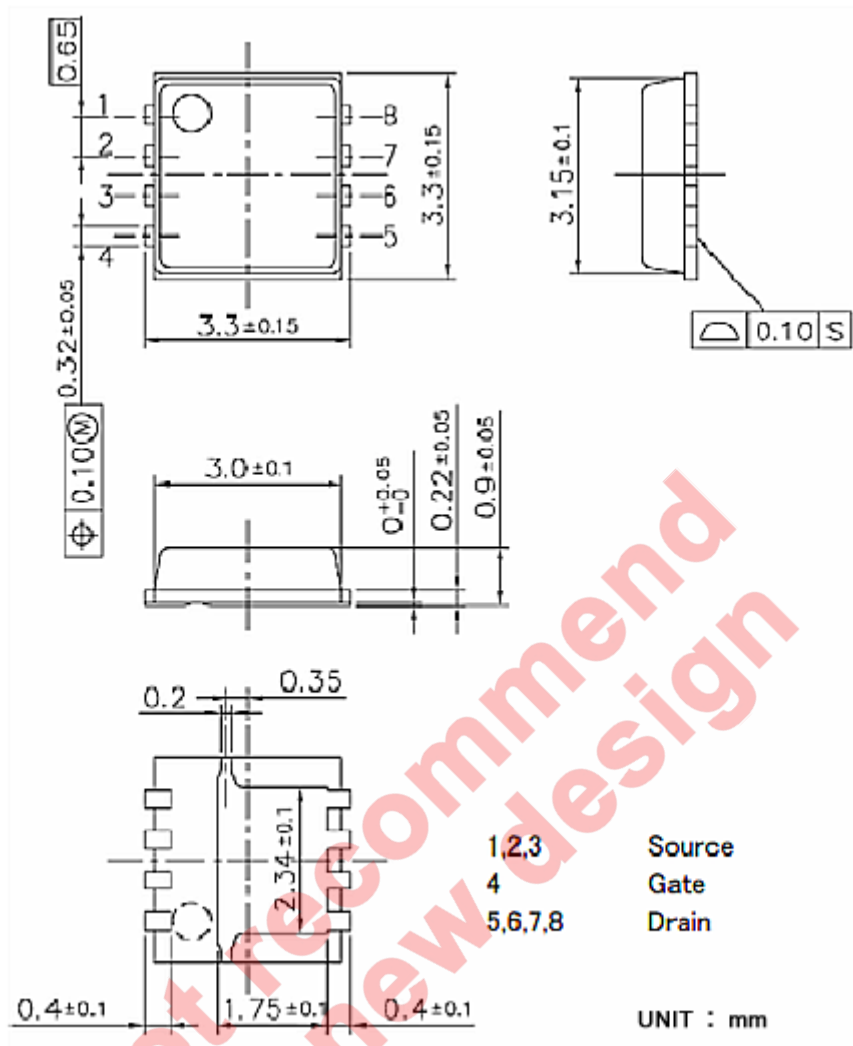
Switching Time Test Circuit



Switching Time Waveform



Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container	Package
RJK0226DNS-00-J5	3000 pcs	Taping	8pin HVSON(3333) 0.028g TYP

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