

RJK0355DSP

Silicon N Channel Power MOS FET Power Switching

REJ03G1650-0301 Rev.3.01 Apr 24, 2008

Features

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 8.5 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V)}$
- Pb-free

Outline

RENESAS Package code: PRSP0008DD-D (Package name: SOP-8<FP-8DAV>)

8765

BDDDDD

1, 2, 3 Source
4 Gate
5, 6, 7, 8 Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	12	А
Drain peak current	I _{D(pulse)} Note1	96	А
Body-drain diode reverse drain current	I _{DR}	12	А
Avalanche current	I _{AP} Note 2	9	A
Avalanche energy	E _{AR} Note 2	8.1	mJ
Channel dissipation	Pch Note3	1.8	W
Channel to ambient thermal impedance	θch-a Note3	69.4	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s

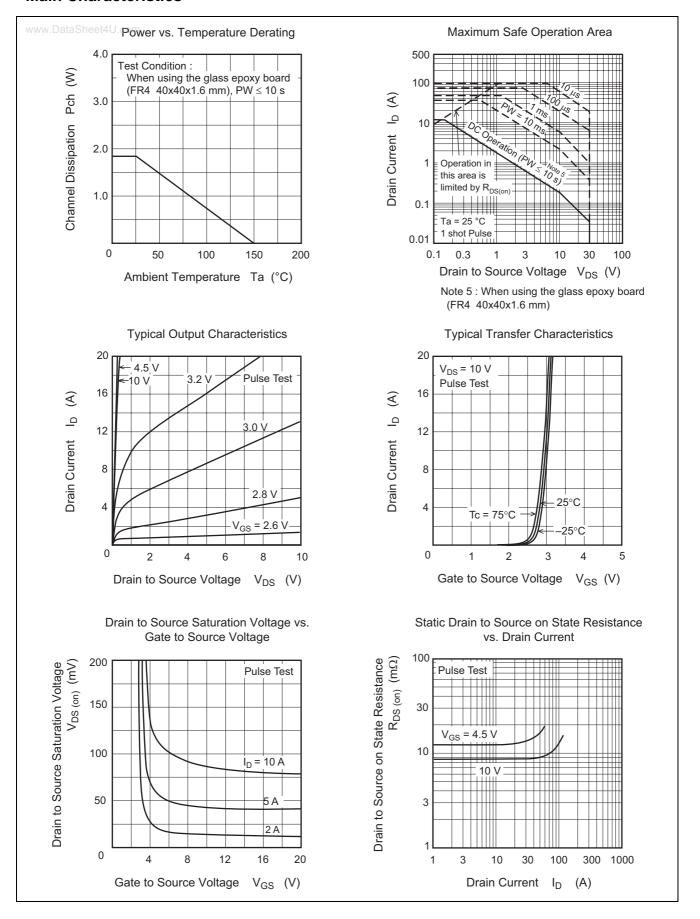
Electrical Characteristics

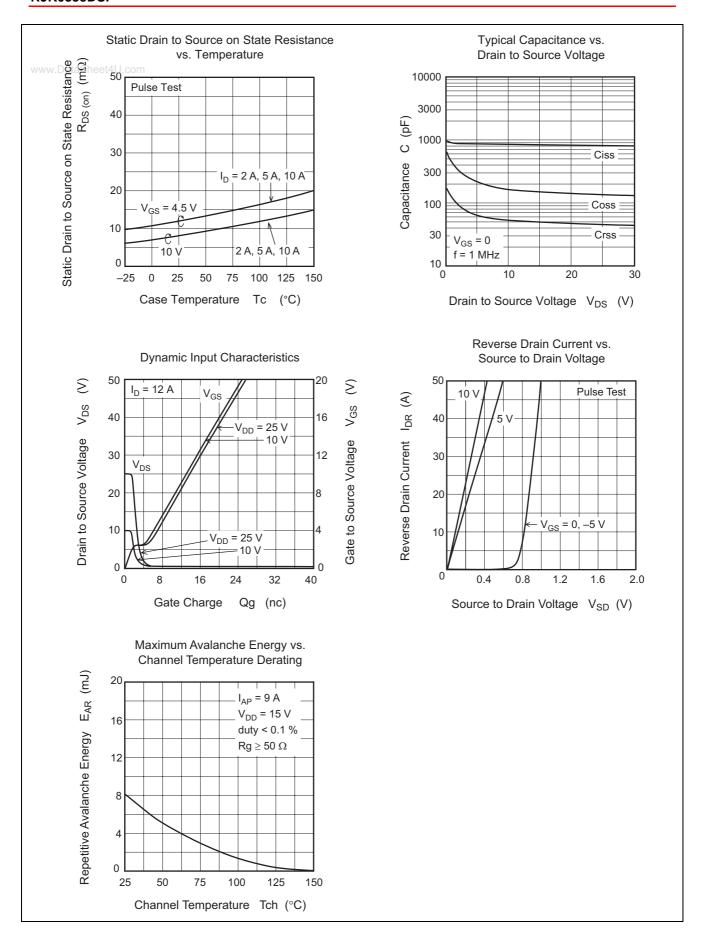
 $(Ta = 25^{\circ}C)$

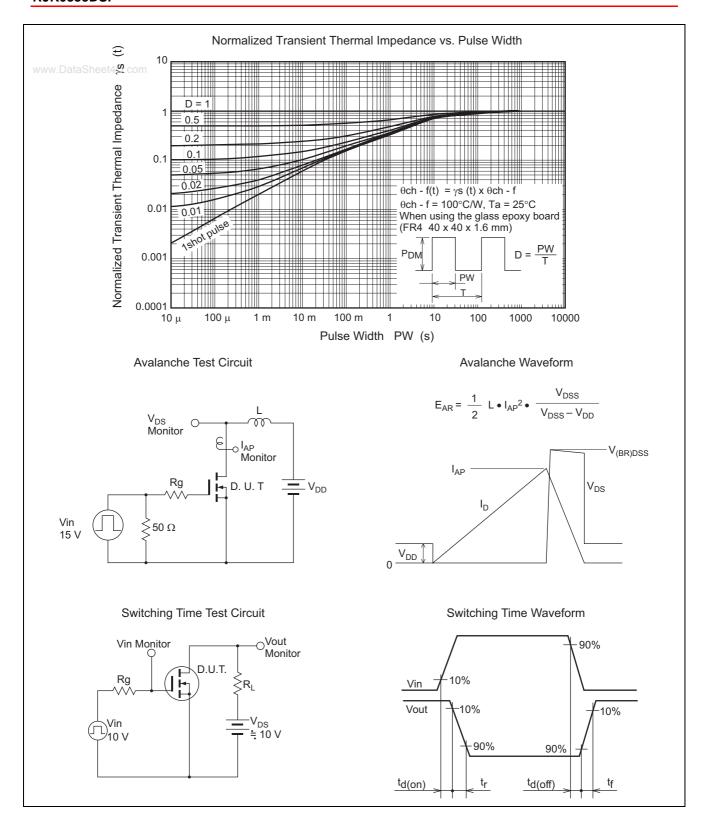
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	8.5	11.1	mΩ	$I_D = 6 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}		12.0	16.8	mΩ	$I_D = 6 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}		35	_	S	I _D = 6 A, V _{DS} = 10 V Note4
Input capacitance	Ciss		860	_	pF	V _{DS} = 10 V
Output capacitance	Coss		165	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		53	_	pF	f = 1 MHz
Gate Resistance	Rg		4.2	_	Ω	
Total gate charge	Qg		6.0	_	nC	V _{DD} = 10 V
Gate to source charge	Qgs	_	2.3	_	nC	V _{GS} = 4.5 V
Gate to drain charge	Qgd	_	1.4	_	nC	I _D = 6 A
Turn-on delay time	t _{d(on)}	_	6.9	_	ns	$V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$
Rise time	t _r		3.5	_	ns	V _{DD} ≅ 10 V
Turn-off delay time	t _{d(off)}		40.2	_	ns	$R_L = 1.67 \Omega$
Fall time	t _f		4.8	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}	_	0.82	1.07	V	I _F = 12 A, V _{GS} = 0 ^{Note3}
Body-drain diode reverse recovery	t _{rr}	_	20	_	ns	I _F = 12 A, V _{GS} = 0
time						$di_F/dt = 100 A/ \mu s$

Notes: 4. Pulse test

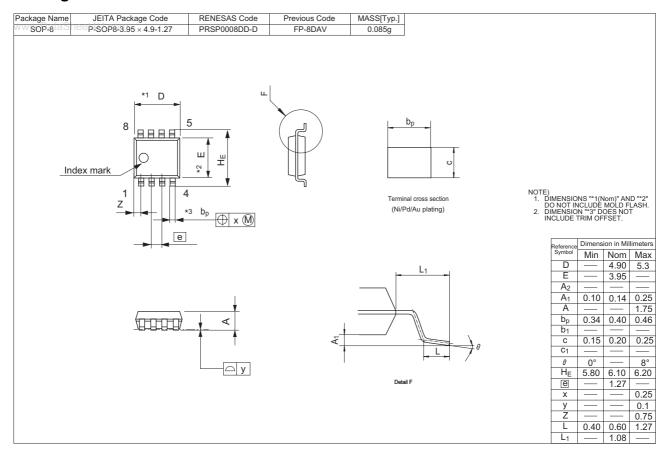
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0355DSP-00-J0	2500 pcs	Taping

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