

RJK0371DSP

Silicon N Channel Power MOS FET Power Switching

REJ03G1663-0201 Rev.2.01 Apr 24, 2008

Features

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 14 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V)}$
- Pb-free

Outline

RENESAS Package code: PRSP0008DD-D (Package name: SOP-8<FP-8DAV>)

8765

BDDDDD

1, 2, 3 Source
4 Gate
5, 6, 7, 8 Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	8	A
Drain peak current	I _{D(pulse)} Note1	64	A
Body-drain diode reverse drain current	I _{DR}	8	A
Avalanche current	I _{AP} Note 2	7	A
Avalanche energy	E _{AR} Note 2	4.9	mJ
Channel dissipation	Pch Note3	1.8	W
Channel to ambient thermal impedance	θch-a Note3	69.4	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s

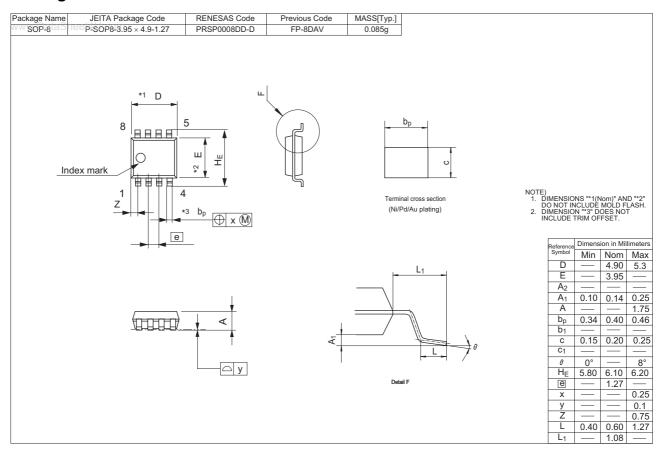
Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source leak current	I _{GSS}	_	_	± 0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	
Static drain to source on state	R _{DS(on)}	_	14	19	mΩ	$I_D = 4 A, V_{GS} = 10 V^{Note4}$	
resistance	R _{DS(on)}	_	19	27	mΩ	$I_D = 4 A$, $V_{GS} = 4.5 V^{Note4}$	
Forward transfer admittance	y _{fs}	_	15	_	S	$I_D = 4 A, V_{DS} = 10 V^{Note4}$	
Input capacitance	Ciss	_	590	_	pF	V _{DS} = 10 V	
Output capacitance	Coss	_	115	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	36	_	pF	f = 1 MHz	
Gate Resistance	Rg	_	1.5	_	Ω		
Total gate charge	Qg	_	3.8	_	nC	V _{DD} = 10 V	
Gate to source charge	Qgs	_	1.5	_	nC	$V_{GS} = 4.5 \text{ V}$	
Gate to drain charge	Qgd	_	1.0	_	nC	$I_D = 8 A$	
Turn-on delay time	t _{d(on)}	_	4.5	_	ns	$V_{GS} = 10 \text{ V}, I_D = 4 \text{ A}$	
Rise time	t _r	_	3.0	_	ns	$V_{DD} \cong 10 \text{ V}$	
Turn-off delay time	t _{d(off)}	_	31.4	_	ns	$R_L = 2.5 \Omega$	
Fall time	t _f	_	3.5	_	ns	$Rg = 4.7 \Omega$	
Body-drain diode forward voltage	V_{DF}	_	0.83	1.08	V	$I_F = 8 \text{ A}, V_{GS} = 0^{\text{Note4}}$	
Body-drain diode reverse recovery	t _{rr}	_	15	_	ns	$I_F = 8 A, V_{GS} = 0$	
time						$di_F/dt = 100 A/ \mu s$	

Notes: 4. Pulse test

Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0371DSP-00-J0	2500 pcs	Taping

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