

# RJK03F0DPA

# Silicon N Channel Power MOS FET Power Switching

REJ03G1934-0210 Rev.2.10 May 20, 2010

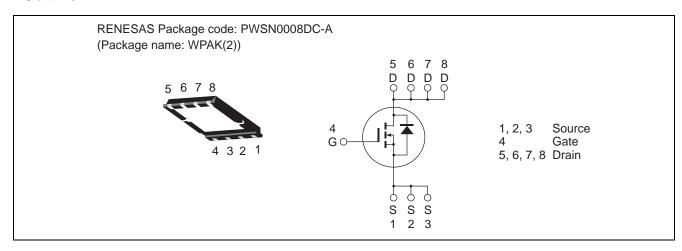
#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)}$  = 5.3 m $\Omega$  typ. (at  $V_{GS}$  = 8 V)

- Pb-free
- Halogen-free

#### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±12	V
Drain current	I <sub>D</sub>	30	A
Drain peak current	I <sub>D(pulse)</sub> Note1	120	A
Body-drain diode reverse drain current	I <sub>DR</sub>	30	A
Avalanche current	I <sub>AP</sub> Note 2	12	A
Avalanche energy	E <sub>AR</sub> Note 2	14.4	mJ
Channel dissipation	Pch Note3	30	W
Channel to case thermal impedance	θch-c Note3	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Tc = 25°C

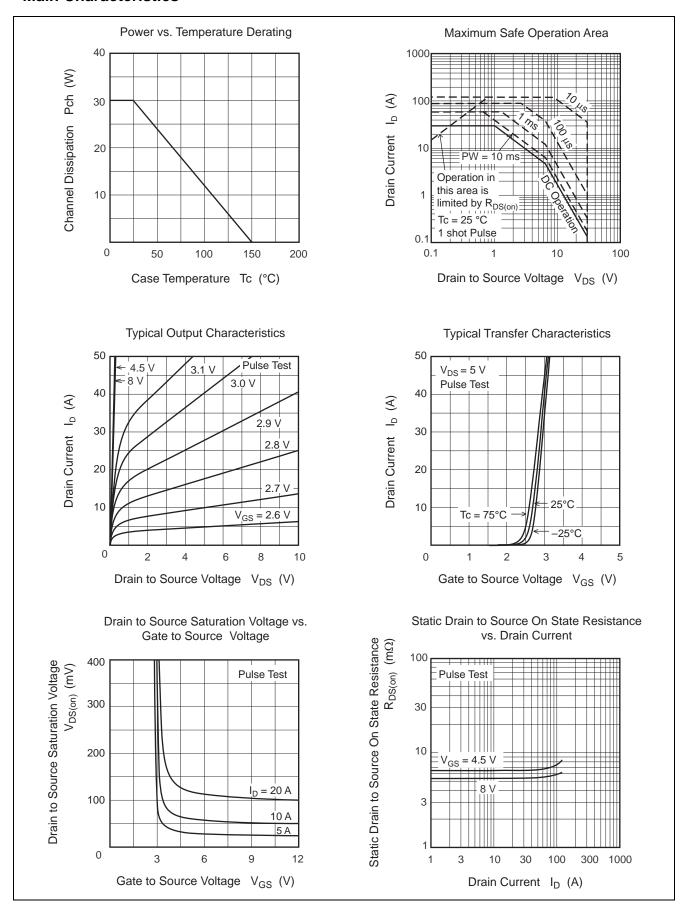
# **Electrical Characteristics**

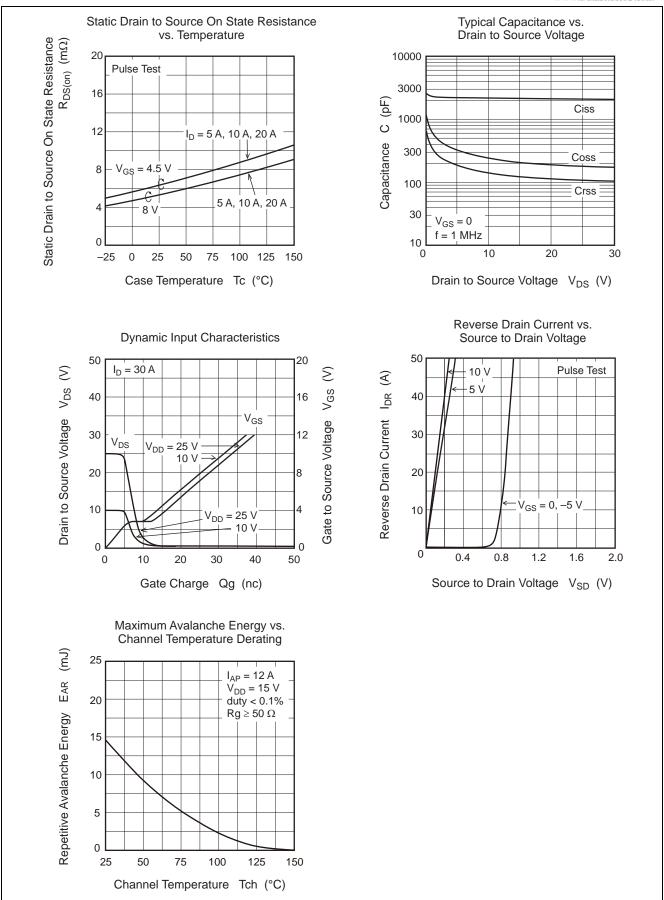
 $(Ta = 25^{\circ}C)$ 

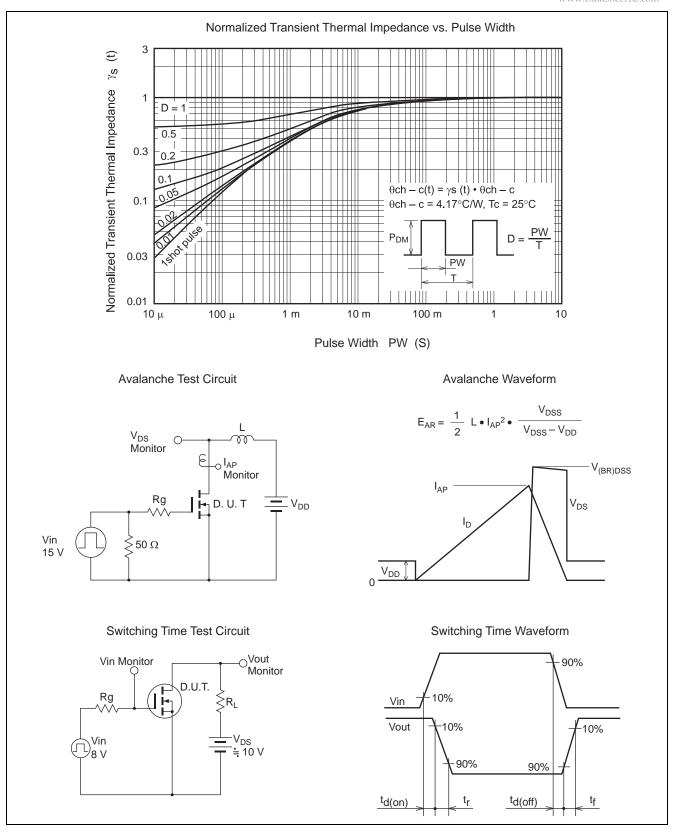
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	$I_{GSS}$	_	_	± 0.1	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	1	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	5.3	6.4	mΩ	$I_D = 15 \text{ A}, V_{GS} = 8.0 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	6.3	7.8	mΩ	$I_D = 15 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	_	80	_	S	$I_D = 15 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	2150	3010	рF	$V_{DS} = 10 \text{ V}$
Output capacitance	Coss	_	240	_	рF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	140	_	рF	
Gate Resistance	Rg	_	1.7	3.4	Ω	
Total gate charge	Qg	_	15	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	6.1	_	nC	V <sub>GS</sub> = 4.5 V
Gate to drain charge	Qgd	_	4.4	_	nC	I <sub>D</sub> = 30 A
Turn-on delay time	t <sub>d(on)</sub>	_	12.5	_	ns	$V_{GS} = 8 \text{ V}, I_D = 15 \text{ A}$
Rise time	t <sub>r</sub>	_	4.8	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	_	43	_	ns	$R_L = 0.67 \Omega$
Fall time	t <sub>f</sub>	_	7.3	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	0.87	1.13	V	$I_F = 30 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	16	_	ns	$I_F = 30 \text{ A}, V_{GS} = 0$ $di_F / dt = 100 \text{ A} / \mu \text{s}$

Notes: 4. Pulse test

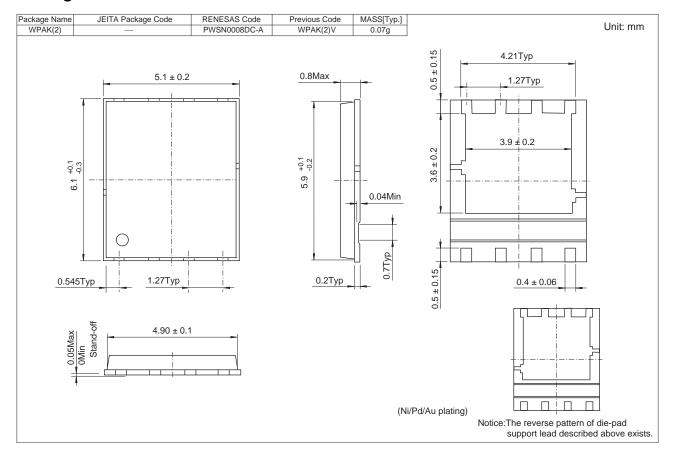
#### **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

Part No.	Quantity	Shipping Container
RJK03F0DPA-00-J53	3000 pcs	Taping

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