

RJK1021DPE

N-Channel Power MOSFET
High-Speed Switching Use

REJ03G1630-0100

Rev.1.00

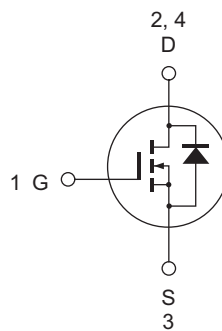
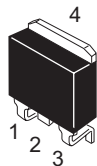
Apr 03, 2008

Features

- V_{DSS} : 100 V
- $R_{DS(on)}$: 20 m Ω (Max)
- I_D : 70 A

Outline

RENESAS Package code: PRSS0004AE-B
(Package name: LDKPAK(S)-(1))



1. Gate
2. Drain
3. Source
4. Drain

Application

- Motor control, Lighting control, Solenoid control, DC-DC converter, etc.

Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_D	70	A
Drain peak current	$I_{D(pulse)}$	140	A
Body-drain diode reverse drain current	I_{DR}	70	A
Body-drain diode reverse drain peak current	$I_{DR(pulse)}$	140	A
Avalanche current	I_{AP} ^{Note2}	35	A
Channel dissipation	P_{ch} ^{Note1}	100	W
Channel to case thermal impedance	θ_{ch-c}	1.25	$^\circ\text{C}/\text{W}$
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

Notes: 1. Value at $T_c = 25^\circ\text{C}$

2. $ST_{ch} = 25^\circ\text{C}$, $T_{ch} \leq 150^\circ\text{C}$, $L = 100 \mu\text{H}$

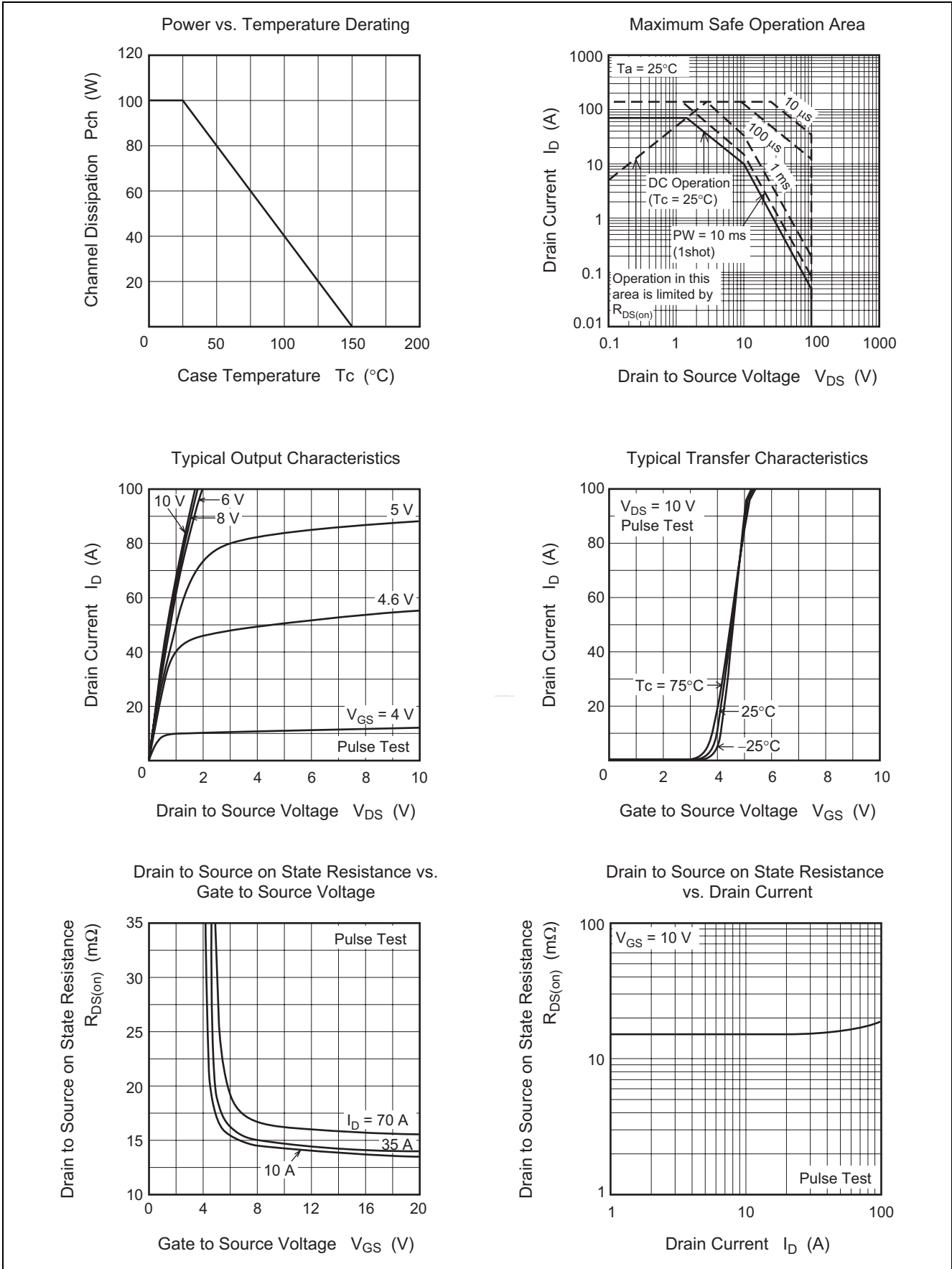
Electrical Characteristics

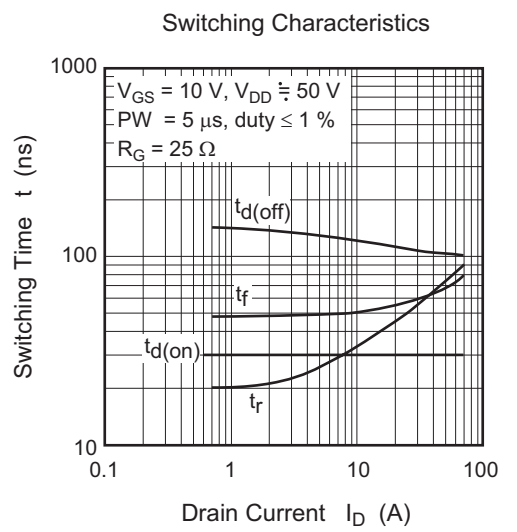
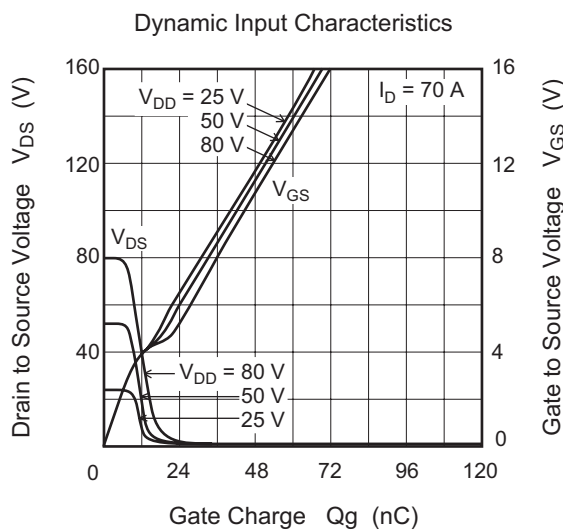
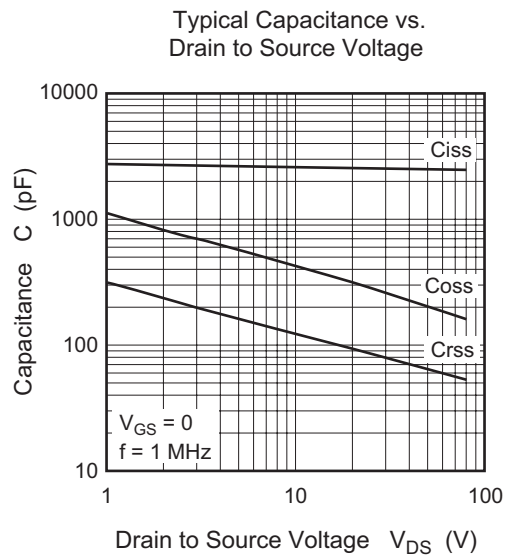
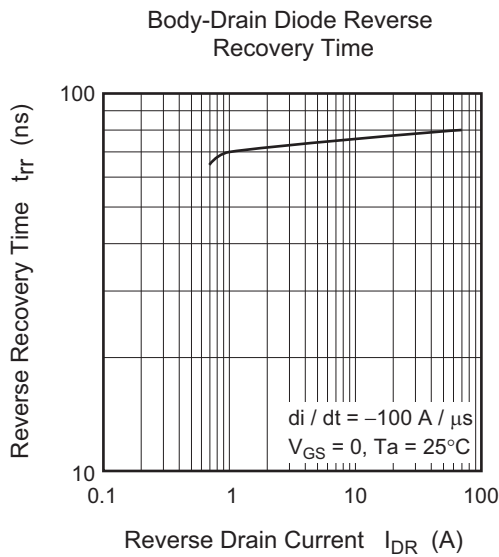
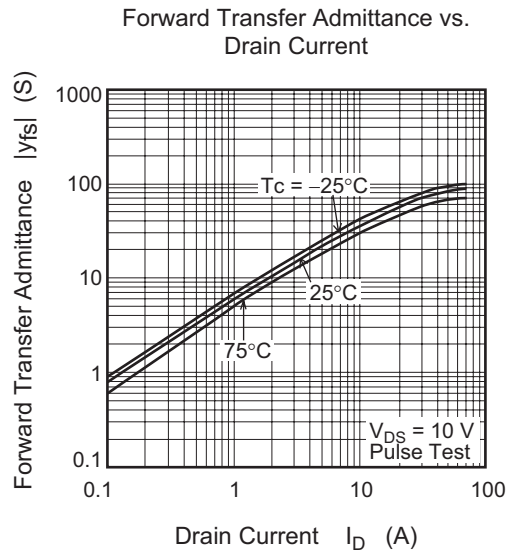
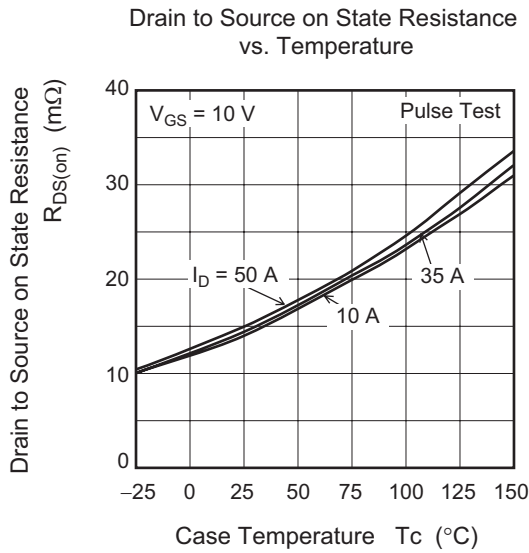
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	—	—	V	$I_D = 1 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	100	μA	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	3.0	4.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$ ^{Note3}
Static drain to source on state voltage	$V_{DS(on)}$	—	0.56	0.70	V	$I_D = 35 \text{ A}, V_{GS} = 10 \text{ V}$ ^{Note3}
Static drain to source on state resistance	$R_{DS(on)}$	—	16	20	$\text{m}\Omega$	$I_D = 35 \text{ A}, V_{GS} = 10 \text{ V}$ ^{Note3}
Input capacitance	C_{iss}	—	2600	—	pF	$V_{DS} = 10 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	430	—	pF	
Reverse transfer capacitance	C_{rss}	—	160	—	pF	
Turn-on delay time	$t_{d(on)}$	—	30	—	ns	$V_{DD} = 50 \text{ V}$ $I_D = 35 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_G = 25 \Omega$
Rise time	t_r	—	70	—	ns	
Turn-off delay time	$t_{d(off)}$	—	110	—	ns	
Fall time	t_f	—	65	—	ns	
Body-drain diode forward voltage	V_{DF}	—	0.9	1.5	V	$I_F = 35 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t_{rr}	—	80	—	ns	$I_F = 70 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

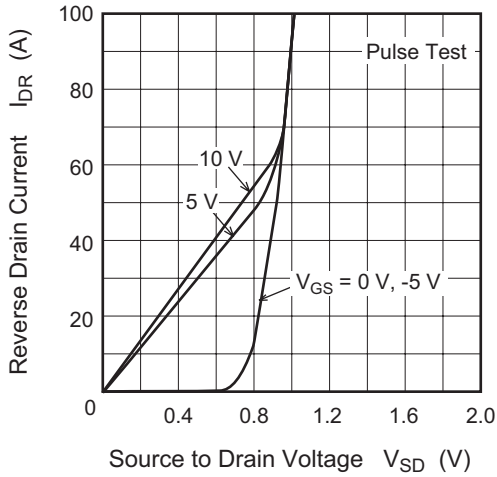
Notes: 3. Pulse test

Main Characteristics

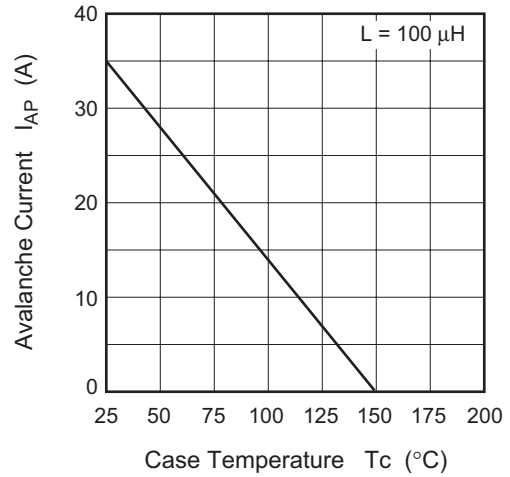




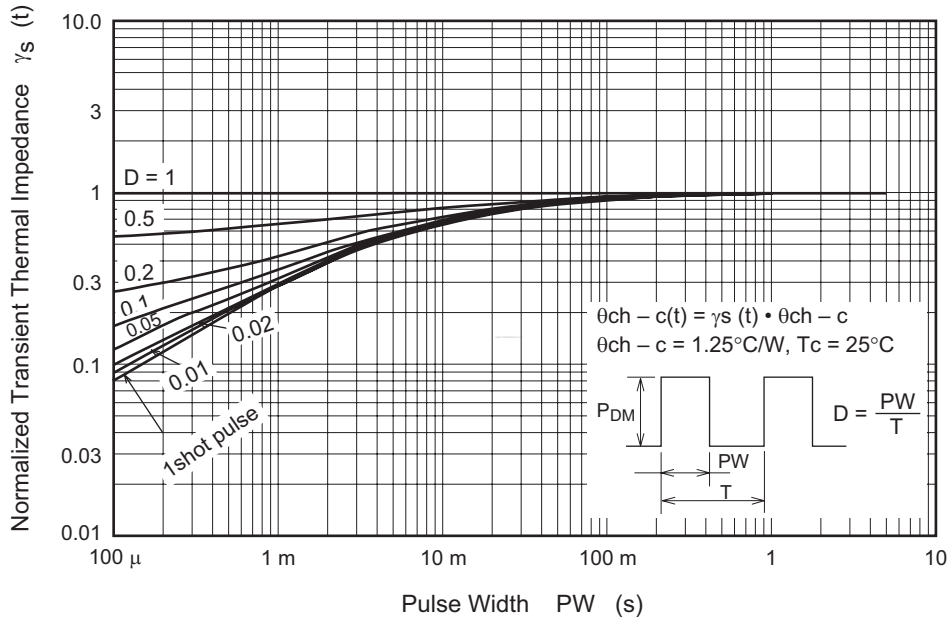
Reverse Drain Current vs. Source to Drain Voltage



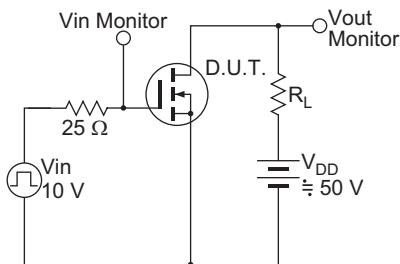
Avalanche Current vs. Case Temperature



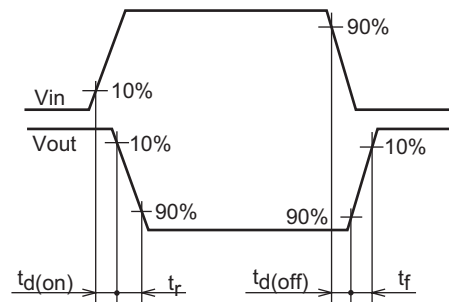
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



Waveform



Notes:

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510