

RJL6018DPK

Silicon N Channel MOS FET
High Speed Power Switching

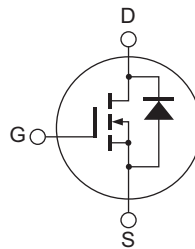
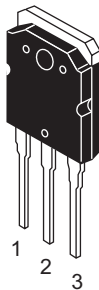
REJ03G1819-0100
Rev.1.00
Sep 11, 2009

Features

- Built-in fast recovery diode
- Low on-resistance
- Low leakage current
- High speed switching

Outline

RENESAS Package code: PRSS0004ZE-A
(Package name: TO-3P)



1. Gate
2. Drain (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	600	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	27	A
Drain peak current	I _{D (pulse)} ^{Note1}	81	A
Body-drain diode reverse drain current	I _{DR}	27	A
Body-drain diode reverse drain peak current	I _{DR (pulse)} ^{Note1}	81	A
Avalanche current	I _{AP} ^{Note3}	6	A
Avalanche energy	E _{AR} ^{Note3}	1.9	mJ
Channel dissipation	P _{ch} ^{Note2}	200	W
Channel to case thermal impedance	θ _{ch-c}	0.625	°C/W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. STch = 25°C, Tch ≤ 150°C

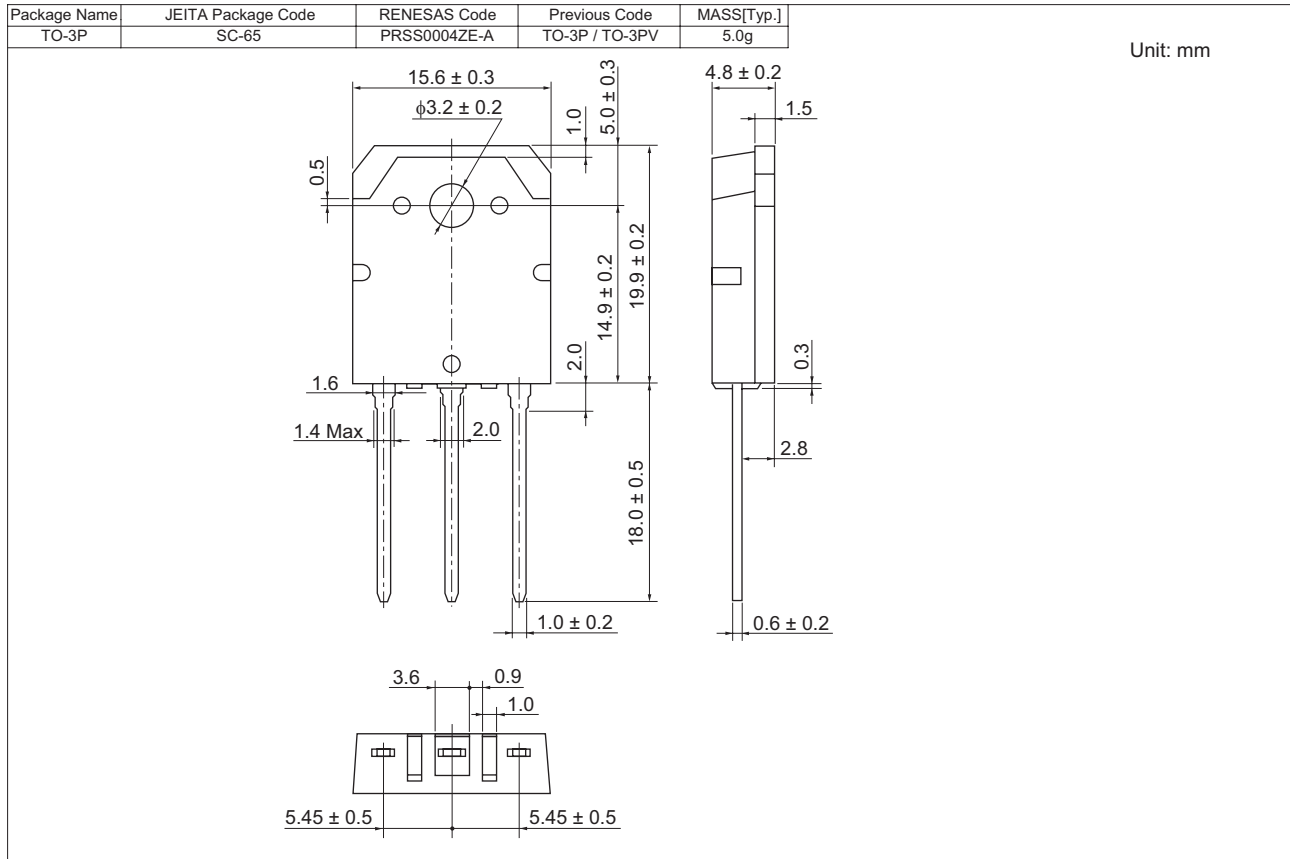
Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	10	μA	$V_{DS} = 600 \text{ V}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 30 \text{ V}$, $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	—	4.0	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.220	0.265	Ω	$I_D = 13.5 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	3830	—	pF	$V_{DS} = 25 \text{ V}$ $V_{GS} = 0$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	369	—	pF	
Reverse transfer capacitance	C_{rss}	—	43	—	pF	
Turn-on delay time	$t_{d(on)}$	—	42	—	ns	$I_D = 13.5 \text{ A}$ $V_{GS} = 10 \text{ V}$ $R_L = 22.2 \Omega$ $R_g = 10 \Omega$
Rise time	t_r	—	53	—	ns	
Turn-off delay time	$t_{d(off)}$	—	157	—	ns	
Fall time	t_f	—	87	—	ns	
Total gate charge	Q_g	—	98	—	nC	$V_{DD} = 480 \text{ V}$ $V_{GS} = 10 \text{ V}$ $I_D = 27 \text{ A}$
Gate to source charge	Q_{gs}	—	17.5	—	nC	
Gate to drain charge	Q_{gd}	—	41.6	—	nC	
Body-drain diode forward voltage	V_{DF}	—	1.00	1.65	V	$I_F = 27 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-drain diode reverse recovery time	t_{rr}	—	180	—	ns	$I_F = 27 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJL6018DPK-00-T0	360 pcs	Box (Tube)

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