

RJL6020DPK

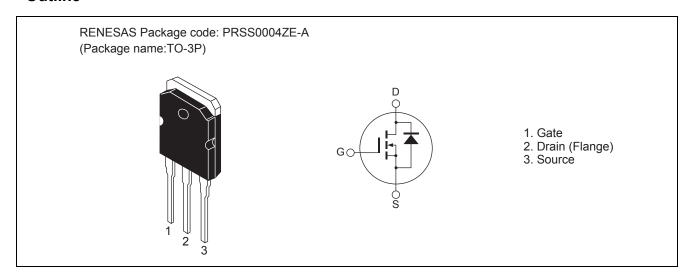
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1618-0300 Rev.3.00 Jan 22, 2010

Features

- Built-in fast recovery diode
- Low on-resistance $R_{DS(on)} = 0.17 \Omega \text{ typ. (at } I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}, Ta = 25^{\circ}\text{C})$
- Low leakage current
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	600	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I _D	30	Α
Drain peak current	I _{D (pulse)} Note1	90	Α
Body-drain diode reverse drain current	I _{DR}	30	Α
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	90	Α
Avalanche current	I _{AP} Note3	8.5	Α
Avalanche energy	E _{AR} Note3	3.9	mJ
Channel dissipation	Pch Note2	200	W
Channel to case thermal impedance	θch-c	0.625	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tc = 25°C
- 3. STch = 25° C, Tch $\leq 150^{\circ}$ C

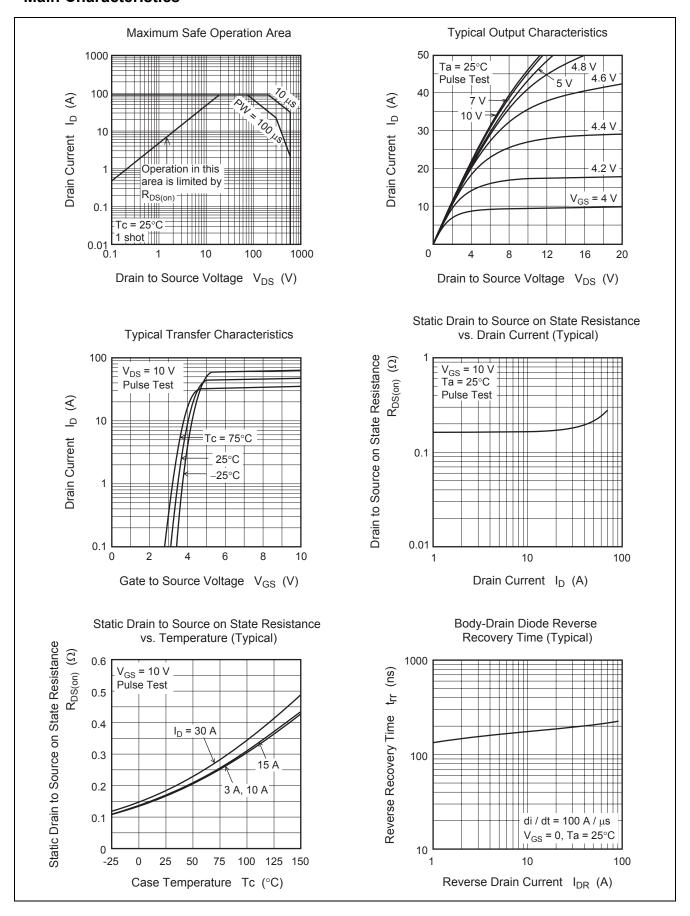
Electrical Characteristics

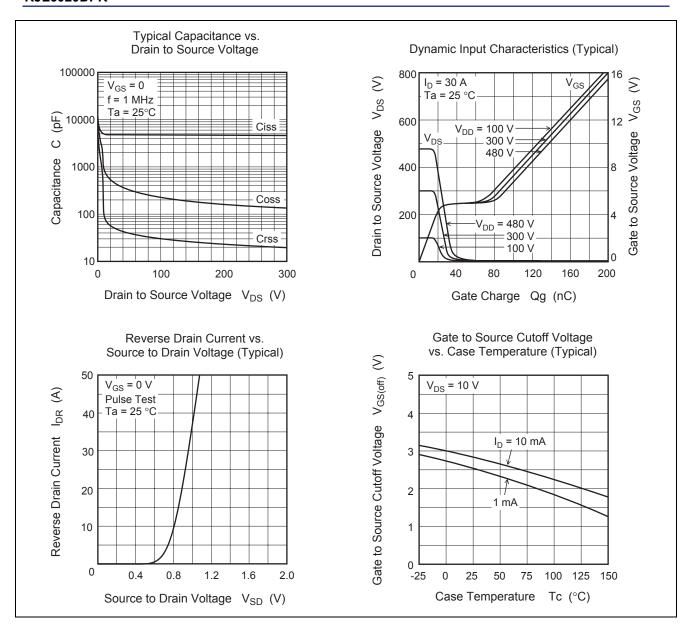
 $(Ta = 25^{\circ}C)$

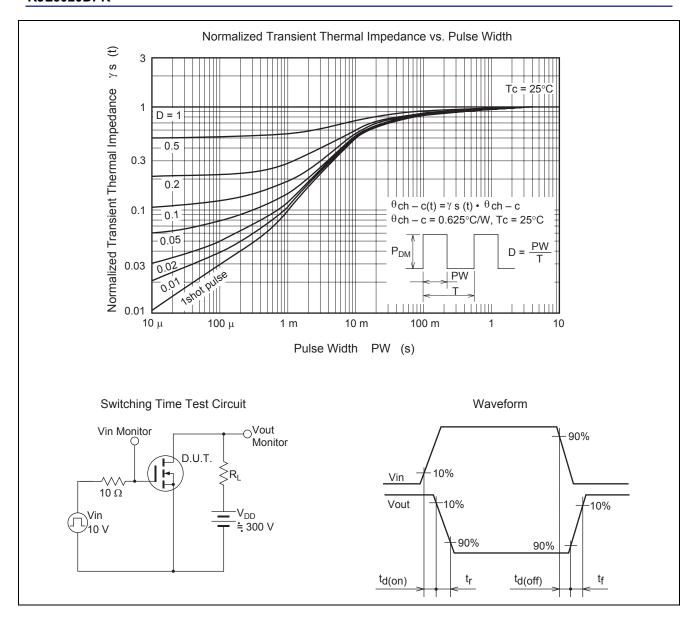
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Zero gate voltage drain current	I _{DSS}		_	10	μА	V _{DS} = 600 V, V _{GS} = 0	
Gate to source leak current	I _{GSS}	_	_	±0.1	μА	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	_	4.0	V	V_{DS} = 10 V, I_{D} = 1 mA	
Static drain to source on state resistance	R _{DS(on)}	_	0.17	0.21	Ω	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss	_	4750	_	pF	V _{DS} = 25 V	
Output capacitance	Coss	_	465	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	53	_	pF	f = 1 MHz	
Turn-on delay time	$t_{d(on)}$	_	44	_	ns	I _D = 15 A	
Rise time	t _r	_	64	_	ns	V _{GS} = 10 V	
Turn-off delay time	$t_{\text{d(off)}}$	_	206	_	ns	$R_L = 20 \Omega$	
Fall time	t _f	_	121	_	ns	$Rg = 10 \Omega$	
Total gate charge	Qg	_	130	_	nC	V _{DD} = 480 V	
Gate to source charge	Qgs	_	21	_	nC	V _{GS} = 10 V	
Gate to drain charge	Qgd	_	53	_	nC	I _D = 30 A	
Body-drain diode forward voltage	V_{DF}	_	0.96	1.60	V	$I_F = 30 \text{ A}, V_{GS} = 0^{\text{Note4}}$	
Body-drain diode reverse recovery time	t _{rr}	-	180	_	ns	$I_F = 30 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$	

Notes: 4. Pulse test

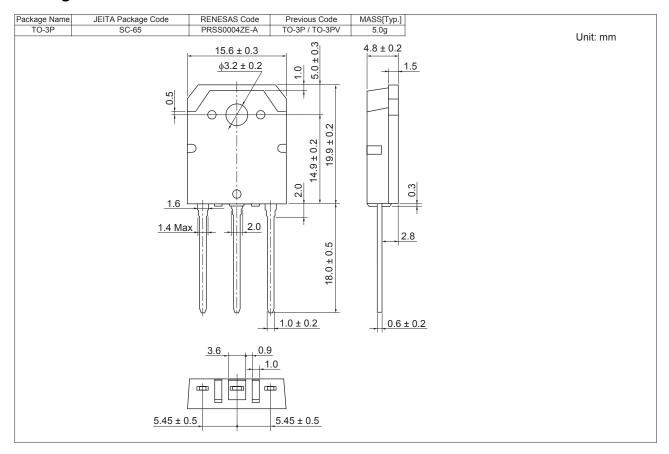
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJL6020DPK-00-T0	360 pcs	Box (Tube)

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