

# RKS1510DKK

Silicon Epitaxial Planar Diode for UHF/VHF tuner Band Switch

REJ03G0538-0100

Rev.1.00

Feb 23, 2005

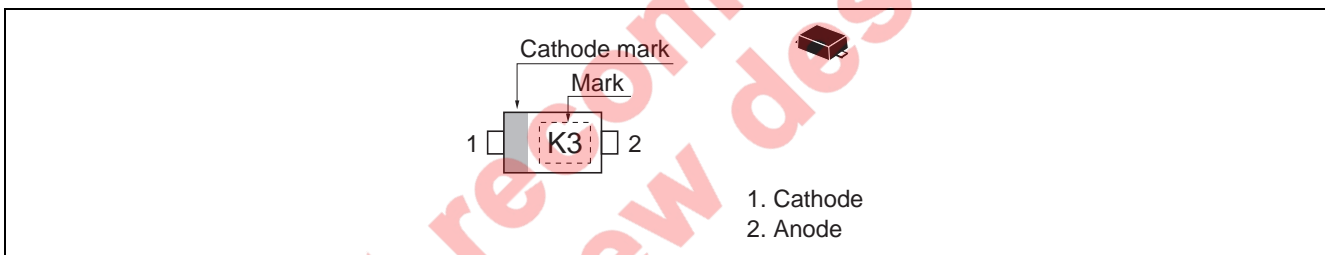
## Features

- Low capacitance. ( $C = 0.8 \text{ pF max}$ )
- Low forward resistance. ( $r_f = 0.7 \Omega \text{ max}$ )
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

## Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
RKS1510DKK	K3	SFP	PUSF0002ZB-A (SFP)

## Pin Arrangement



Not recommended for new design

## Absolute Maximum Ratings

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(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	$V_R$	35	V
Power dissipation	$P_d$	150	mW
Junction temperature	$T_j$	125	°C
Storage temperature	$T_{stg}$	-55 to +125	°C

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	$I_R$	—	—	50	nA	$V_R = 25\text{ V}$
Forward voltage	$V_F$	—	—	1.0	V	$I_F = 10\text{ mA}$
Capacitance	$C$	—	—	0.8	pF	$V_R = 6\text{ V}$ , $f = 1\text{ MHz}$
Forward resistance	$r_f$	—	—	0.7	$\Omega$	$I_F = 2\text{ mA}$ , $f = 100\text{ MHz}$

Notes: 1. Please do not use the soldering iron due to avoid high stress to the SFP package.

2. The material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

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Main Characteristic

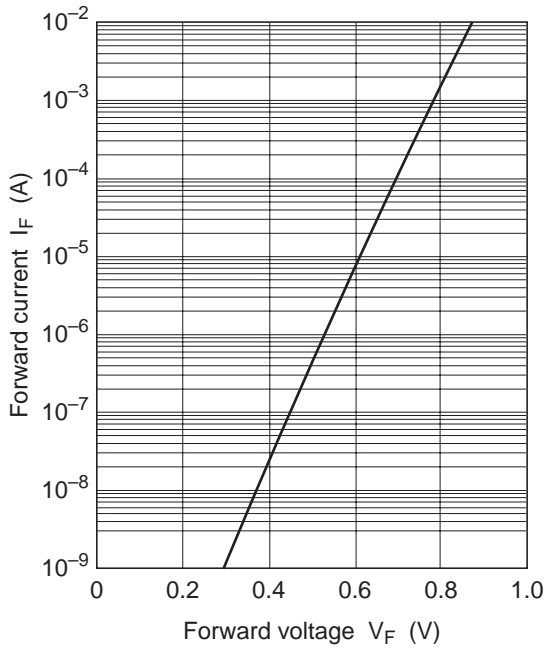


Fig.1 Forward current vs. Forward voltage

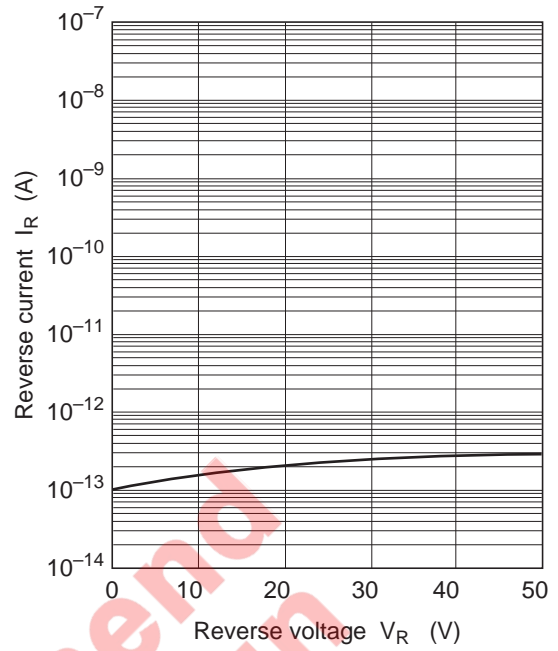


Fig.2 Reverse current vs. Reverse voltage

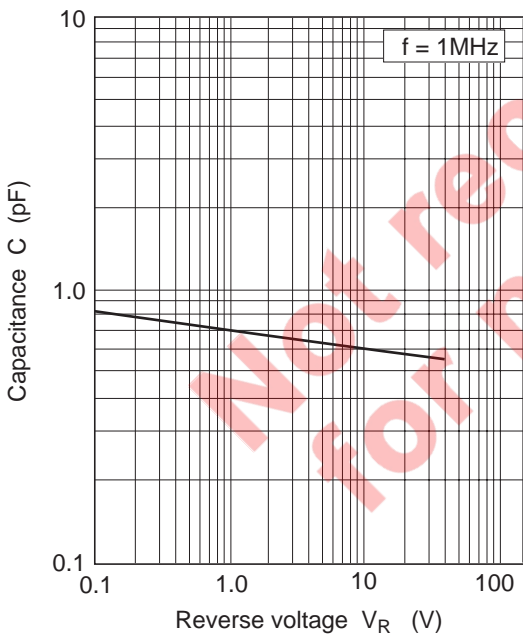


Fig.3 Capacitance vs. Reverse voltage

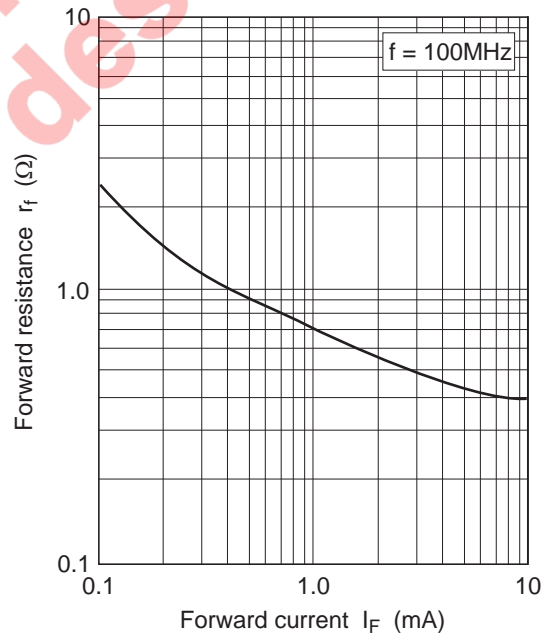
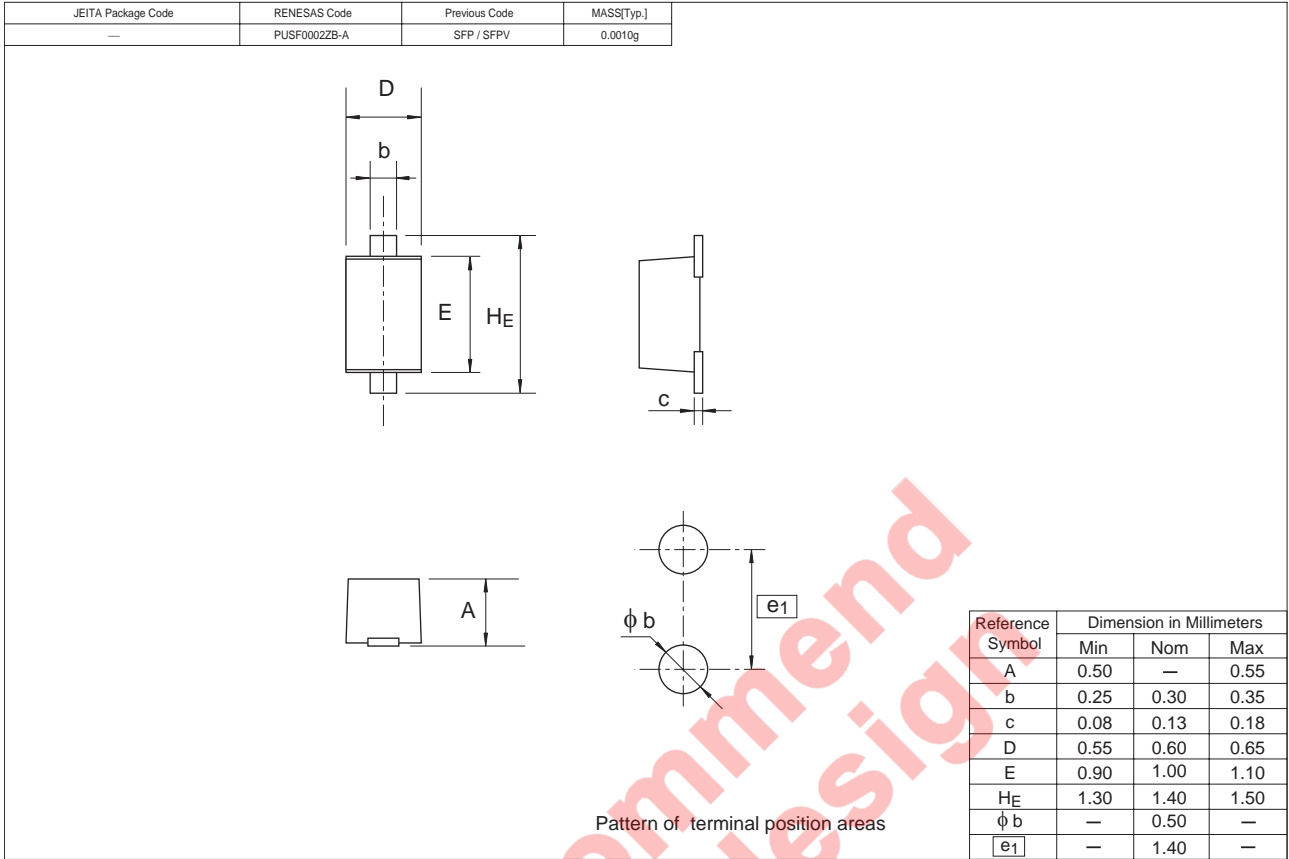


Fig.4 Forward resistance vs. Forward current

Package Dimensions



Not recommended for new designs

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