CYStech Electronics Corp.

Spec. No. : C775LG Issued Date : 2010.03.26 Revised Date :

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Glass Passivated Junction Rectifiers Reverse Voltage 50V to 1000V Forward Current 2.0 Amperes

RL201G thru RL207G



Features

- Low forward voltage drop
- High current capability
- High reliability
- High surge current capability
- The plastic material carries UL recognition 94V-0

Mechanical Characteristics:

- •Case: JEDEC DO-204AC(DO-15) molded plastic
- •Terminals: Tin plated axial leads, solderable per MIL-STD-202E, method 208C
- •Polarity: Color band denotes cathode
- •Mounting position: Any
- •High temperature soldering guaranteed : 250°C/10seconds, 0.375"(9.5mm) lead length at 5 lbs(2.3kg) tension
- •Weight: 0.014oz., 0.39grams

Maximum Ratings and Electrical Characteristics

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

Parameter		Symbol	Туре							11.4
			RL201G	RL202G	RL203G	RL204G	RL205G	RL206G	RL207G	Units
Maximum Repetitive peak reverse voltage		Vrrm	50	100	200	400	600	800	1000	V
Maximum RMS voltage		V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage		V_{DC}	50	100	200	400	600	800	1000	V
Maximum instantaneous forward voltage IF=2A		V_{F}	1.1							V
Maximum average forward rectified current, 0.375"(9.5mm) lead length at T _A =60°C		I _F (AV)	2							A
Peak forward surge current @8.3ms single half sine wave superimposed on rated load (JEDEC method)		Ifsm	55						A	
Maximum DC reverse current	Ta=25°C	т_	5							μΑ
at rated DC blocking voltage	Ta=125°C	Ir	100							
Typical junction capacitance (Note 1)		Сл	30							pF
Typical Thermal Resistance		R _θ JA	40						°C/W	
Storage temperature range		Tstg	- 55 ∼ + 150							$^{\circ}\!\mathbb{C}$
Operating junction temperature range		Тл	-55 ∼ +150							$^{\circ}\!\mathbb{C}$

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0VDC



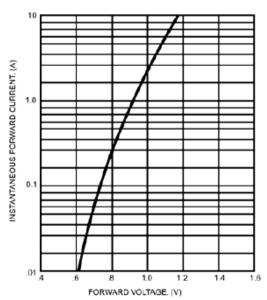
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Ratings and Characteristic Curves

FIG.1- TYPICAL FORWARD CHARACTERISTICS



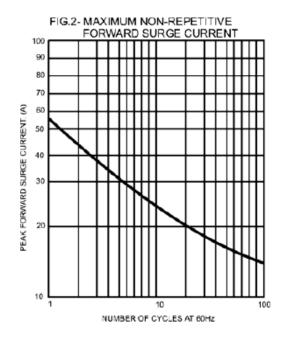


FIG.3- MAXIMUM FORWARD CURRENT DERATING

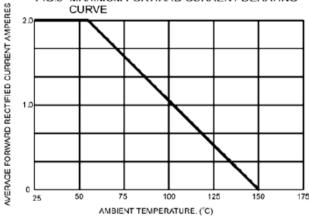
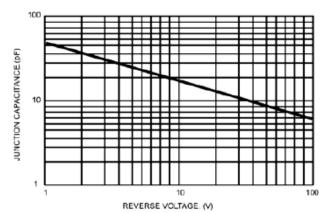


FIG.4- TYPICAL JUNCTION CAPACITANCE



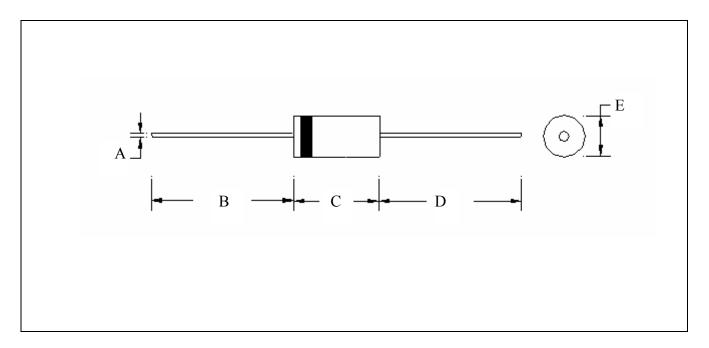


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DO-204AC(DO-15) Dimension



*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.	DIIVI	Min.	Max.	Min.	Max.
Α	φ0.028	φ0.034	φ0.70	φ0.90	D	1.000	-	25.40	-
В	1.000	-	25.40	-	Е	φ0.104	φ0.140	φ2.60	φ3.60
С	0.2300	0.3000	5.80	7.60					

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

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