

	S-CD941	SPECIFICATION		
Issue Date	March 11, 2002			
1. Title LED Outdoor Dot Matrix Display Module □96				
2. Item number RLU 96 D 1632 RG-01				
① ② ③ ④ ⑤ ⑥				
① Series Name				
② Display area: 96x192mm				
③ Included with driver (D)				
④ Dot matrix: 16(Vertical) x 32(Horizontal) dot				
⑤ Emitted color: R(Red)/ G(Green)				
⑥ Specific symbol				
3. Specification				
3a. Absolute Maximum Ratings				
Item	Symbol	Rating	Unit	Remark
Logic Voltage	V _{CC}	-0.3~+6.0	V	
LED Voltage	V _{LED}	-0.3~+3.2	V	
Input Voltage	V _I	-0.3~V _{CC} +0.3	V	
Operating Temp.	T _{opr} 1	-20~+25	°C	100% when illuminated (each color)
	T _{opr} 2	-20~+50	°C	50% when illuminated (each color)
	T _{opr} 3	-20~+65	°C	25% when illuminated (each color)
Storage Temp.	T _{stg}	-25~+85	°C	
Humidity Range		30~90	%RH	
3b. Mechanical Rating				
Item	Rating			Unit
Displayed Color	Red, Yellow Green, Orange (red, yellow and green illuminate same time)			
Dot Dimension	3.0 x 2.5			mm
Dot-to-Dot Pitch	6.0 ± 0.2			mm
Number of dots	16 x 32 (512)			dot
Display area dimension	95.7 x 191.7 ± 0.2			mm
Drive method	1/4 Dynamic illumination			
Number of modules connected	Max 16			Module
Module Weight	180			g
OKAYA Electric America, Inc. 503 Wall St., Valparaiso IN 46383 Phone: 800-852-0122 Fax: 219-477-4856 http://www.okaya.com				

	S-CD941	SPECIFICATION	
Issue Date	March 11, 2002		

3c. Electrical Specifications

Item	Symbol	Rating			Unit	Remark
		MIN.	TYP	MAX		
Logic Voltage	V_{CC}	4.5	5.0	5.5	V	
LED Voltage	V_{LED}	2.8	3.0	3.2	V	
Input Voltage	V_{IL}	0.0	-	1.3	V	
	V_{IH}	3.9	-	VCC		
Input Current	I_{IL}	-	-	1.3	mA	
	I_{IH}	-	-	0.2	μA	
Current Consumption	I_{CC}	-	-	150	mA	
	I_{LED}	-	-	12	A	
Operating Frequency	f_{CLK}	-	-	5	MHz	

3d. Optical Characteristics (Ta=25°C, V_{CC} =5.0V, V_{LED} =3.0V)

Item		Symbol	Rating			Unit	Remark
			MIN	TYP	MAX		
Luminous Intensity		I_V	-	600	-	cd/m ²	*1
Dominant Wavelength	Red	λ_d	-	660	-	nm	
	Yellow, Green		-	570	-		
Half Angle Luminous Intensity	Horizontal	2 θ 1/2	-	30	-	Degree	
	Vertical	2 θ 1/2	-	30	-		
Luminous Intensity (Factory setting)	Red	I_{VR}	240	300	360	cd/m ²	*2
	Yellow, Green	I_{VG}	240	300	360		
Variation of luminous intensity between the dot			within 1.7 times				
Variation of luminous intensity between the unit			within 1.5 times				

*1: Conditions- The intensity with both Red and Green active.

*2: Conditions- Ta=25+3°C, V_{CC} =5.0V, V_{LED} =4.5V Average luminous intensity including non-emitting part immediately after adjustment.

3e. Life Expectancy (Ta=25°C, V_{CC} =5.0V, V_{LED} =3.0V)

Item	Conditions	Rating	Unit
Life expectancy	Red and Green both active Topr=25°C	30,000 Hr. min. (Time to half-life of initial luminous intensity)	Hr



	S-CD941	SPECIFICATION	
Issue Date	March 11, 2002		

4. Description of Signals

4-a. Power Supply (CN1: The side of the main body: 1-353524-8 : AMP)

Item	Terminal Number	Functions	Remark
V _{LED}	1	for logic	
V _{CC}	2 ~ 9	for LED	
GND	10 ~ 18	Power supply ground	

4-b. Input Signal (CN2: The side of the main body: B10B-PH : JST)

Item	Terminal Number	Functions	Remark
A1	1	Row address selection signal	
A0	2		
<u>LATCH</u>	3	Latch signal of display data. Display data at shift register is transferred by low level and latched by high level	
Red Data	4	Display data input of red LED. H: ON, L: OFF (shift from 1 to 128)	
Green Data	5	Display data input of yellow, green LED. H: ON, L: OFF (shift from 1 to 128)	
Shift Clock	6	Data shifting clock of the shift register. Start-up of the clock: ddata shifting by changing from L to H.	
<u>Red Enable</u>	7	Control signal of turning on/off of red LED. H: Turn Off, L: Turn On	
<u>Green Enable</u>	8	Control signal of turning on/off of yellow, green LED. H: Turn Off, L: Turn On	
NC	9		
GND	10	Signal ground (internal connection of power supply ground and signal ground)	

4c. Output Signal (CN3: The side of the main body : B10B-PH : JST)

Output each input signal through buffer. Attaches to the input connector for the next row.

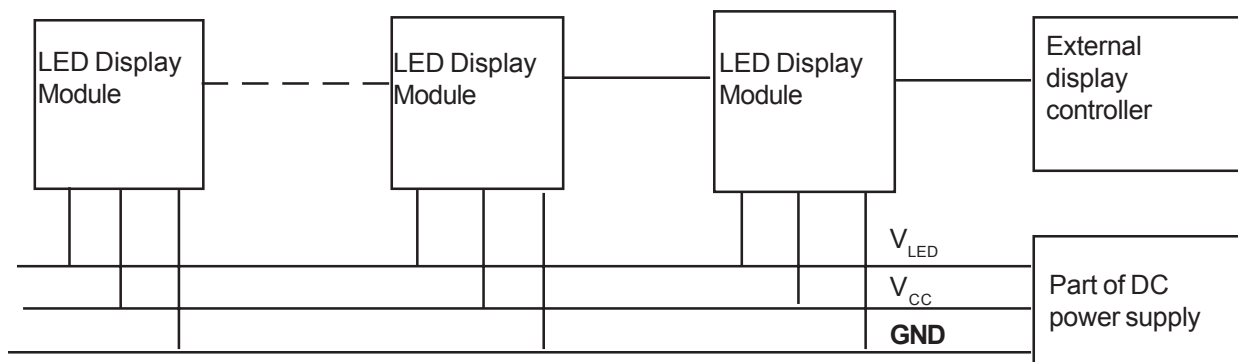
Item	Terminal Number	Functions	Remark
A1	1	Outputs the input signal through buffer	
A0	2	Outputs the input signal through buffer	
<u>Latch</u>	3	Outputs the input signal through buffer	
Red Data	4	Outputs the input signal through buffer 128-bit shift register and buffer	
Green Data	5	Outputs the input signal through buffer 128-bit shift register and buffer	
Shift Clock	6	Outputs the input signal through buffer	
<u>Red Enable</u>	7	Outputs the input signal through buffer	
<u>Green Enable</u>	8	Outputs the input signal through buffer	
NC	9		
GND	10	Signal ground (internal connection of power supply ground and signal ground)	

	S-CD941	SPECIFICATION	
Issue Date	March 11, 2002		

5. Interface

5a. Connection

The display module requires a display controller and DC power supply for proper operation.



5b. Input method of Display Data

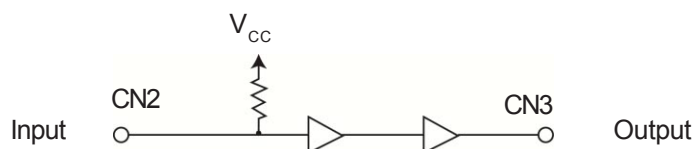
The display data is entered by selecting the red or green data for the 128 pixels starting at the upper right of the screen.

The following table shows display row and row address selection signal (A1, A0).

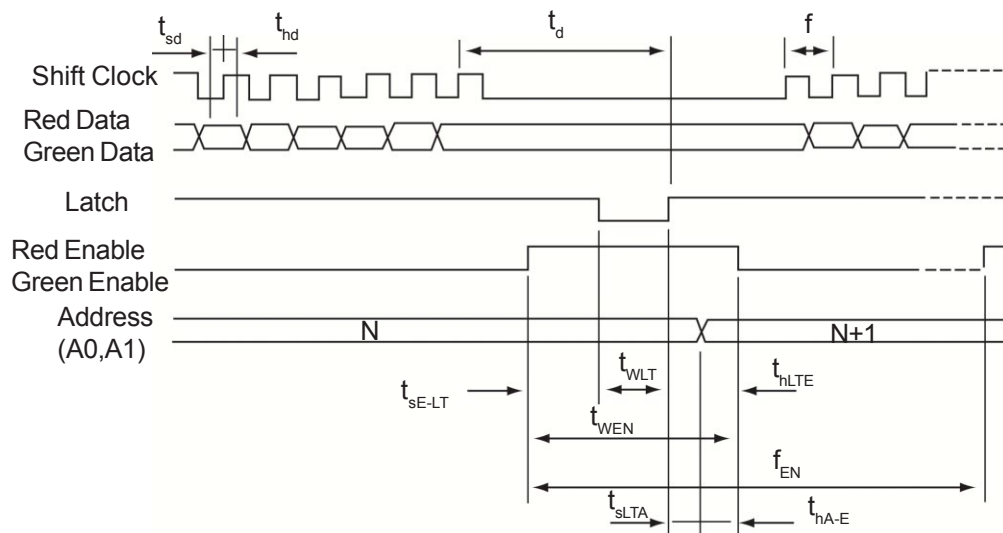
Front look of LED display surface										Row address	
										A1	A0
80	79		66	65	16	15		2	1	0	0
↑	↑		↑	↑	↑	↑		↑	↑	0	1
↑	↑		↑	↑	↑	↑		↑	↑	1	0
↑	↑		↑	↑	↑	↑		↑	↑	1	1
96	95		82	81	32	31		18	17	0	0
↑	↑		↑	↑	↑	↑		↑	↑	0	1
↑	↑		↑	↑	↑	↑		↑	↑	1	0
↑	↑		↑	↑	↑	↑		↑	↑	1	1
112	111		98	97	48	47		34	33	0	0
↑	↑		↑	↑	↑	↑		↑	↑	0	1
↑	↑		↑	↑	↑	↑		↑	↑	1	0
↑	↑		↑	↑	↑	↑		↑	↑	1	1
128	127		114	113	64	63		50	49	0	0
↑	↑		↑	↑	↑	↑		↑	↑	0	1
↑	↑		↑	↑	↑	↑		↑	↑	1	0
↑	↑		↑	↑	↑	↑		↑	↑	1	1

	S-CD941	SPECIFICATION	
Issue Date	March 11, 2002		

5c. Input-output circuit



5d. Timing chart



Item	Symbol	Rating			Unit	Remark
		MIN	TYP	MAX		
Clock Frequency	f	-	-	5	MHz	
Data set-up time	t_{sd}	100	-	-	ns	
Data hold time	t_{hd}	100	-	-	ns	
Clock - latch time	t_d	200	-	-	ns	
Latch pulse width	t_{WLT}	200	-	-	ns	
Enable pulse width	t_{WEN}	11	-	-	μs	
Enable latch width	t_{sE-LT}	200	-	-	ns	
Latch-enable time	t_{hLT-E}	11	-	-	μs	
Latch-address time	t_{SLTA}	500	-	-	ns	
Address-enable time	t_{hA-E}	10	-	-	μs	

6. Handling

6a. Installation

- Securely mount using the specified torque (3 Kg/cm).
- The module spacing should be vertical 96mm and horizontally 192mm.
- To insure proper water seal, care must be taken that where the plate and waterproof packing material are bonded remains free from deformation or penetration of foreign material.
- Make sure that water proof packing material does not stick out of the frame when installing the modules.



	S-CD941	SPECIFICATION	
Issue Date	March 11, 2002		

6b. Heat

Using a large number of modules can cause degradation due to heat radiation. Care should be taken to prevent the temperature over the case's surface from exceeding 65°C. The use of fans or reduction of illumination intensity may be required for heat control. Fig. 1 shows the ratio of illumination to ambient temperature.

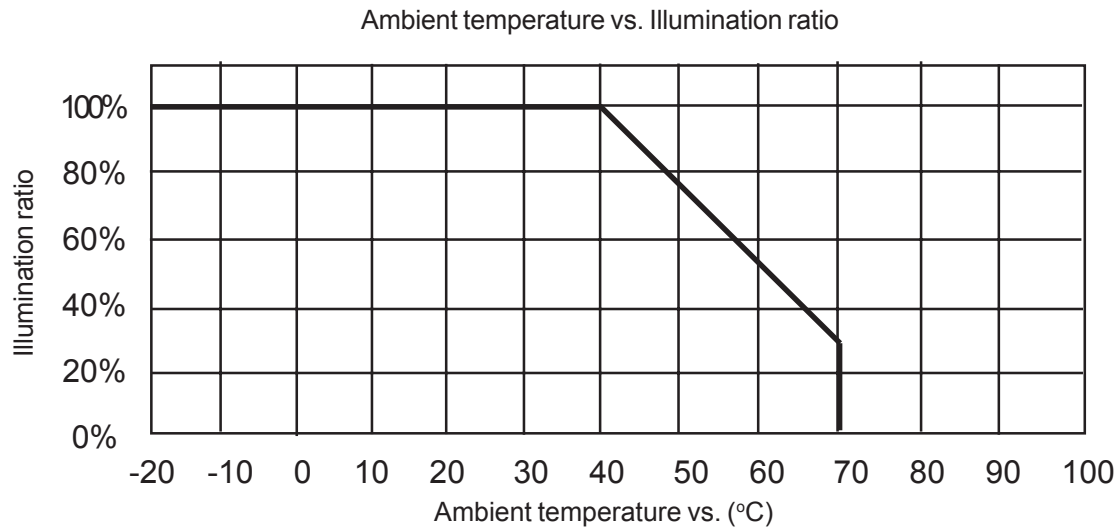


Fig.1

6c. Chemical and abrasion withstand capability

To clean the surface of the module, use a soft cloth and a neutral detergent.

6d. Static

These modules contain static sensitive CMOS-IC circuitry. Antistatic measures must be followed during assembly or testing to prevent damage to the electronics.

6e. Connecting Cable

To make electrical connection to the modules use only the provided or specified connectors. Use extreme caution to prevent damage when attaching or detaching the connectors.

6f. Others

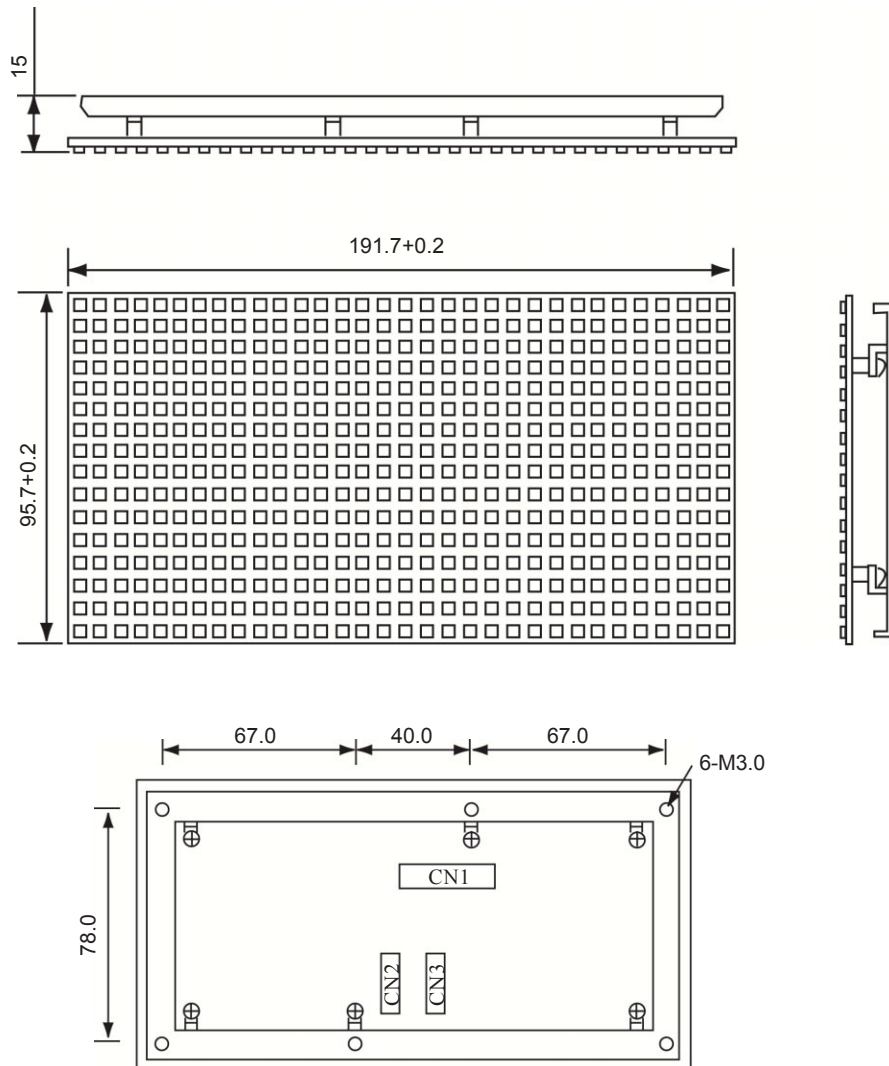
- i. Do not attempt to adjust the pre-set resistor used for luminous intensity adjustment.
- ii. Take care to avoid electromagnetic interface when assembling and installing the modules.
- iii. Be sure to disconnect power when installing or disassembling the module.
- ix. Caution: excessive ambient electrical noise may cause modules to malfunction.

7. Included Parts (refer to the figure 11a and 11b)

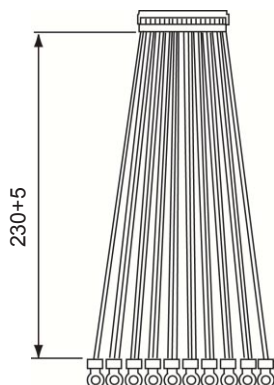
Number		
1	Power cable	1
2	Signal cable	1

	S-CD941	SPECIFICATION	
Issue Date	March 11. 2002		

8. External forms and dimensions

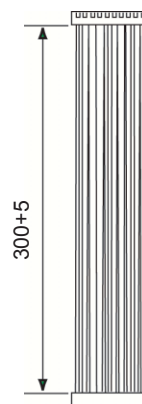


9a. Included Parts1: Power cable



Cable: UL1007 AWG26
Brown 1Pin
Red 2~9Pin
Black 10~18Pin
Contact: 353907-1
353918-1
Housing: VHR-6N×1

9b. Included Parts2: Signal cable



Cable: UL1007 AWG24 White
Contact: SPH-002T-PO 5S(JST)×20
Housing: PHR-10(JST)×2
Connection: 1×1

(Unit: mm)



OKAYA Electric America, Inc. 503 Wall St., Valparaiso IN 46383
Phone: 800-852-0122 Fax: 219-477-4856 <http://www.okaya.com>