



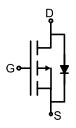
P-Channel Enhancement Mosfet

Feature

• -40V,5A

 $R_{DS (ON)} < 85 \text{m} \Omega @V_{GS} = -10 \text{V}$ TYP: $65 \text{ m} \Omega$ $R_{DS (ON)} < 120 \text{m} \Omega @V_{GS} = -4.5 \text{V}$ TYP: $90 \text{ m} \Omega$

- Advanced Trench Technology
- Lead free product is acquired



Schematic diagram

Application

- Interfacing Switching
- Load Switching
- Power management
- P/N suffix V means AEC-Q101 qualified, e.g:RM5P40S2V
- Halogen-free



SOT-23 top view

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Package code | Quantity |
|----------------|-----------|----------------|-----------|--------------------|---------------|
| 40P05 | RM5P40S2V | SOT23 | 7inch | -T(reel packing) | 3000 pcs/reel |

ABSOLUTE MAXIMUM RATINGS (T₂=25℃ unless otherwise noted)

| Parameter | Symbol | Value | Unit |
|--|------------------|-------------|--------------|
| Drain-Source Voltage | V _{DS} | - 40 | V |
| Gate-Source Voltage | V _G S | ±20 | V |
| Continuous Drain Current (T _a =25℃) | ID | - 5 | А |
| Continuous Drain Current (T _a =70℃) | ID | -3.5 | А |
| Pulsed Drain Current | І _{ОМ} | -20 | A |
| Power Dissipation | PD | 2 | W |
| Thermal Resistance from Junction to Ambient ⁽⁴⁾ | R _{θJA} | `62.5 | °C/W |
| Junction Temperature | TJ | 150 | $^{\circ}$ C |
| Storage Temperature | T _{STG} | -55~ +150 | $^{\circ}$ |

MOSFET ELECTRICAL CHARACTERISTICS(T_a=25℃ unless otherwise noted)

| Parameter | Symbol | Test Condition | Min | Type | Max | Unit | |
|---|----------------------|---|-----|------|------|------|--|
| Static Characteristics | | | | | | | |
| Drain-source breakdown voltage | V _{(BR)DSS} | V _{GS} = 0V, I _D =-250μA | -40 | - | - | V | |
| Zero gate voltage drain current | IDSS | V _{DS} =-40V, V _{GS} = 0V | - | - | 1 | μΑ | |
| Gate-body leakage current | Igss | V _{GS} =±20V,V _{DS} = 0V | - | - | ±100 | nA | |
| Gate threshold voltage ⁽³⁾ | V _{GS(th)} | V _{DS} =V _{GS} , I _D =-250μA | -1 | -1.6 | -2.5 | V | |
| D | Б | V _{GS} =-10V, I _D =-3A | - | 65 | 85 | mΩ | |
| Drain-source on-resistance ⁽³⁾ | R _{DS(on)} | V _{GS} =-4.5V, I _D =-2A | - | 90 | 120 | | |
| Dynamic characteristics | · | | | | | | |
| Input Capacitance | C _{iss} | | - | 596 | - | pF | |
| Output Capacitance | Coss | V _{DS} =-20V, V _{GS} =0V, f =1MHz | - | 90 | - | | |
| Reverse Transfer Capacitance | C _{rss} | - | - | 70 | - | | |
| Switching characteristics | | | | | | | |
| Turn-on delay time | t _{d(on)} | | - | 9 | - | | |
| Turn-on rise time | t _r | V _{DD} =-20V, I _D =-3A, | - | 8 | - | ns | |
| Turn-off delay time | t _{d(off)} | V_{GS} =-10V, R_G =3 Ω | - | 28 | - | | |
| Turn-off fall time | t _f | - | - | 10 | - | | |
| Total Gate Charge | Qg | \/D0= 00\/ ID= 0A | - | 14 | - | | |
| Gate-Source Charge | Qgs | VDS=-20V, ID=-3A, VGS=-10V | - | 2.9 | - | nC | |
| Gate-Drain Charge | Qgd | 7 VGS10V | - | 3.8 | - | | |
| Source-Drain Diode characteristics | | | | | | | |
| Diode Forward voltage ⁽³⁾ | V _{DS} | V _{GS} =0V, I _S =-3A | - | - | 1.2 | V | |
| Diode Forward current ⁽⁴⁾ | ls | | - | _ | -4.0 | Α | |

Notes:

- 1. Repetitive Rating: pulse width limited by maximum junction temperature
- 2. Pulse Test: pulse width≤300µs, duty cycle≤2%
- 3. Surface Mounted on FR4 Board,t≤10 sec



Test Circuit

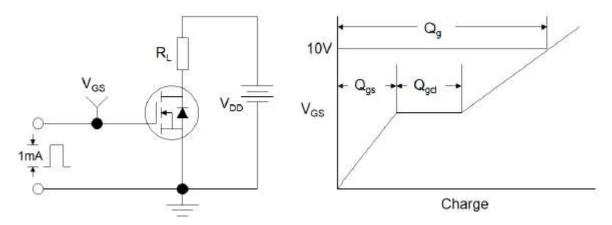


Figure1:Gate Charge Test Circuit & Waveform

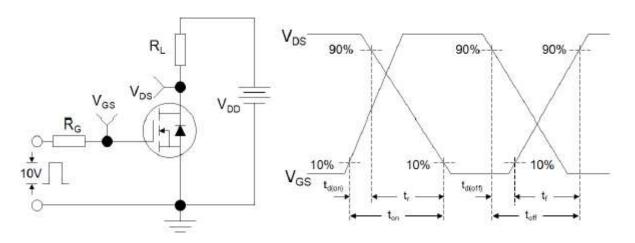


Figure 2: Resistive Switching Test Circuit & Waveforms

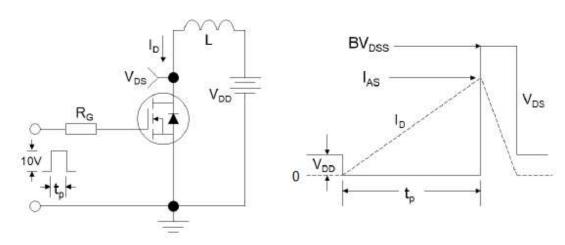


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



RATING AND CHARACTERISTICS CURVES (RM5P40S2V)

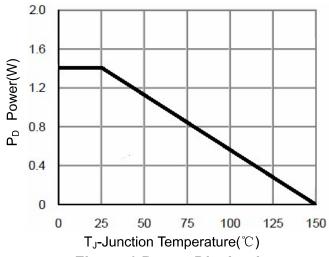


Figure 1 Power Dissipation

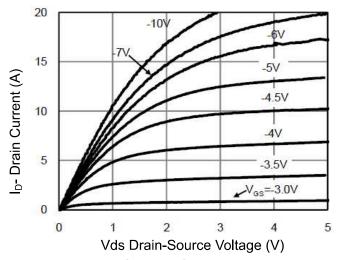


Figure 3 Output Characteristics

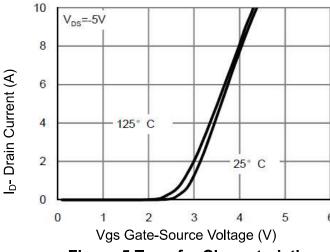


Figure 5 Transfer Characteristics

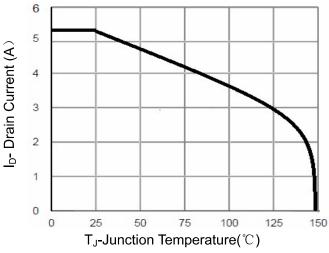


Figure 2 Drain Current

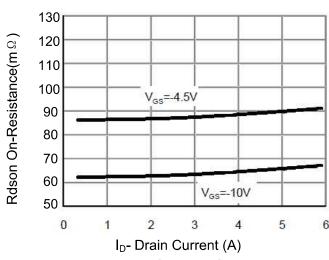


Figure 4 Drain-Source On-Resistance

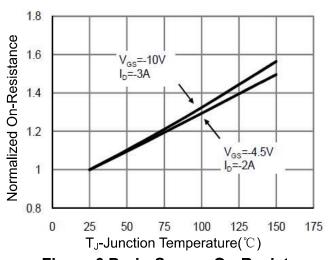
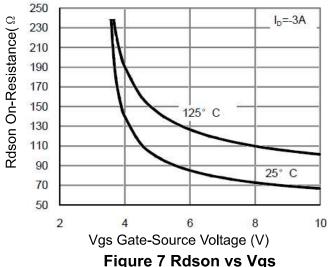


Figure 6 Drain-Source On-Resistance



RATING AND CHARACTERISTICS CURVES (RM5P40S2V)



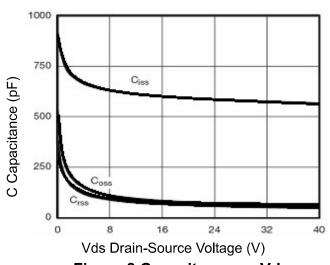
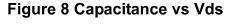
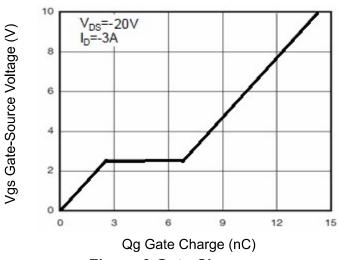


Figure 7 Rdson vs Vgs





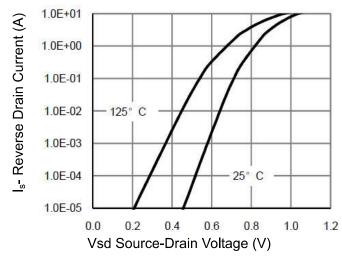


Figure 9 Gate Charge

Figure 10 Source- Drain Diode Forward

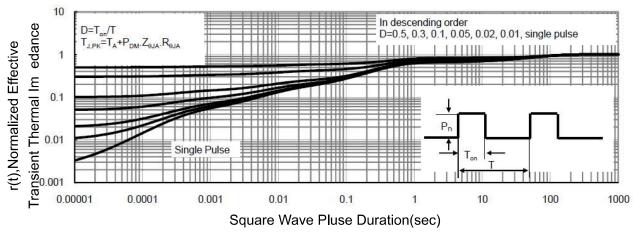
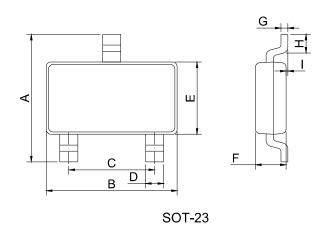


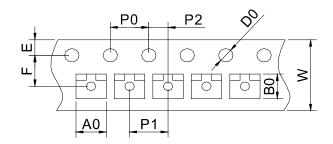
Figure 11 Normalized Maximum Transient Thermal Impedance

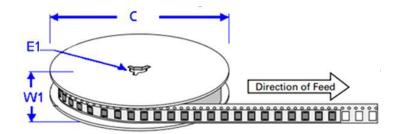
SOT-23 Package Information



| Dimensions | | | | | | |
|------------|-------------|------|------|-----------|--------|-------|
| Ref. | Millimeters | | | | Inches | |
| | Min. | Тур. | Max. | Min. | Тур. | Max. |
| Α | 2.30 | 2.40 | 2.50 | 0.091 | 0.095 | 0.098 |
| В | 2.80 | 2.90 | 3.00 | 0.110 | 0.114 | 0.118 |
| С | 1.90 REF | | | 0.075 REF | | |
| D | 0.35 | 0.40 | 0.45 | 0.014 | 0.016 | 0.018 |
| Е | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 |
| F | 0.90 | 1.00 | 1.10 | 0.035 | 0.039 | 0.043 |
| G | | 0.10 | 0.15 | | 0.004 | 0.006 |
| Н | 0.20 | | | 0.008 | | |
| I | 0 | | 0.10 | 0 | | 0.004 |

Package Information-SOT-23





| | D: | Dimensions | | | |
|------|-------------|---------------|--|--|--|
| Ref. | Dimensions | | | | |
| | Millimeters | Inches | | | |
| A0 | 3.15 ± 0.3 | 0.124 ± 0.012 | | | |
| В0 | 2.77 ± 0.3 | 0.109 ± 0.012 | | | |
| С | 178 | 7.0 | | | |
| D0 | 1.50±0.1 | 0.059 ± 0.004 | | | |
| Е | 1.75 ± 0.2 | 0.069 ± 0.008 | | | |
| E1 | 13.3±0.3 | 0.524± 0.012 | | | |
| F | 3.5 ± 0.2 | 0.138 ± 0.008 | | | |
| P0 | 4.00 ± 0.2 | 0.157 ± 0.008 | | | |
| P1 | 4.00 ± 0.2 | 0.157 ± 0.008 | | | |
| P2 | 2.00 ± 0.2 | 0.079 ± 0.008 | | | |
| W | 8.00 ± 0.2 | 0.315 ± 0.008 | | | |
| W1 | 11.5±1.0 | 0.453 ± 0.039 | | | |



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