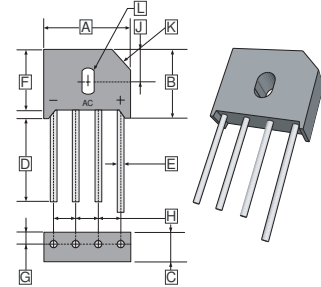


RoHS Compliant Product  
 A suffix of "-C" specifies halogen & lead-free

**KBU**

**FEATURES**

- Surge overload rating – 175 amperes peak
- Ideal for printed circuit board
- Reliable low cost construction utilizing molded plastic technique
- Plastic material has UL
- Mounting position: Any
- Mounting torque: 5 In.lb.Max



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	22.70	23.70	G	1.80	2.20
B	18.80	19.80	H	4.60	5.60
C	6.50	7.00	J	7.50 REF	
D	25.40	-	K	4.0 x 45°	
E	1.20 Ø TYP		L	3.8 Ø x 5.7L	
F	16.80	17.80			

**MAXIMUM RATINGS**

(T<sub>A</sub>=25°C unless otherwise specified)

(Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

PARAMETER	SYMBOL	PART NUMBERS							UNIT
		RS 601L	RS 602L	RS 603L	RS 604L	RS 605L	RS 606L	RS 607L	
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Output Current at T <sub>C</sub> =100°C	I <sub>(AV)</sub>	6.0							A
Peak Forward Surge Current, 8.3ms single half sine-wave superimposed on rated load (JEDEC methode)	I <sub>FSM</sub>	175							A
Maximum Instantaneous Forward Voltage Drop Per Element @ 3.0A	V <sub>F</sub>	1.0							V
Maximum Reverse Current @ Rated DC Blocking Voltage Per Element	T <sub>J</sub> =25°C	10							µA
	T <sub>J</sub> =100°C	200							
Typical Junction Capacitance Per Element <sup>1</sup>	C <sub>J</sub>	260							pF
Operating & Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55~125, -55~150							°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

**RATINGS AND CHARACTERISTIC CURVES**

FIG.1-DERATING CURVE FOR OUTPUT RECTIFIED CURRENT

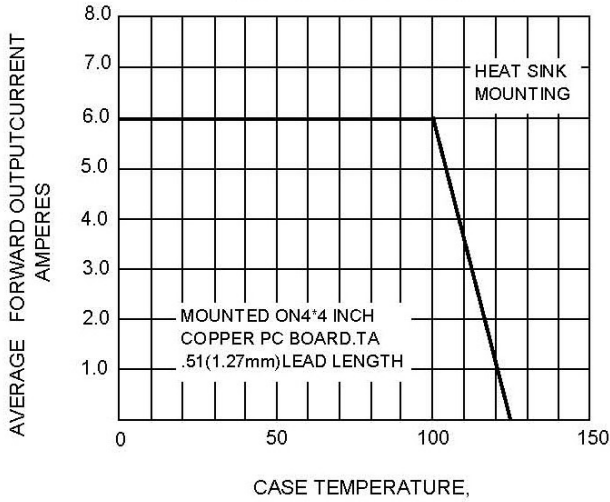


FIG.2 TYPICAL INSTANTANEOUS FORWARD CHARACTERISTIC

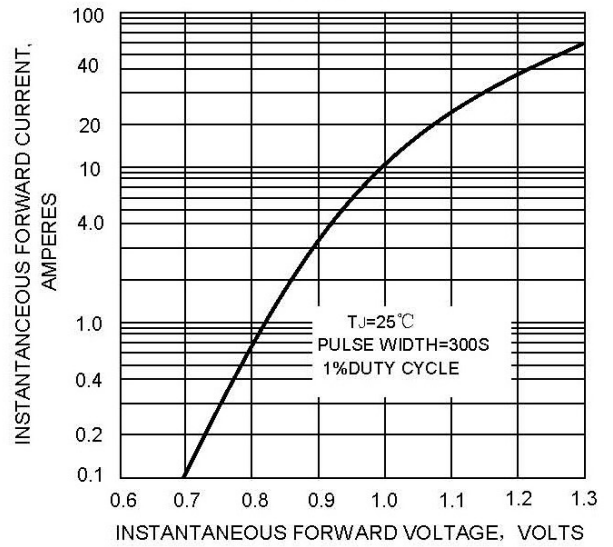


FIG.3-MAXIMUM NON-RETTITIVE PEAK FORWARD SURGE CURRENT

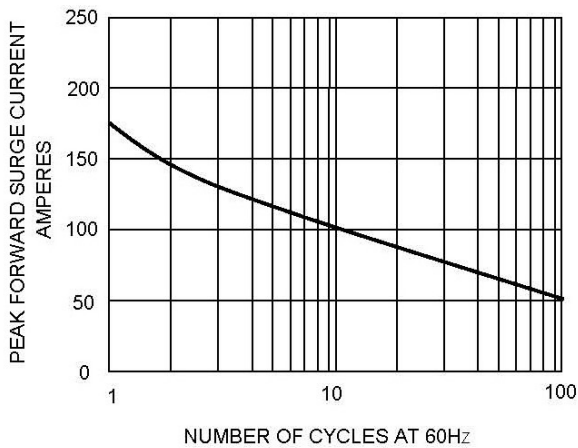


FIG.4-TYPICAL REVERSE CHARACTERISTICS

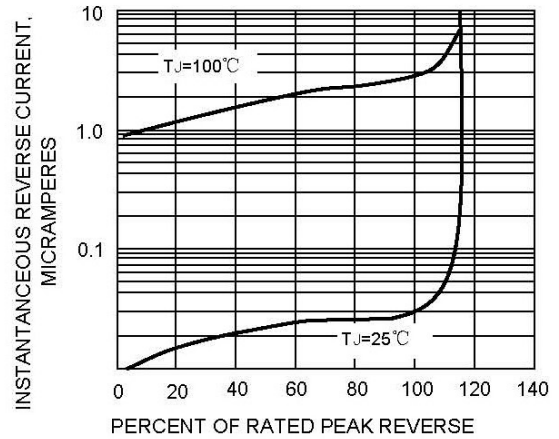


FIG.5-TYPICAL JUNCTION CAPACITANCE PER ELEMENT

