# **Transistors**

# 4V Drive Pch MOS FET RSF010P03

#### Structure

Silicon P-channel MOS FET

### ● Features

- 1) Low on-resistance.
- 2) High speed switching.

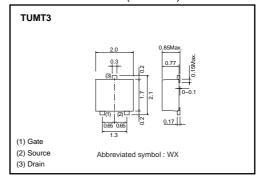
### Applications

Switching

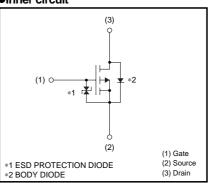
# Packaging specifications

|           | Package                      | Taping |  |
|-----------|------------------------------|--------|--|
| Туре      | Code                         | TL     |  |
|           | Basic ordering unit (pieces) | 3000   |  |
| RSF010P03 | 0                            |        |  |

# ●External dimensions (Unit : mm)



# •Inner circuit



# ●Absolute maximum ratings (Ta=25°C)

| Parameter                    |            | Symbol            | Limits      | Unit |
|------------------------------|------------|-------------------|-------------|------|
| Drain-source voltage         |            | VDSS              | -30         | V    |
| Gate-source voltage          |            | Vgss              | ±20         | V    |
| Drain current                | Continuous | lσ                | ±1          | Α    |
| Drain current                | Pulsed     | IDP *1            | ±4          | Α    |
| Source current               | Continuous | Is                | -0.3        | Α    |
| (Body diode)                 | Pulsed     | Isp *1            | -4          | Α    |
| Total power dissipation      |            | P <sub>D</sub> *2 | 0.8         | W    |
| Channel temperature          |            | Tch               | 150         | °C   |
| Range of storage temperature |            | Tstg              | -55 to +150 | °C   |

<sup>\*1</sup> Pw≤10μs, Duty cycle≤1% \*2 Mounted on a ceramic board

### ●Thermal resistance

| Parameter          | Symbol     | Limits | Unit |
|--------------------|------------|--------|------|
| Channel to ambient | Rth(ch-a)* | 156    | °C/W |

<sup>\*</sup> Mounted on a ceramic board

# ●Electrical characteristics (Ta=25°C)

| Parameter                               | Symbol                 | Min. | Тур. | Max. | Unit | Conditions                                      |
|---|------------------------|------|------|------|------|---|
| Gate-source leakage                     | Igss                   | -    | _    | ±10  | μΑ   | Vgs= ±20V, Vps=0V                               |
| Drain-source breakdown voltage          | V <sub>(BR) DSS</sub>  | -30  | _    | _    | V    | $I_D = -1 \text{mA}, V_{GS} = 0 \text{V}$       |
| Zero gate voltage drain current         | IDSS                   | -    | _    | -1   | μΑ   | V <sub>DS</sub> = -30V, V <sub>GS</sub> =0V     |
| Gate threshold voltage                  | V <sub>GS (th)</sub>   | -1.0 | _    | -2.5 | V    | $V_{DS}$ = -10V, $I_{D}$ = -1mA                 |
|   |                        | _    | 250  | 350  | mΩ   | I <sub>D</sub> = -1A, V <sub>G</sub> S= -10V    |
| Static drain-source on-state resistance | R <sub>DS (on)</sub> * | _    | 400  | 560  | mΩ   | I <sub>D</sub> = -0.5A, V <sub>G</sub> S= -4.5V |
| resistance                              |                        | _    | 450  | 630  | mΩ   | I <sub>D</sub> = -0.5A, V <sub>G</sub> S= -4.0V |
| Forward transfer admittance             | Y <sub>fs</sub>   *    | 0.5  | _    | _    | S    | $V_{DS} = -10V, I_{D} = -0.5A$                  |
| Input capacitance                       | Ciss                   | _    | 120  | _    | pF   | V <sub>DS</sub> = -10V                          |
| Output capacitance                      | Coss                   | _    | 27   | _    | pF   | Vgs=0V  |
| Reverse transfer capacitance            | Crss                   | _    | 17   | _    | pF   | f=1MHz  |
| Turn-on delay time                      | t <sub>d (on)</sub> *  | _    | 8    | _    | ns   | V <sub>DD</sub> ≒ −15V                          |
| Rise time                               | tr *                   | -    | 11   | _    | ns   | ID= -0.5A                                       |
| Turn-off delay time                     | t <sub>d (off)</sub> * | _    | 20   | _    | ns   | V <sub>GS</sub> = −10V<br>R <sub>L</sub> =30Ω   |
| Fall time                               | t <sub>f</sub> *       | -    | 12   | _    | ns   | R <sub>G</sub> =10Ω                             |
| Total gate charge                       | Qg                     | -    | 1.9  | -    | nC   | V <sub>DD</sub> ≒-15V, V <sub>GS</sub> =-5V     |
| Gate-source charge                      | Qgs                    | -    | 0.7  | -    | nC   | I <sub>D</sub> = -1A                            |
| Gate-drain charge                       | Q <sub>gd</sub>        | _    | 0.4  | _    | nC   | $R_L=15\Omega$ , $R_G=10\Omega$                 |

\*Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

| Parameter       | Symbol | Min. | Тур. | Max. | Unit | Conditions                                  |
|-----------------|--------|------|------|------|------|---|
| Forward voltage | Vsp    | -    | _    | -1.2 | V    | I <sub>S</sub> = -0.3A, V <sub>GS</sub> =0V |

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