

10V Drive Nch MOSFET

RSJ400N06

Structure

Silicon N-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) High current
- 3) High power Package

Application

Switching

Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	1000
RSJ400N0	0	

●Absolute maximum ratings (T_a = 25°C)

Drain-source voltage V _{DSS}	60	V
Gate-source voltage V _{GSS}	±20	V
Drain current Continuous I _D	±40	Α
Pulsed I _{DP} *1	±80	Α
Source current Continuous I _S	40	Α
(Body Diode) Pulsed I _{SP} *1	80	Α
Power dissipation P _D *2	50	W
Channel temperature Tch	150	°C
Range of storage temperature Tstg -	55 to +150	°C

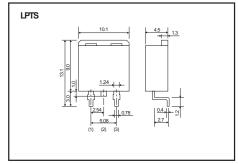
^{*1} Pw≤10µs, Duty cycle≤1%

Thermal resistance

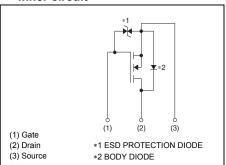
Parameter	Symbol	Limits	Unit
Channel to Case	R _{th (ch-c)} *	2.5	°C / W

^{*} T_c=25°C

• Dimensions (Unit : mm)



• Inner circuit



^{*2} T_c=25°C

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	-	-	±10	μA	V_{GS} =±20V, V_{DS} =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	60	1	1	>	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	1	1	μA	V _{DS} =60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	-	3.0	٧	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS (on)}	-	11	16	mΩ	I _D =40A, V _{GS} =10V
Forward transfer admittance	IY _{fs} I*	14	1	1	S	I _D =20A, V _{DS} =10V
Input capacitance	C _{iss}	1	2400	1	pF	V _{DS} =10V
Output capacitance	C _{oss}	1	490	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	250	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	1	20	-	ns	I _D =20A, V _{DD} ≒ 30V
Rise time	t _r *	1	60	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	1	90	-	ns	R_L =1.5 Ω
Fall time	t _f *	1	140	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	-	52	-	nC	V _{DD} ≒ 30V
Gate-source charge	Q _{gs} *	-	8	-	nC	I _D =40A,
Gate-drain charge	Q _{gd} *	-	15	-	nC	V _{GS} =10V

^{*}Pulsed

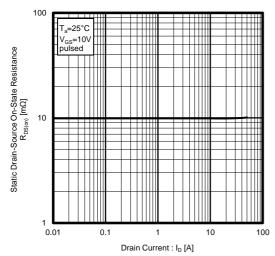
●Body diode characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.2	V	I _s =40A, V _{GS} =0V

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)

Fig.1 Static Drain-Source On-State Resistance vs. Drain Current



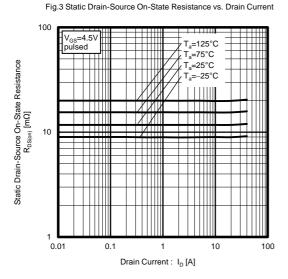


Fig.5 Forward Transfer Admittance vs. Drain Current

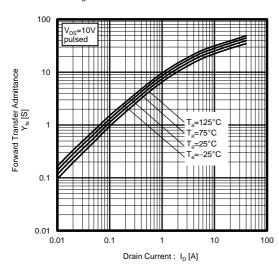


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

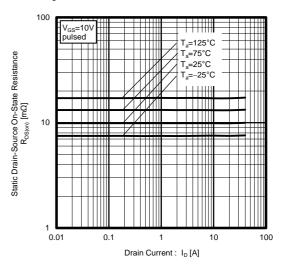


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

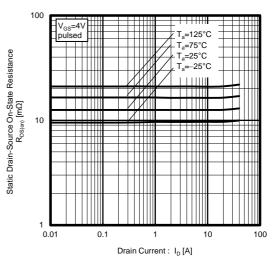
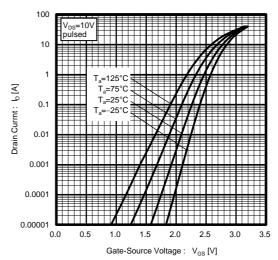
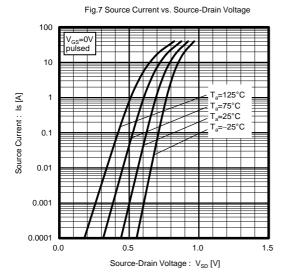
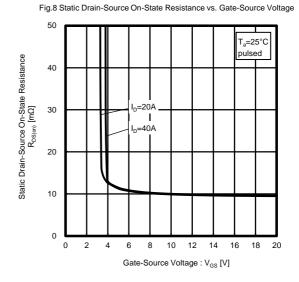
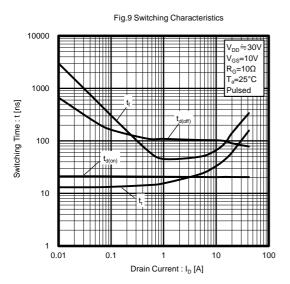


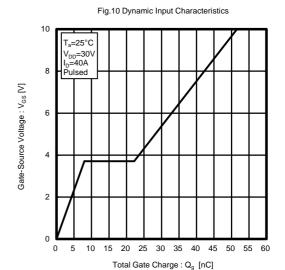
Fig.6 Typical Transfer Characteristics

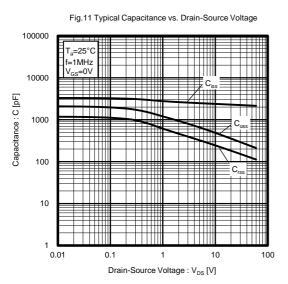




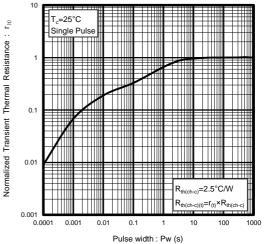












Measurement circuits

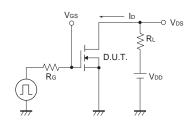


Fig.1-1 Switching Time Measurement Circuit

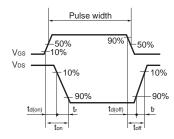


Fig.1-2 Switching Waveforms

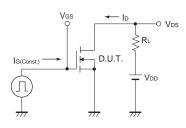


Fig.2-1 Gate Charge Measurement Circuit

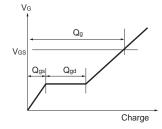


Fig.2-2 Gate Charge Waveform

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