## RT1P440X SERIES

**(Transistor)** 

UNIT: mm

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

### **DESCRIPTION**

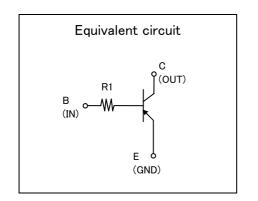
RT1P440X is a one chip transistor with built-in bias resistor, NPN type is RT1N440X.

### **FEATURE**

•Built-in bias resistor (R1=47k $\Omega$ ).

## **APPLICATION**

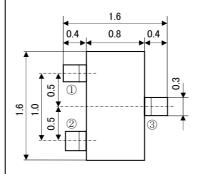
Inverted circuit, switching circuit, interface circuit, driver circuit.



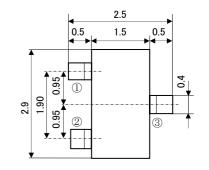
RT1P440T2

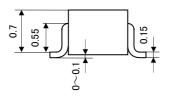
#### OUTLINE DRAWING

RT1P440C



RT1P440U





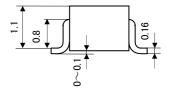
JEITA: -JEDEC: -

Terminal Connector

①:Base

2: Emitter

3: Collector



JEITA: SC-59

JEDEC: Similar to TO-236

**Terminal Connector** 

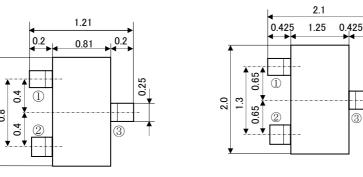
1:Base

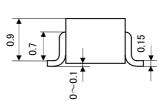
2: Emitter

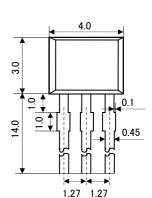
3: Collector

RT1P440M









JEITA: SC-70 JEDEC: -

**Terminal Connector** 

(1):Base

2: Emitter

3: Collector

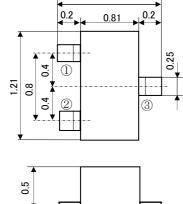


**Terminal Connector** 

(1): Emitter

2: Collector

3:Base



JEITA, JEDEC: -ISAHAYA: T-USM **Terminal Connector** 

- 1:Base
- 2: Emitter
- 3: Collector

# RT1P440X SERIES

**(Transistor)** 

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

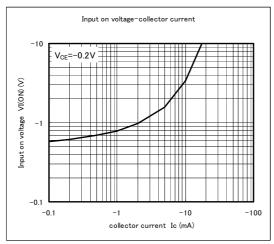
## MAXIMUM RATING (Ta=25°C)

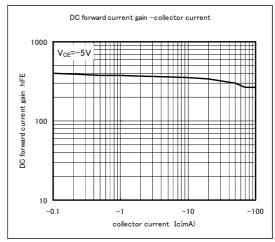
SYMBOL	PARAMETER	RATING					UNIT
		RT1P440T2	RT1P440U	RT1P440M	RT1P440C	RT1P440S	OINLI
V <sub>CBO</sub>	Collector to Base voltage	<b>−50</b>					
$V_{EBO}$	Emitter to Base voltage	-6					
$V_{CEO}$	Collector to Emitter voltage	-50					
Ic	Collector current	-100					
I <sub>CM</sub>	Peak Collector current	-200					
P <sub>c</sub>	Collector dissipation(Ta=25°C)	125(※)	150	200		450	mW
Tj	Junction temperature	+125 +150					°C
Tstg	Storage temperature	<b>−55∼+125 −55∼+150</b>					°C

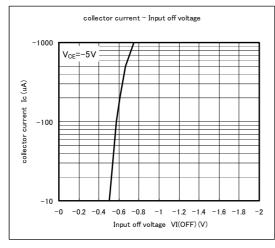
## **ELECTRICAL CHARACTERISTICS (Ta=25°C)** (%) package mounted on 9mm×19mm×1mm glass-epoxy substrate.

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
		TEST CONDITION	MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$	C to E break down voltage	I <sub>C</sub> =-100 μ A, R <sub>BE</sub> =∞	-50			V
I <sub>CBO</sub>	Collector cut off current	$V_{CB}$ =-50V, I $_{E}$ =0			-0.1	μΑ
$h_{FE}$	DC forward current gain	$V_{CE}$ =-5V, $I_{C}$ =-1mA	100			ı
$V_{CE(sat)}$	C to E saturation voltage	$I_{C} = -10 \text{mA}, I_{B} = -0.5 \text{mA}$			-0.3	٧
R <sub>1</sub>	Input resistance		33	47	61	kΩ
$f_T$	Gain band width product	$V_{CE}$ =-6V, $I_{E}$ =10mA		150		MHz

#### TYPICAL CHARACTERISTICS









Marketing division, Marketing planning department 6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

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