# RT1P44HX SERIES

〈Transistor〉 Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

OUTLINE DRAWING

UNIT:mm

## DESCRIPTION

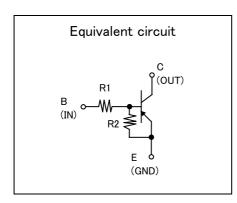
RT1P44HX is a one chip transistor with built-in bias resistor,NPN type is RT1N44HX.

### FEATURE

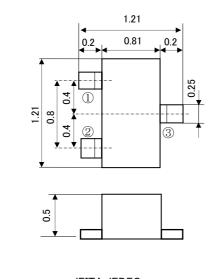
•Built-in bias resistor (R1=47k $\Omega$ ,R2=22k $\Omega$ ).



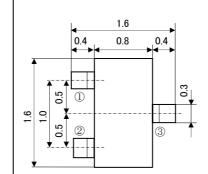
Inverted circuit, switching circuit, interface circuit, driver circuit.







JEITA, JEDEC : -ISAHAYA : T-USM Terminal Connector ① : Base ② : Emitter ③ : Collector



0.1

JEITA: -

JEDEC: -

Terminal Connector

①:Base

(2): Emitter

3: Collector

RT1P44HM

2.1

1.25

0.425

0.3

0.425

1

 $0 \sim 0.1$ 

JEITA: SC-70

JEDEC: -

**Terminal Connector** 

(1):Base

2: Emitter

3: Collector

0.65

0.65

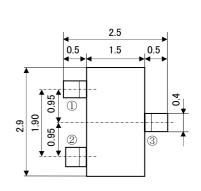
2.0

0.9

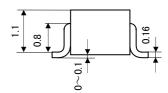
6

0.7

RT1P44HU



RT1P44HC

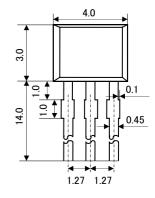


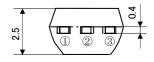
JEITA: SC-59 JEDEC: Similar to TO-236

**Terminal Connector** 

- ①:Base
- 2:Emitter
- 3: Collector







JEITA: --JEDEC: --Terminal Connector ①: Emitter ②: Collector ③: Base

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ISAHAYA ELECTRONICS CORPORATION

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**(Transistor)** 

Transistor With Resistor

For Switching Application

Silicon PNP Epitaxial Type

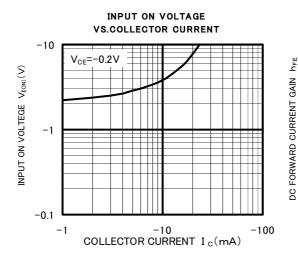
# MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING					UNIT
		RT1P44HT2	RT1P44HU	RT1P44HM	RT1P44HC	RT1P44HS	UNIT
V <sub>CBO</sub>	Collector to Base voltage	-50					V
V <sub>EBO</sub>	Emitter to Base voltage	-10					V
V <sub>CEO</sub>	Collector to Emitter voltage	-50				V	
Ι <sub>c</sub>	Collector current	-100					mA
I <sub>CM</sub>	Peak Collector current	-200				mA	
Pc	Collector dissipation(Ta=25°C)	125(※)	150	20	0	450	mW
Tj	Junction temperature	+125 +150			°C		
Tstg	Storage temperature	-55~+125 -55~+150			°C		

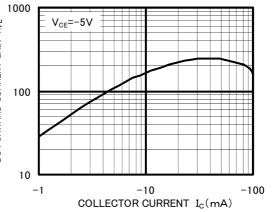
ELECTRICAL CHARACTERISTICS (Ta=25°C) (X) package mounted on 9mm×19mm×1mm glass-epoxy substrate.

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
		TEST CONDITION	MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$	C to E break down voltage	I <sub>c</sub> =−100 μ A, R <sub>BE</sub> =∞	-50			V
I <sub>CBO</sub>	Collector cut off current	V <sub>CB</sub> =-50V, I <sub>E</sub> =0			-0.1	μA
h <sub>FE</sub>	DC forward current gain	V <sub>CE</sub> =-5V, I <sub>c</sub> =-5mA	56			-
$V_{CE(sat)}$	C to E saturation voltage	I <sub>c</sub> =–10mA, I <sub>B</sub> =–0.5mA			-0.3	V
V <sub>I(ON)</sub>	Input on voltage	V <sub>CE</sub> =-0.2V, I <sub>C</sub> =-5mA		-2.6	-6.3	V
V <sub>I(OFF)</sub>	Input off voltage	$V_{ce}$ =-5V, I <sub>c</sub> =-100 $\mu$ A	-1.3	-1.7		V
R <sub>1</sub>	Input resistance		33	47	61	kΩ
$R_2 / R_1$	Resistance ratio		0.37	0.47	0.57	
$f_{T}$	Gain band width product	V <sub>ce</sub> =-6V, I <sub>e</sub> =10mA		150		MHz

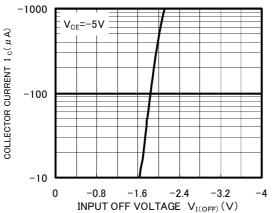
## **TYPICAL CHARACTERISTICS**







COLLECTOR CURRENT VS.INPUT OFF VOLTAGE



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