# RT2N12M

COMPOSITE TRANSISTOR WITH RESISTOR FOR SWITCHING APPLICATION SILICON NPN EPITAXIAL TYPE

## **DESCRIPTION**

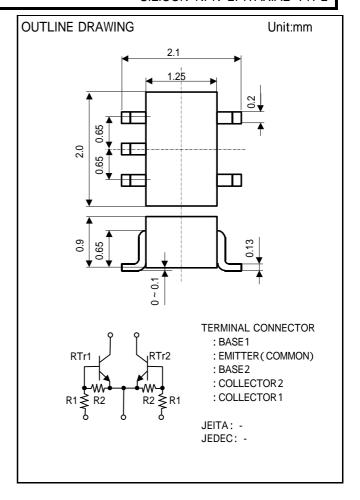
RT2N12M is a composite transistor with built-in bias resistor

### **FEATURE**

Built-in bias resistor ( R1=4.7~K , R2=47K ) Mini package for easy mounting

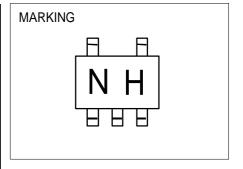
## **APPLICATION**

Inverted circuit , switching circuit , interface circuit , driver circuit



# MAXIMUM RATINGS (Ta=25 )(RTr1, RTr2)

Symbol	Parameter	Ratings	Unit
$V_{CBO}$	Collector to Base voltage	50	V
$V_{EBO}$	Emitter to Base voltage	6	٧
$V_{CEO}$	Collector to Emitter voltage	50	>
I <sub>c</sub>	Collector current	100	mA
I <sub>CM</sub>	Peak Collector current	200	mA
Pc	Collector dissipation(Total Ta=25 )	150	mW
T <sub>j</sub>	Junction temperature	+ 150	
T <sub>stg</sub>	Storage temperature	-55 ~ + 150	



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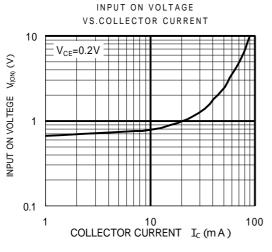
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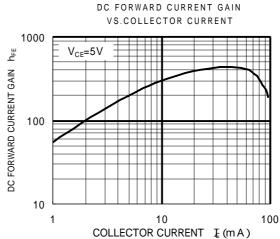
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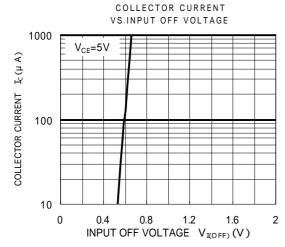
## ELECTRICAL CHARACTERISTICS (Ta=25 )(RTr1, RTr2)

Symbol	Parameter	Test conditions	Limits			l lait
			Min	Тур	Max	Unit
V <sub>(BR)CEO</sub>	Collector to Emitter break down voltage	I <sub>C</sub> =100 μ A , R <sub>BE</sub> =	50	-	-	V
I <sub>CBO</sub>	Collector cut off current	$V_{CB}$ =50V , I $_{E}$ =0mA	-	-	0.1	μA
h <sub>FE</sub>	DC forward current gain	V <sub>CE</sub> =5V , I <sub>C</sub> =10mA	80	-	-	-
V <sub>CE(sat)</sub>	Collector to Emitter saturation voltage	I <sub>C</sub> =10mA , I <sub>B</sub> =0.5mA	-	0.1	0.3	V
V <sub>I(ON)</sub>	Input on voltage	$V_{\text{CE}}$ =0.2V , I $_{\text{C}}$ =5mA	-	0.8	1.4	V
$V_{I(OFF)}$	Input off voltage	$V_{CE}$ =5V , I $_{C}$ =100 $\mu$ A	0.5	0.7	-	V
R <sub>1</sub>	Input resistor		3.3	4.7	6.1	K
$R_2/R_1$	Resistor ratio		8	10	12	-
f <sub>T</sub>	Gain band width product	V <sub>CE</sub> =6V , I <sub>E</sub> =-10mA	-	200	-	MHz

TYPICAL CHARACTRISTICS (Tr1, Tr2)









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