

RT3TSSM

Composite Transistor With Resistor
For Switching Application
Silicon Epitaxial Type

DESCRIPTION

RT3TSSM is a composite transistor built with RT1N150 chip and RT1P150 chip in SC-88 package.

FEATURE

Silicon epitaxial type
Each transistor elements are independent.
Mini package for easy mounting

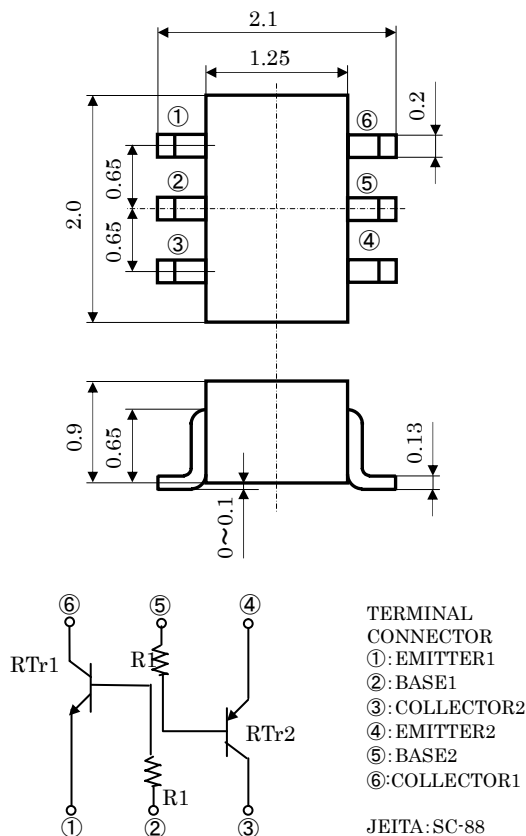
APPLICATION

Inverted circuit, switching circuit,
interface circuit, driver circuit

※PNP built in transistor of “-” sign is abbreviation.

OUTLINE DRAWING

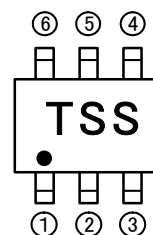
Unit: mm



MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
IC	Collector current	100	mA
ICM	Peak Collector current	200	mA
PC	Collector dissipation (Total, Ta=25°C)	150	mW
Tj	Junction temperature	+150	°C
Tstg	Storage temperature	-55~+150	°C

MARKING



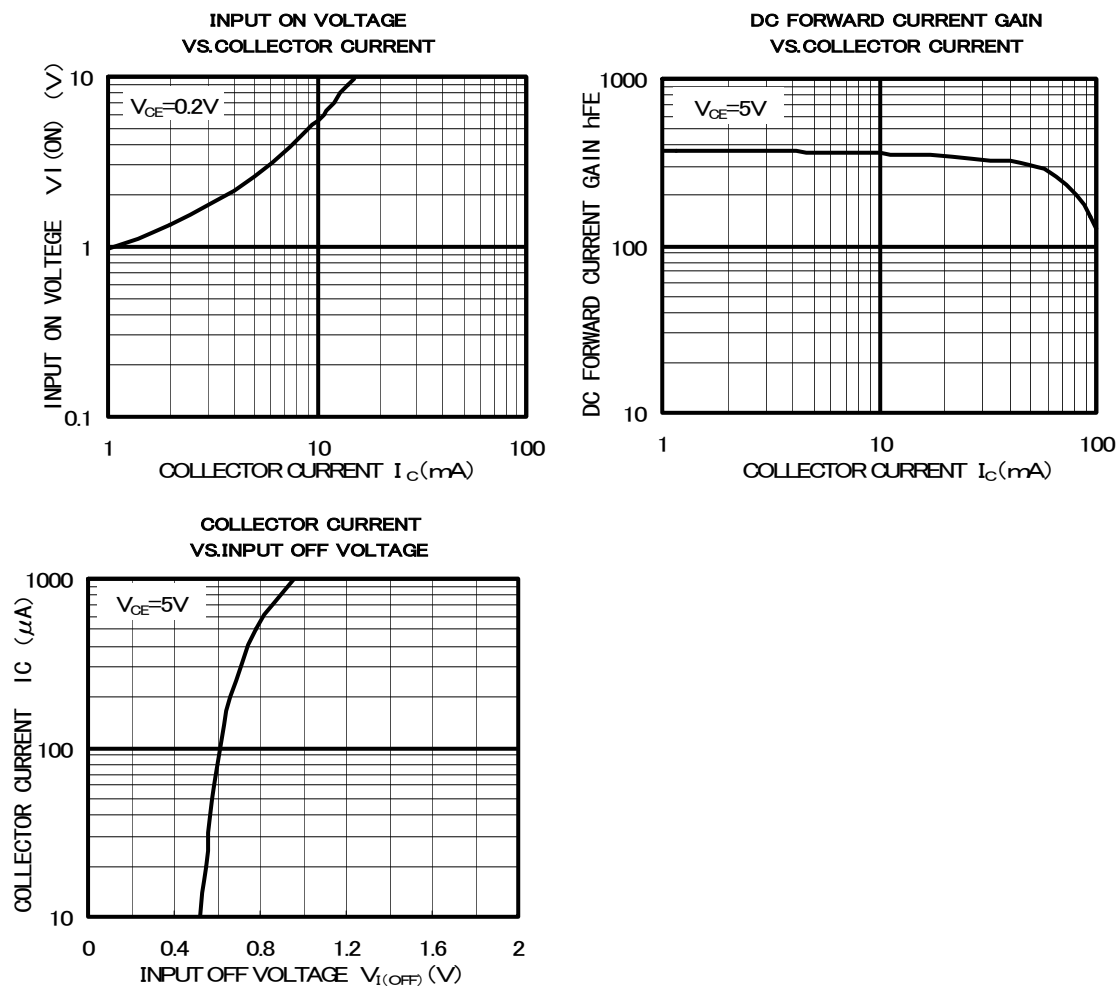
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ELECTRICAL CHARACTERISTICS (Ta=25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
V(BR)CEO	Collector to Emitter break down voltage	$I_C=100\mu A, R_{BE}=\infty$	50	-	-	V
ICBO	Collector cut off current	$V_{CB}=50V, I_E=0$	-	-	0.1	μA
hFE	DC forward current gain	$V_{CE}=5V, I_C=1mA$	100	-	-	-
VCE(sat)	Collector to Emitter saturation voltage	$I_C=1mA, I_B=0.1mA$	-	0.1	0.3	V
R ₁	Input resistor	-	-	100	-	k Ω
f _T	Gain band width product	$V_{CE}=6V, I_E=10mA$	Tr1	-	200	MHz
			Tr2	-	150	

TYPICAL CHARACTERISTICS (Tr1)

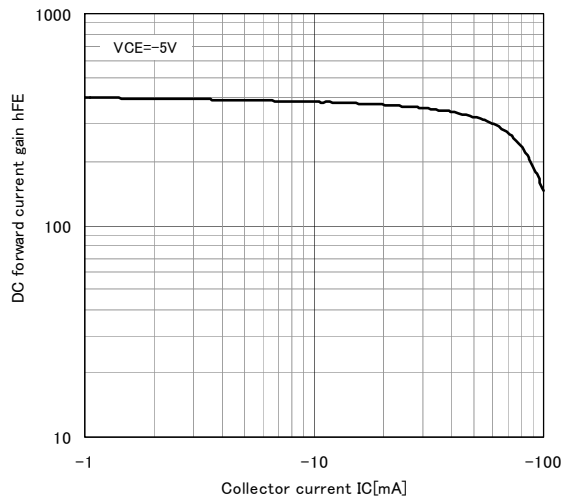


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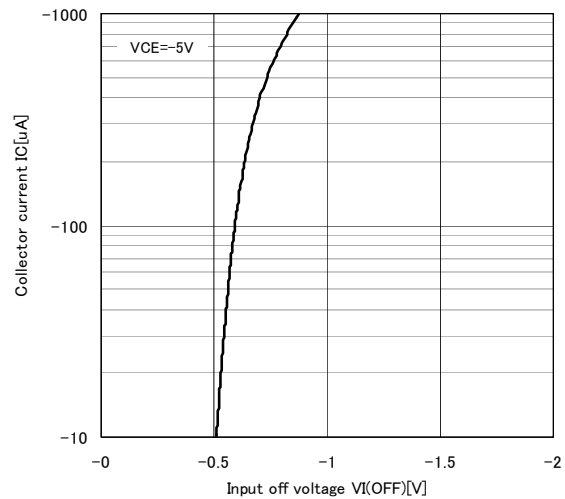
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TYPICAL CHARACTERISTICS (Tr2)

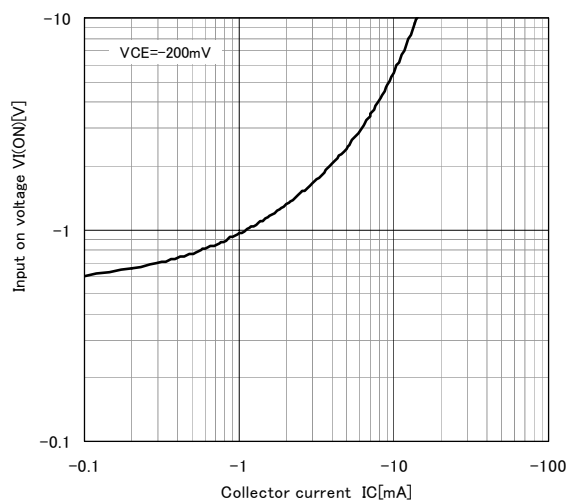
DC forward current gain-Collector current



Collector current-Input off voltage



Input on voltage-Collector current





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