Transistors

2.5V Drive Pch MOS FET RTE002P02

Structure

Silicon P-channel MOS FET

● Features

- 1) Low On-resistance.
- 2) Small package (EMT3).
- 3) 2.5V drive.

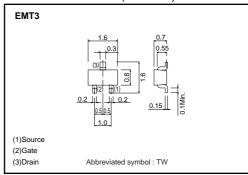
Applications

Switching

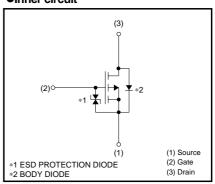
Package specifications

	Package	Taping	
Туре	Code	TL	
	Basic ordering unit (pieces)	3000	
RTE002P02	0		

●External dimensions (Unit : mm)



●Inner circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit		
Drain-source voltage		V_{DSS}	-20	V		
Gate-source voltage		V _{GSS}	±12	V		
Drain augrant	Continuous	ID	±0.2	Α		
Drain current	Pulsed	I _{DP} *1	±0.4	Α		
Total power dissipation	P _D *2	0.15	W			
Channel temperature		Tch	150	°C		
Range of storage temperature		Tstg	-55 to +150	°C		

^{*1} Pw≤10μs, Duty cycle≤1%

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a)*	833	°C/W

^{*} Each terminal mounted on a recommended land

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1/2

^{*2} Each terminal mounted on a recommended land

RTE002P02

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	_	_	±10	μΑ	Vgs= ±12V, Vps=0V	
Drain-source breakdown voltage	V _{(BR) DSS}	-20	_	_	٧	I _D = -1mA, V _{GS} =0V	
Zero gate voltage drain current	I _{DSS}	_	_	-1	μΑ	V _{DS} = -20V, V _{GS} =0V	
Gate threshold voltage	V _{GS (th)}	-0.7	_	-2.0	٧	V_{DS} = -10V, I_{D} = -1mA	
Static drain-source on-state resistance	R _{DS} (on)*	_	1.0	1.5	Ω	I _D = -0.2A, V _G S= -4.5V	
		_	1.1	1.6	Ω	I _D = -0.2A, V _G S= -4V	
		_	2.0	3.0	Ω	I _D = -0.15A, V _G S= -2.5V	
Forward transfer admittance	Y _{fs} *	0.2	_	_	S	V _{DS} = -10V, I _D = -0.15A	
Input capacitance	Ciss	_	50	_	pF	V _{DS} = -10V	
Output capacitance	Coss	_	5	_	pF	Vgs= 0V	
Reverse transfer capacitance	Crss	_	5	_	pF	f=1MHz	
Turn-on delay time	t _{d (on)} *	_	9	_	ns	V _{DD} ≒ −15V	
Rise time	tr *	-	6	_	ns	ID= -0.15A	
Turn-off delay time	t _{d (off)} *	-	35	_	ns	V _{GS} = -4.5V R _L = 100Ω	
Fall time	t _f *	_	45	_	ns	R _G = 10Ω	

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Doron	ootor	Symbol	Min	Tvp.	Max.	Lloit	Conditions
raiaii	ietei	Symbol	Mın.	τyp.	IVIAX.	Unit	Conditions
Forward voltage	.	Vsp	_	_	-1.2	V	Is= -0.1A, Vgs=0V

Appendix

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Appendix1-Rev1.1