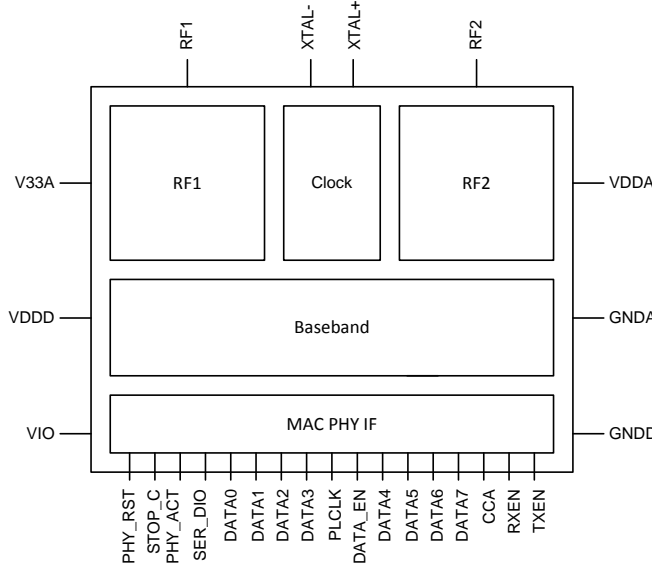


RTU7010-GR WiMedia Compliant UWB CMOS PHY

GENERAL DESCRIPTION	RTU7010 is a highly integrated, high performance, WiMedia-compliant Ultra Wide Band (UWB) physical layer (PHY) solution.		
BLOCK DIAGRAM	 <p>The block diagram shows the internal architecture of the RTU7010-GR. It consists of three main functional blocks: RF1, Clock, and RF2 at the top; Baseband in the middle; and MAC PHY IF at the bottom. Power supplies V33A, VDDD, and VIO are connected to the top, middle, and bottom sections respectively. On the right, VDDA, GNDA, and GNDD are also indicated. The bottom of the chip features a series of pins: PHY_RST, STOP_C, PHY_ACT, SER_DIO, DATA0 through DATA7, PCLK, DATA_EN, DATA4 through DATA7, CCA, RXEN, and TXEN. External connections for RF1, XTAL-, XTAL+, and RF2 are shown at the top.</p>		
CERTIFICATION, LOGO AND BAND GROUP SUPPORT	Completed Certifications WiMedia Wireless USB FCC TELEC CE	RTU7010-GR ✓ NA ✓ ✓ ✓	Supported Band Groups RTU7010-GR BG1 BG3 BG6 BG4 ✓ x x x
FEATURES	<ul style="list-style-type: none"> - Compliant to WiMedia PHY Technical Specification Release 1.1 - Multiple RF chains for transmit and receive diversity - Supports transmission and reception of burst mode and burst preamble in all permitted modes, enabling improved data throughput - Supports Transmit Power Control and Link Quality Indicator - Data rates of 53.3, 80, 106.7, 160, 200, 320, 400, 480Mbps supported - Efficient correction algorithms for residual DC offset, phase noise, frequency offset and timing offset due to RF impairments - Selectable digital transmit and receiver FIR filters provided to meet the requirement of transmit spectrum mask and to reject the adjacent channel interference - Fast receiver automatic gain control (AGC) and antenna diversity functions - MAC-aided transmit power control - On-chip calibration for center carrier leakage, Tx power control, Rx DC offset and BB LPF corner frequency 		

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