

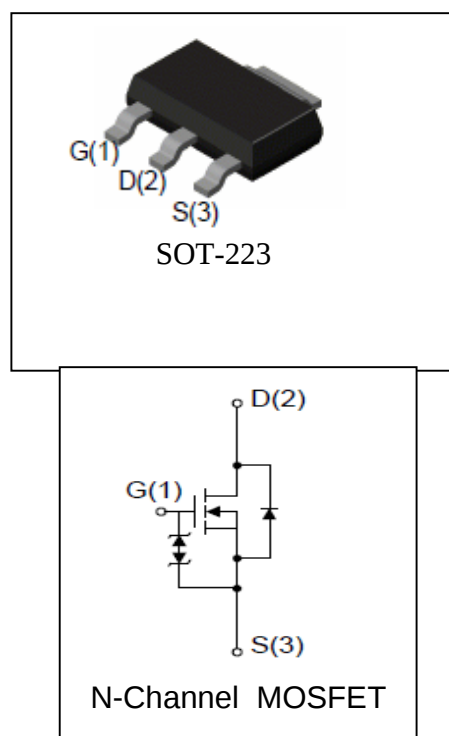
Features

- 100V/3A,
 $R_{DS(ON)} = 130m\Omega$ (Typ.) @ $V_{GS}=10V$
 $R_{DS(ON)} = 140m\Omega$ (Typ.) @ $V_{GS}=4.5V$
- ESD Protected
- Reliable and Rugged
- Ultra Low On-Resistance
- 100% avalanche tested
- Lead Free and Green Available

Applications

- Power Management

Pin Description



Absolute Maximum Ratings

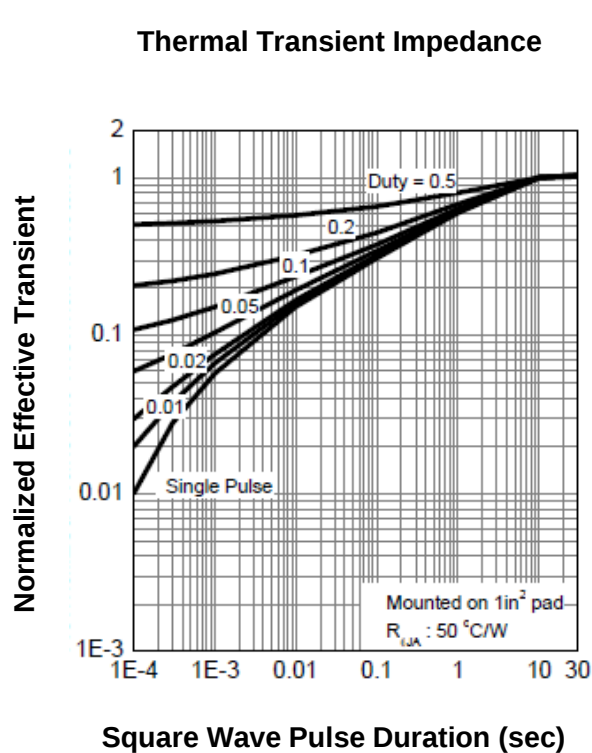
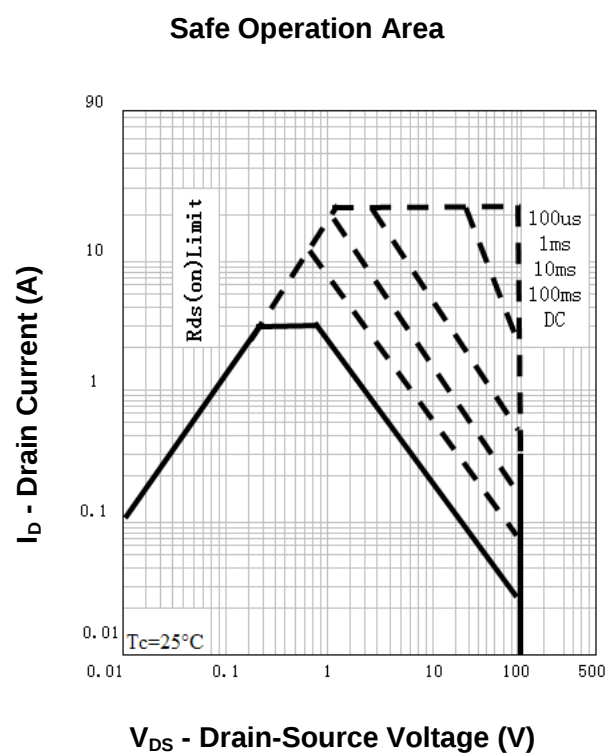
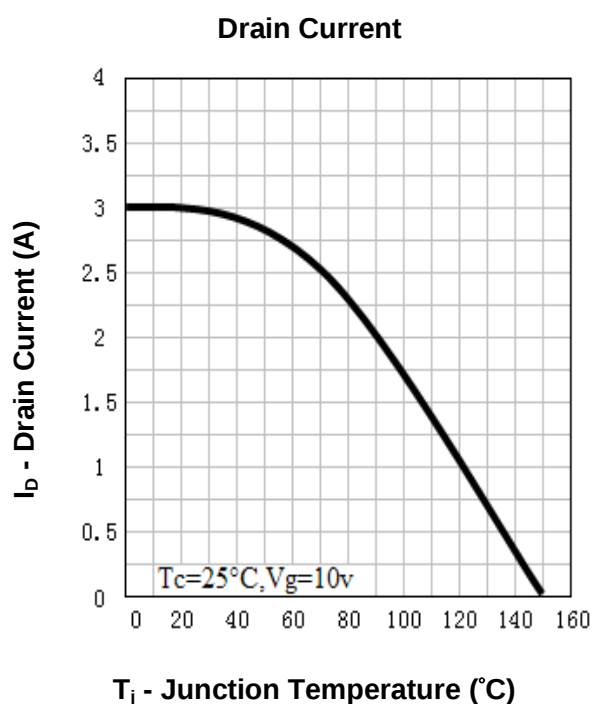
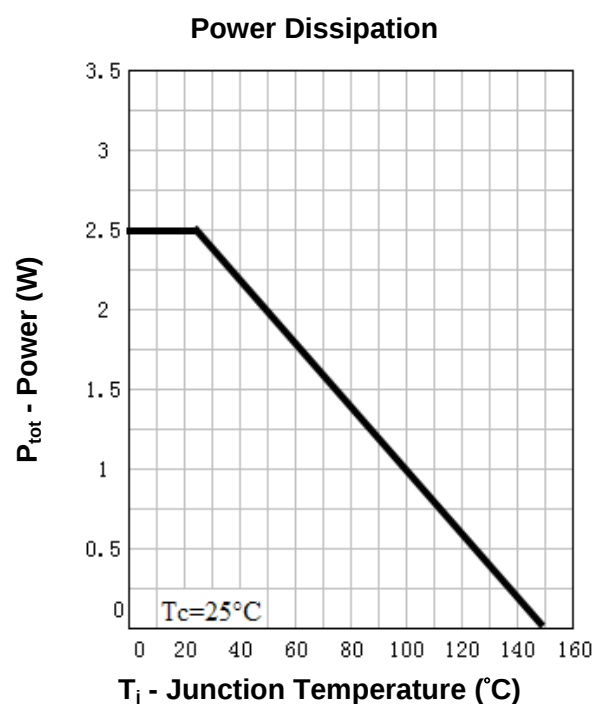
Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _C =25°C	3	A
Mounted on Large Heat Sink				
I _{DP}	300μs Pulse Drain Current Tested	T _C =25°C	12	A
I _D	Continuous Drain Current(V _{GS} =10V)	T _C =25°C	3 ^①	A
		T _C =70°C	2.5	
P _D	Maximum Power Dissipation	T _C =25°C	2.5	W
		T _C =70°C	1.6	
R _{θJA} ^②	Thermal Resistance-Junction to Ambient		50	°C/W

Electrical Characteristics (T_A=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Condition	RU1HE3D			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} =0V			1	μA
		T _J =85°C			30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.5	2	2.7	V
I _{GSS}	Gate Leakage Current	V _{GS} =±16V, V _{DS} =0V			±10	uA
R _{DS(ON)} ③	Drain-Source On-state Resistance	V _{GS} = 10V, I _{DS} =3A		130	145	mΩ
		V _{GS} = 4.5V, I _{DS} =2A		140	180	mΩ
Diode Characteristics						
V _{SD} ③	Diode Forward Voltage	I _{SD} =2.5A, V _{GS} =0V			1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =2.5A, dI _{SD} /dt=100A/μs		43		ns
Q _{rr}	Reverse Recovery Charge			78		nC
Dynamic Characteristics ④						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz		0.6		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} = 30V, Frequency=1.0MHz		930		pF
C _{oss}	Output Capacitance			85		
C _{rss}	Reverse Transfer Capacitance			45		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =50V, R _L =30Ω, I _{DS} =3A, V _{GEN} = 10V, R _G =25Ω		13		ns
t _r	Turn-on Rise Time			15		
t _{d(OFF)}	Turn-off Delay Time			29		
t _f	Turn-off Fall Time			16		
Gate Charge Characteristics ④						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} = 10V, I _{DS} =3A		20	26	nC
Q _{gs}	Gate-Source Charge			5		
Q _{gd}	Gate-Drain Charge			5.9		

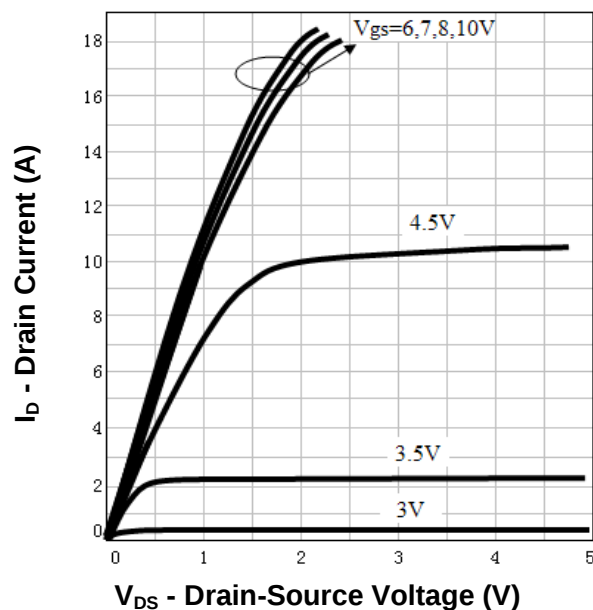
- Notes: ①Current limited by maximum junction temperature.
 ②When mounted on 1 inch square copper board, t ≤10sec.
 ③Pulse test ; Pulse width≤300μs, duty cycle≤2%.
 ④Guaranteed by design, not subject to production testing.

Typical Characteristics

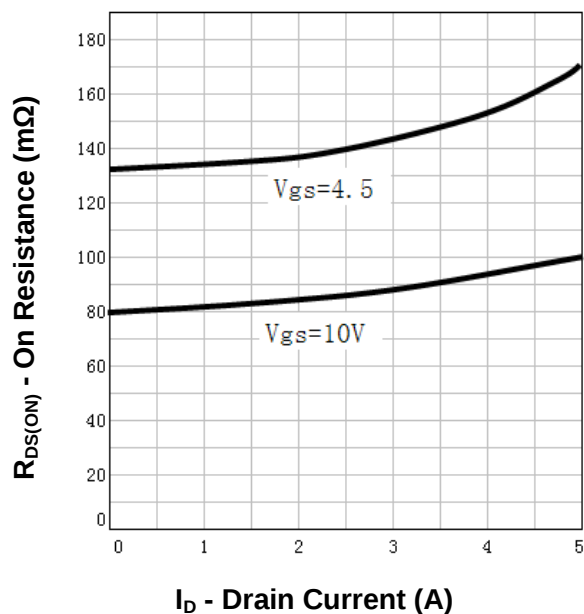


Typical Characteristics

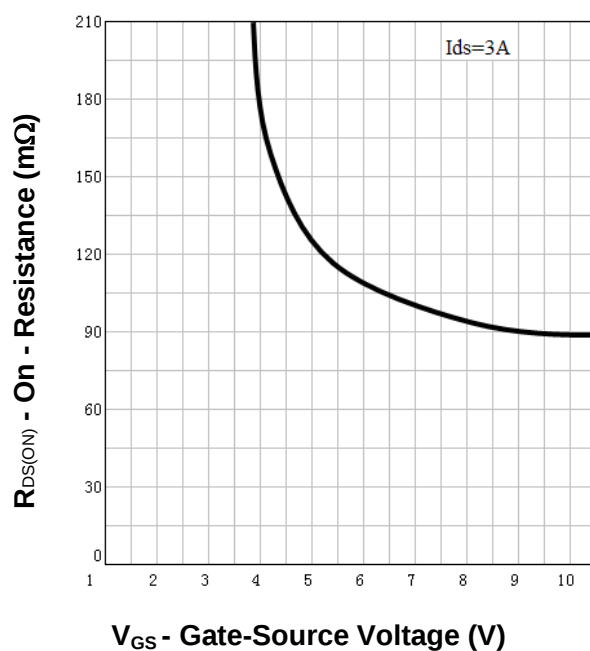
Output Characteristics



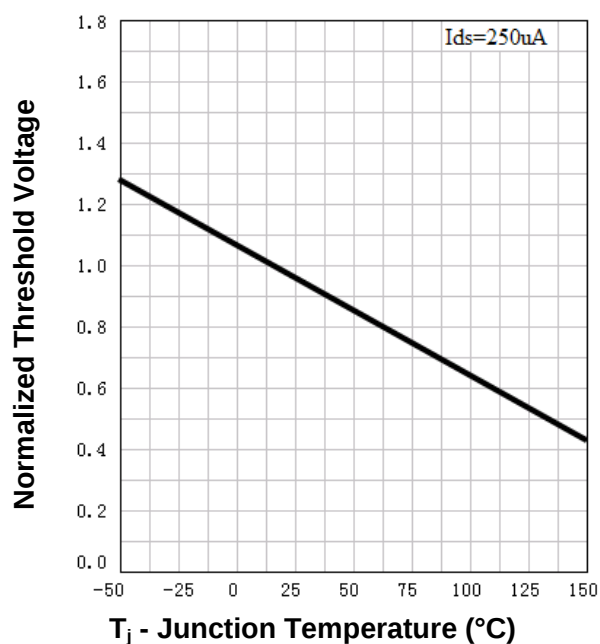
Drain-Source On Resistance



Drain-Source On Resistance

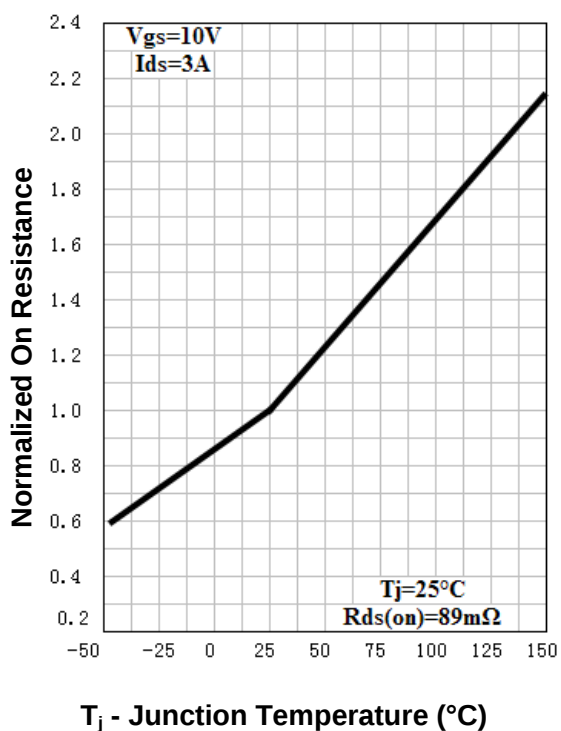


Gate Threshold Voltage

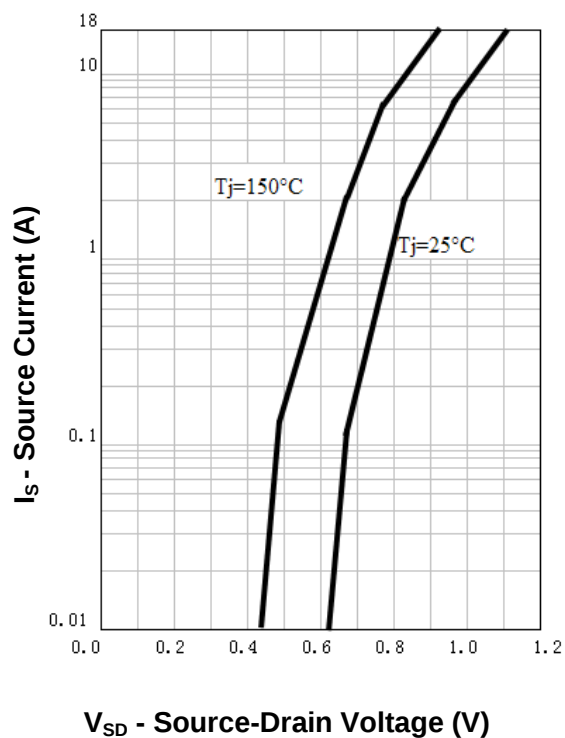


Typical Characteristics

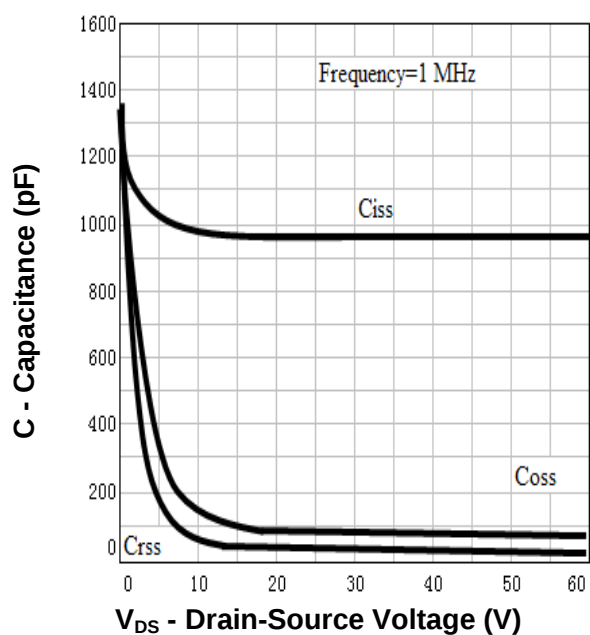
Drain-Source On Resistance



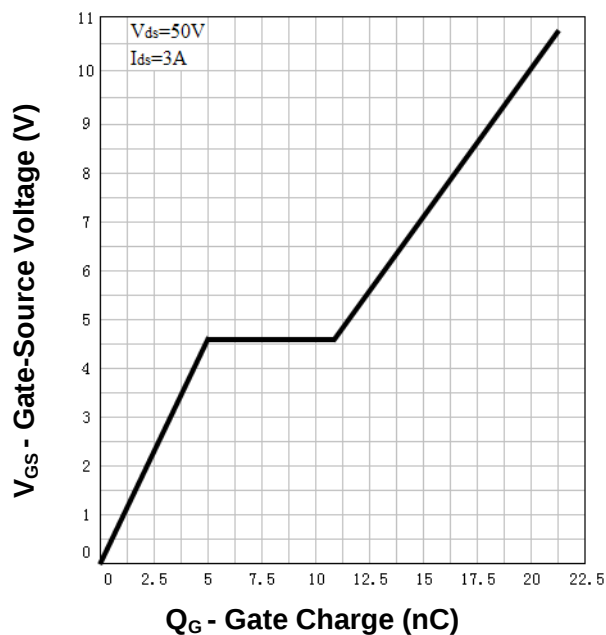
Source-Drain Diode Forward



Capacitance



Gate Charge

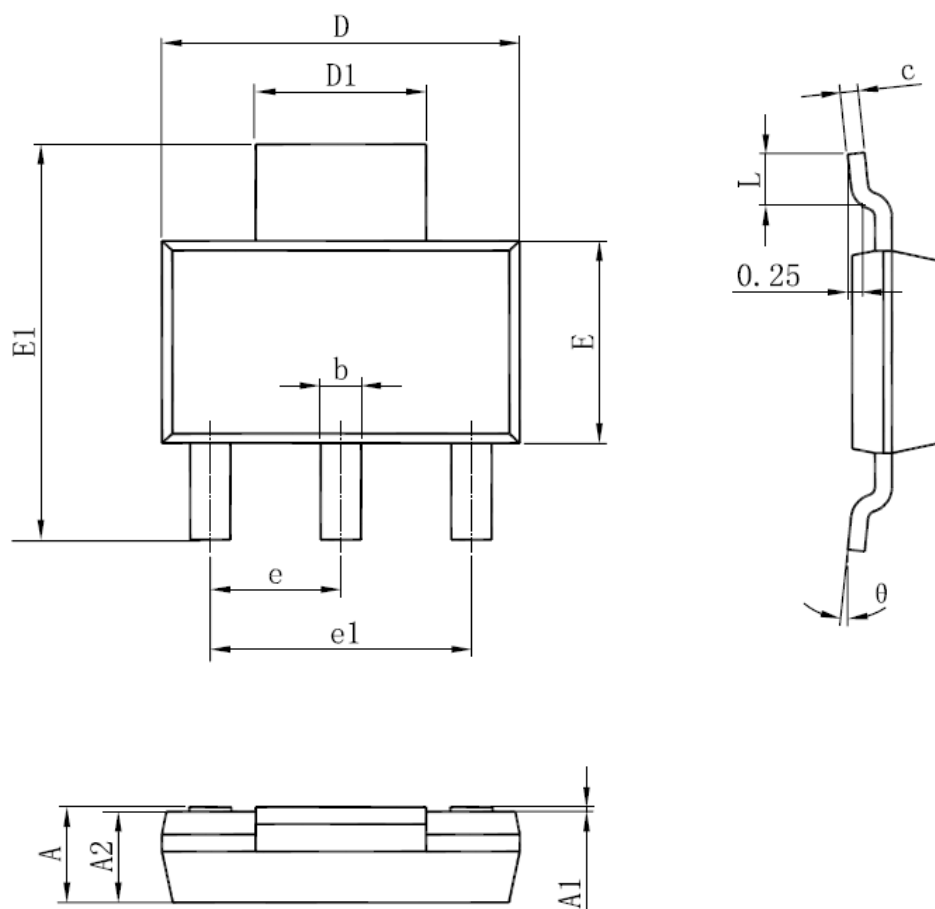


Ordering and Marking Information

Device	Marking	Package	Packaging	Quantity	Reel Size	Tape width
RU1HE3D	RU1HE3D	SOT-223	Tape&Reel	2500	13''	12mm

Package Information

SOT-223



SYMBOL	MM		INCH		SYMBOL	MM		INCH	
	MIN	MAX	MIN	MAX		MIN	MAX	MIN	MAX
A	1.520	1.800	0.060	0.071	E	3.300	3.700	0.130	0.146
A1	0.000	0.100	0.000	0.004	E1	6.830	7.070	0.269	0.278
A2	1.500	1.700	0.059	0.067	e	2.300(BSC)		0.091(BSC)	
b	0.660	0.820	0.026	0.032	e1	4.500	4.700	0.177	0.185
c	0.250	0.350	0.010	0.014	L	0.900	1.150	0.035	0.045
D	6.200	6.400	0.244	0.252	θ	0°	10°	0°	10°
D1	2.900	3.100	0.114	0.122					

ALL DIMENSIONS REFER TO JEDEC STANDARD
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS

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