

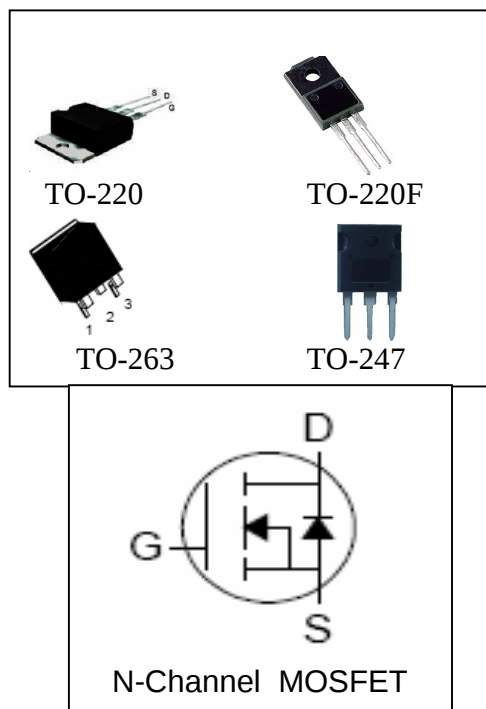
Features

- 200V/30A,
 $R_{DS(ON)} = 75m\Omega$ (Typ.) @ $V_{GS} = 10V$
- Ultra Low On-Resistance
- Fast Switching and Fully Avalanche Rated
- 100% avalanche tested
- 175°C Operating Temperature
- Lead Free and Green Available

Applications

- Switching Application Systems
- DC/DC Converters

Pin Description



Absolute Maximum Ratings

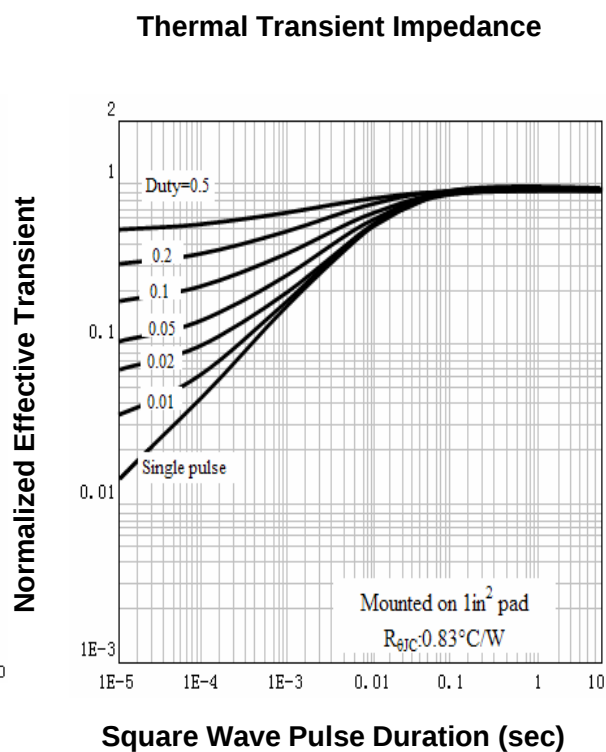
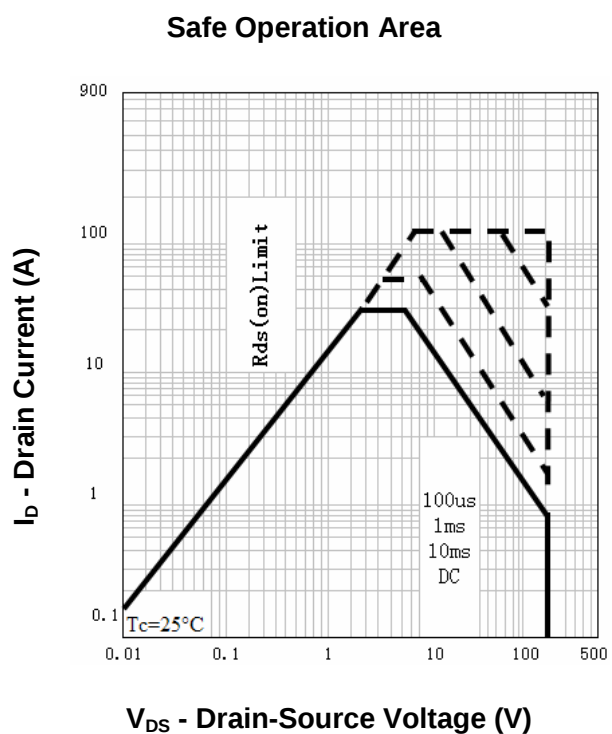
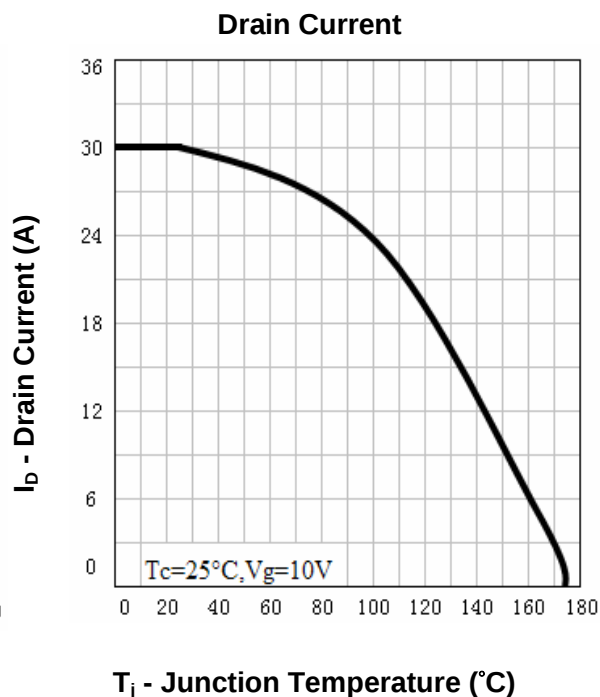
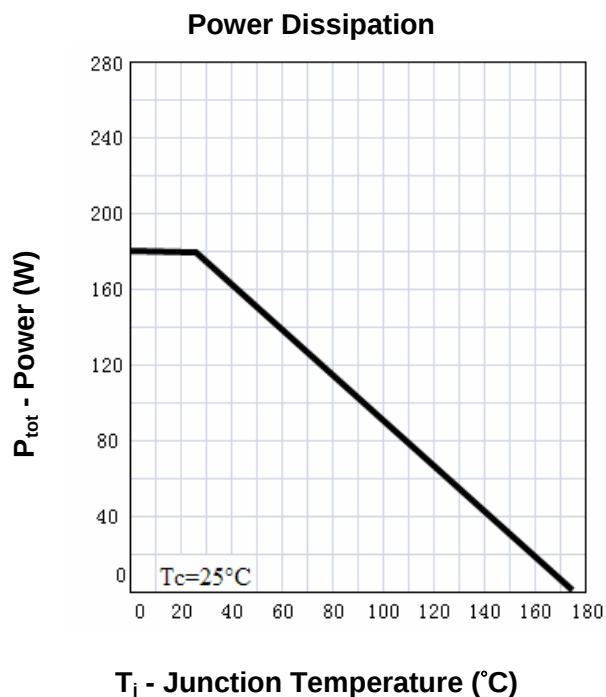
Symbol	Parameter		Rating	Unit
Common Ratings (T _C =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		200	V
V _{GSS}	Gate-Source Voltage		±25	
T _J	Maximum Junction Temperature		175	°C
T _{STG}	Storage Temperature Range		-55 to 175	°C
I _S	Diode Continuous Forward Current	T _C =25°C	30	A
Mounted on Large Heat Sink				
I _{DP}	300μs Pulse Drain Current Tested	T _C =25°C	120 ^①	A
I _D	Continuous Drain Current	T _C =25°C	30 ^②	A
		T _C =100°C	23 ^②	
P _D	Maximum Power Dissipation	T _C =25°C	180	W
		T _C =100°C	90	
R _{θJC}	Thermal Resistance-Junction to Case		0.83	°C/W
Drain-Source Avalanche Ratings				
E _{AS} ^③	Avalanche Energy, Single Pulsed		81	mJ

Electrical Characteristics (T_C=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Condition	RU2H30Q			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200V, V _{GS} =0V			1	μA
		T _J =85°C			30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±25V, V _{DS} =0V			±100	nA
R _{DS(ON)} ^④	Drain-Source On-state Resistance	V _{GS} = 10V, I _{DS} =17A		75	85	mΩ
Diode Characteristics						
V _{SD} ^④	Diode Forward Voltage	I _{SD} =30A, V _{GS} =0V			1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =30A, dI _{SD} /dt=100A/μs		150		ns
Q _{rr}	Reverse Recovery Charge			125		nC
Dynamic Characteristics ^⑤						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		1.0		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =100V, Frequency=1.0MHz		2140		pF
C _{oss}	Output Capacitance			308		
C _{rss}	Reverse Transfer Capacitance			78		
t _{d(ON)}	Turn-on Delay Time	V _{DD} =100V, R _L =3Ω, I _{DS} =30A, V _{GEN} = 10V, R _G =6Ω		16		ns
t _r	Turn-on Rise Time			48		
t _{d(OFF)}	Turn-off Delay Time			38		
t _f	Turn-off Fall Time			33		
Gate Charge Characteristics ^⑤						
Q _g	Total Gate Charge	V _{DS} =160V, V _{GS} = 10V, I _{DS} =30A		116		nC
Q _{gs}	Gate-Source Charge			23		
Q _{gd}	Gate-Drain Charge			52		

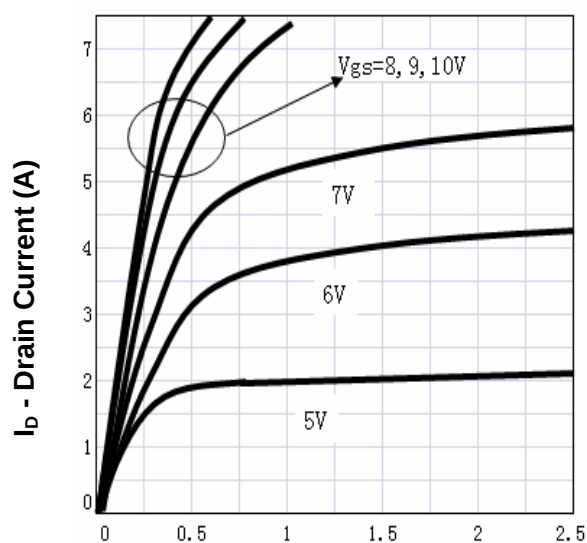
- Notes: ①Pulse width limited by safe operating area.
 ②Calculated continuous current based on maximum allowable junction temperature.
 ③Limited by T_{Jmax}, I_{AS} =18A, V_{DD} = 60V, R_G = 50Ω , Starting T_J = 25°C.
 ④Pulse test ; Pulse width≤300μs, duty cycle≤2%.
 ⑤Guaranteed by design, not subject to production testing.

Typical Characteristics



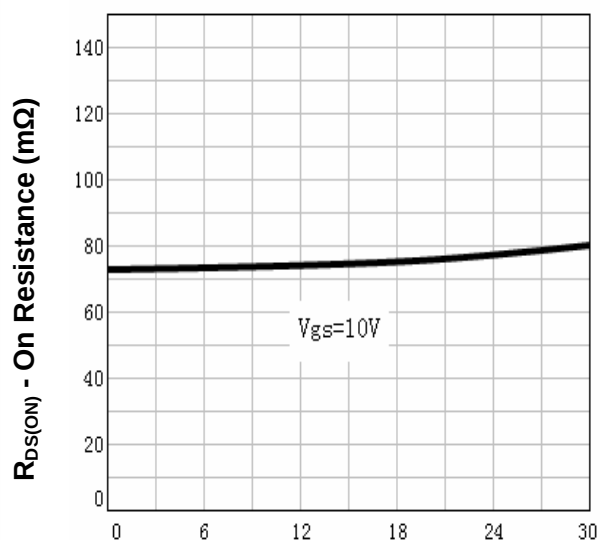
Typical Characteristics

Output Characteristics



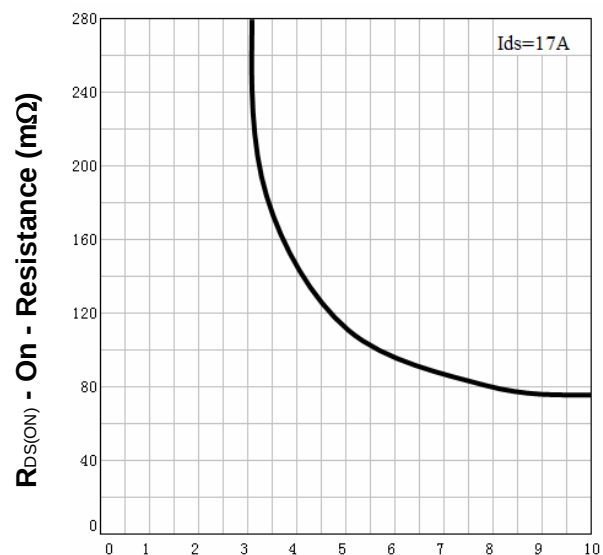
V_{DS} - Drain-Source Voltage (V)

Drain-Source On Resistance



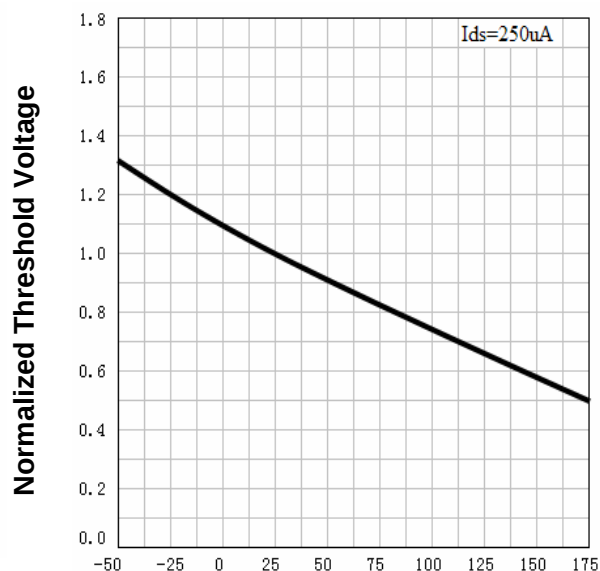
I_D - Drain Current (A)

Drain-Source On Resistance



V_{GS} - Gate-Source Voltage (V)

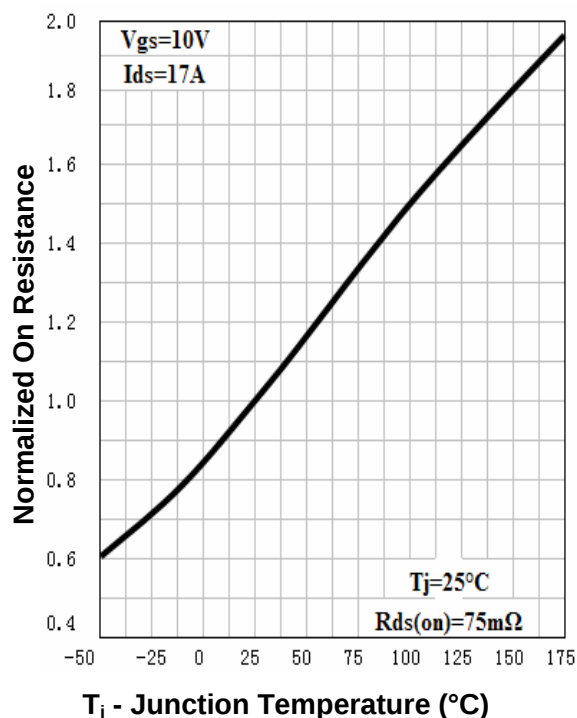
Gate Threshold Voltage



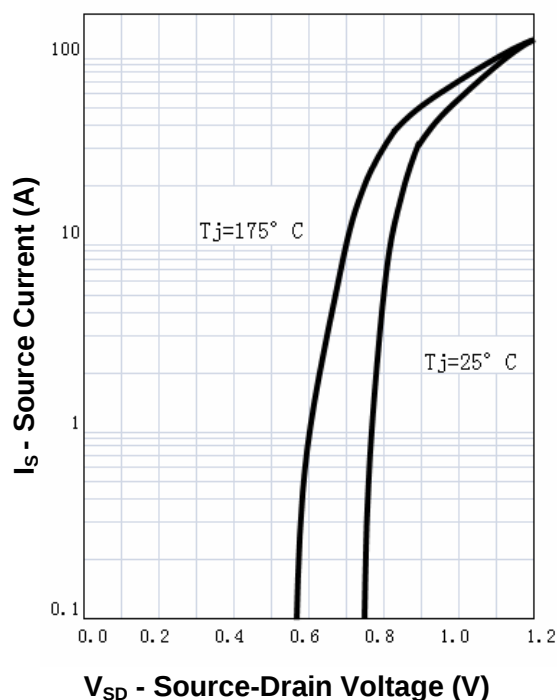
T_J - Junction Temperature ($^{\circ}C$)

Typical Characteristics

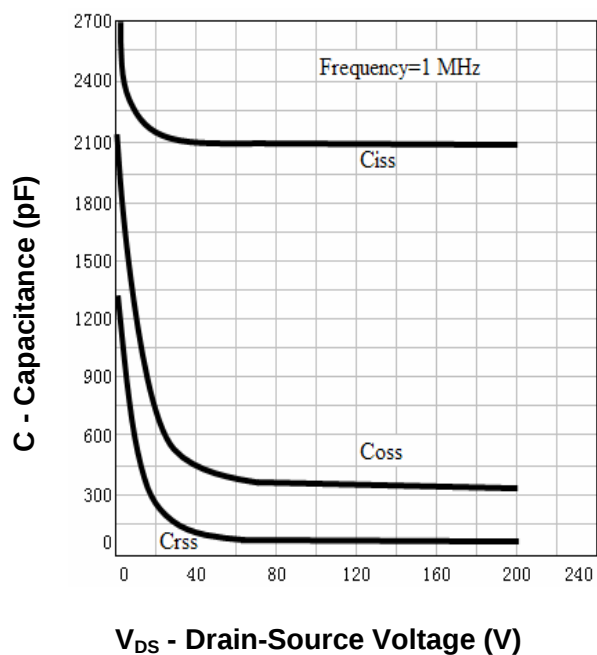
Drain-Source On Resistance



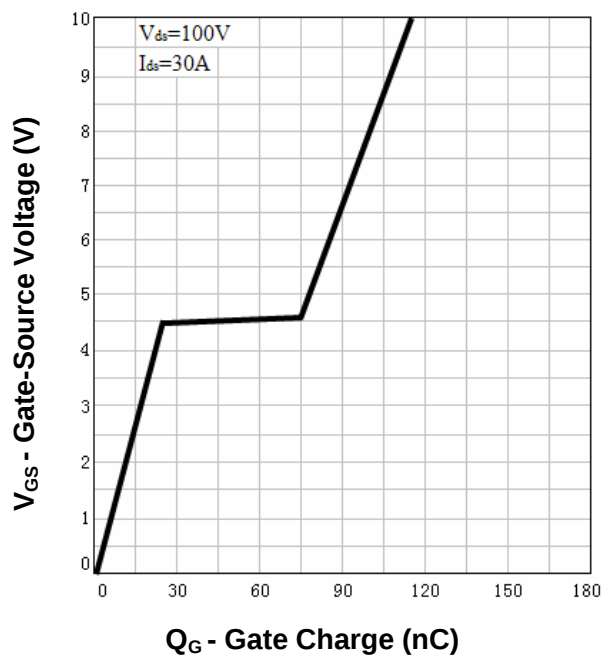
Source-Drain Diode Forward



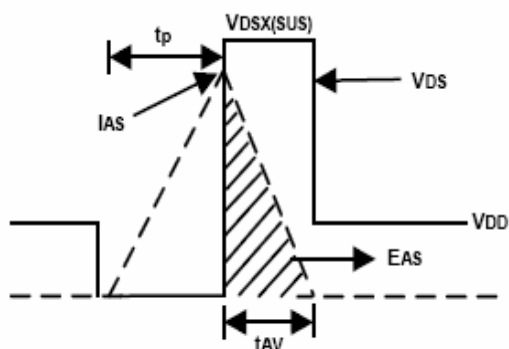
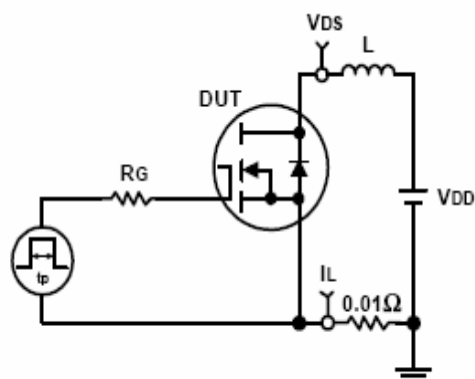
Capacitance



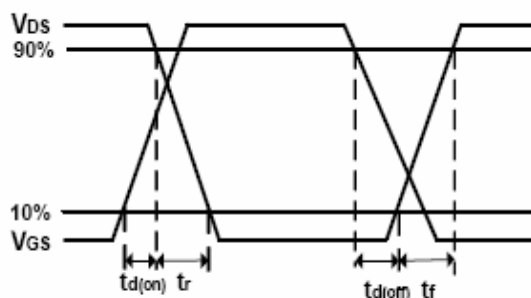
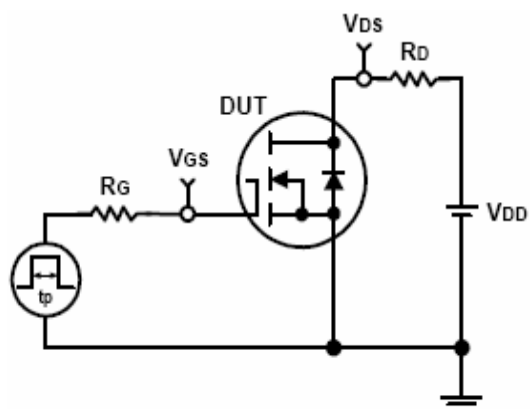
Gate Charge



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Ordering and Marking Information

RU2H30

Package (Available)

R : TO-220; S: TO-263 ; Q: TO-247

Operating Temperature Range

C : -55 to 175 °C

Assembly Material

G : Green & Lead Free

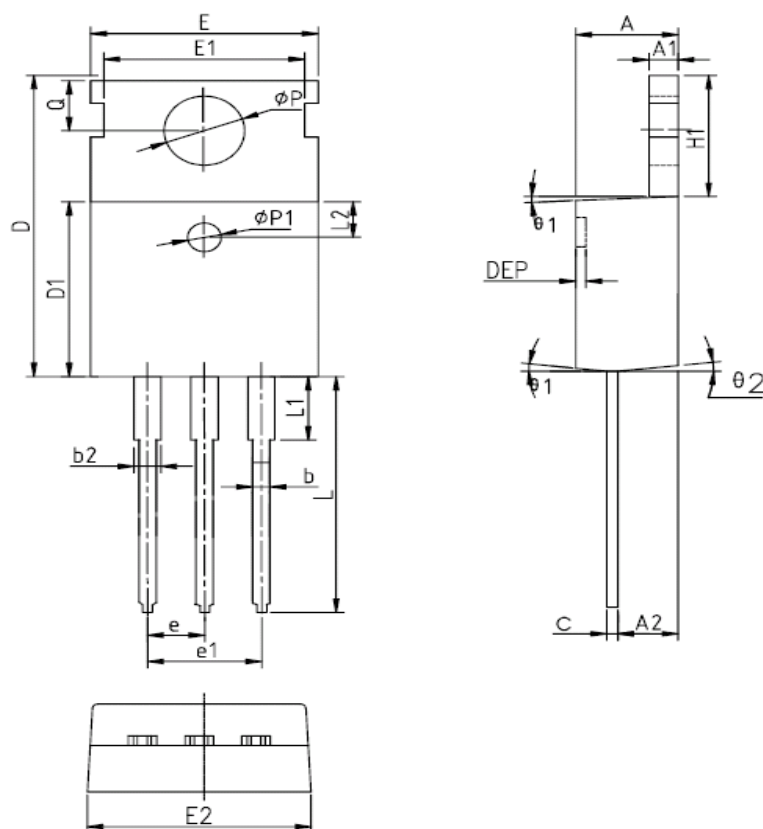
Packaging

T : TUBE

TR : Tape & Reel

Package Information

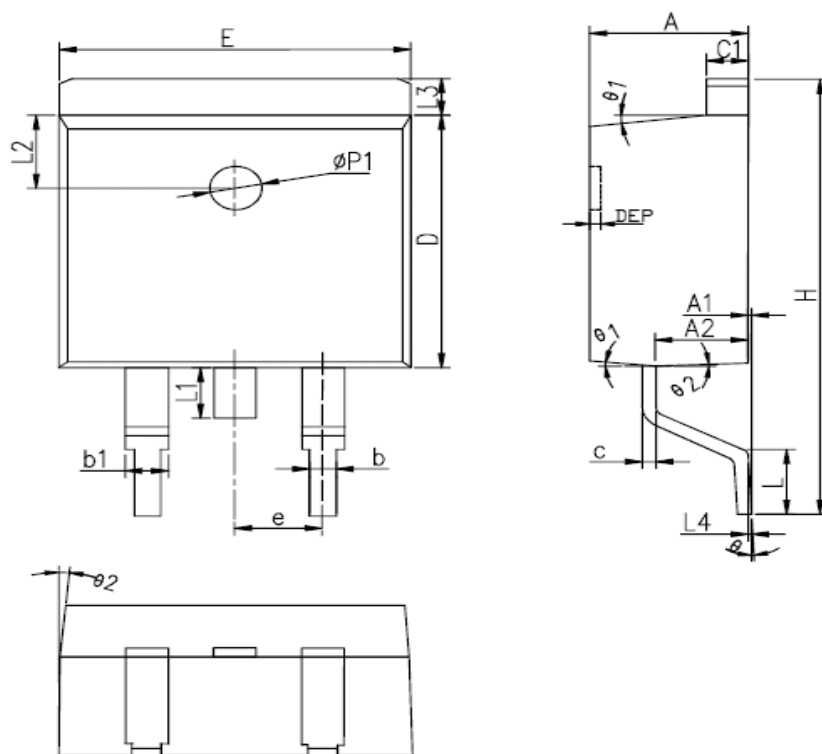
TO-220FB-3L



SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX		MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185	Øp1	1.40	1.50	1.60	0.055	0.059	0.063
A1	1.27	1.30	1.33	0.050	0.051	0.052	e	2.54BSC			0.1BSC		
A2	2.35	2.40	2.50	0.093	0.094	0.098	e1	5.08BSC			0.2BSC		
b	0.77	-	0.90	0.030	-	0.035	H1	6.40	6.50	6.60	0.252	0.256	0.260
b2	1.23	-	1.36	0.048	-	0.054	L	12.75	-	13.17	0.502	-	0.519
C	0.48	0.50	0.52	0.019	0.020	0.021	L1	-	-	3.95	-	-	0.156
D	15.40	15.60	15.80	0.606	0.614	0.622	L2	2.50REF.			0.098REF.		
D1	9.00	9.10	9.20	0.354	0.358	0.362	Øp	3.57	3.60	3.63	0.141	0.142	0.143
DEP	0.05	0.10	0.20	0.002	0.004	0.008	Q	2.73	2.80	2.87	0.107	0.110	0.113
E	9.70	9.90	10.10	0.382	0.389	0.398	θ 1	5°	7°	9°	5°	7°	9°
E1	-	8.70	-	-	0.343	-	θ 2	1°	3°	5°	1°	3°	5°
E2	9.80	10.00	10.20	0.386	0.394	0.401							

ALL DIMENSIONS REFER TO JEDEC STANDARD
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS

TO-263-2L



SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX		MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185	L	2.00	2.30	2.60	0.079	0.090	0.102
A1	0	0.10	0.25	0	0.004	0.010	L3	1.17	1.27	1.40	0.046	0.050	0.055
A2	2.59	2.69	2.79	0.102	0.106	0.110	L1	-	-	1.70	-	-	0.067
b	0.77	-	0.90	0.030	-	0.035	L4	0.25BSC			0.01BSC		
b1	1.23	-	1.36	0.048	-	0.052	L2	2.50REF.			0.098REF.		
c	0.34	-	0.47	0.013	-	0.019	θ	0°	-	8°	0°	-	8°
C1	1.22	-	1.32	0.048	-	0.052	$\theta 1$	5°	7°	9°	5°	7°	9°
D	8.60	8.70	8.80	0.338	0.343	0.346	$\theta 2$	1°	3°	5°	1°	3°	5°
E	10.00	10.16	10.26	0.394	0.4	0.404	DEP	0.05	0.10	0.20	0.002	0.004	0.008
e	2.54BSC			0.1BSC			$\phi p1$	1.40	1.50	1.60	0.055	0.059	0.063
H	14.70	15.10	15.50	0.579	0.594	0.610							

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