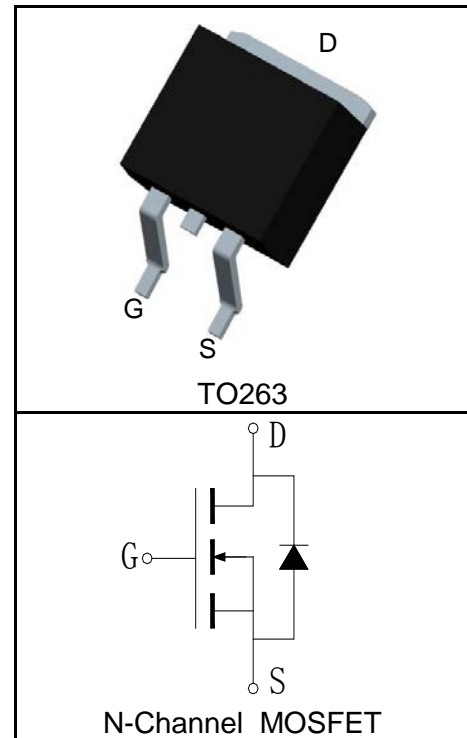


Features

- 40V/120A,
 $R_{DS(ON)} = 3.5m\Omega(Typ.)@V_{GS}=10V$
- Super High Dense Cell Design
- Ultra Low On-Resistance
- 100% avalanche tested
- Lead Free and Green Devices Available (RoHS Compliant)

Applications

- DC-DC Converters

Pin Description

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings ($T_C=25^\circ C$ Unless Otherwise Noted)			
V_{DSS}	Drain-Source Voltage	40	V
V_{GSS}	Gate-Source Voltage	± 20	
T_J	Maximum Junction Temperature	175	$^\circ C$
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
I_S	Diode Continuous Forward Current	$T_C=25^\circ C$ 120	A
Mounted on Large Heat Sink			
$I_{DP}^{①}$	300 μs Pulse Drain Current Tested	$T_C=25^\circ C$ 480	A
$I_D^{②}$	Continuous Drain Current($V_{GS}=10V$)	$T_C=25^\circ C$ 120	A
		$T_C=100^\circ C$ 103	
P_D	Maximum Power Dissipation	$T_C=25^\circ C$ 150	W
		$T_C=100^\circ C$ 75	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	$^\circ C/W$
Drain-Source Avalanche Ratings			
$E_{AS}^{③}$	Avalanche Energy, Single Pulsed	400	mJ

Electrical Characteristics ($T_C=25^\circ\text{C}$ Unless Otherwise Noted)

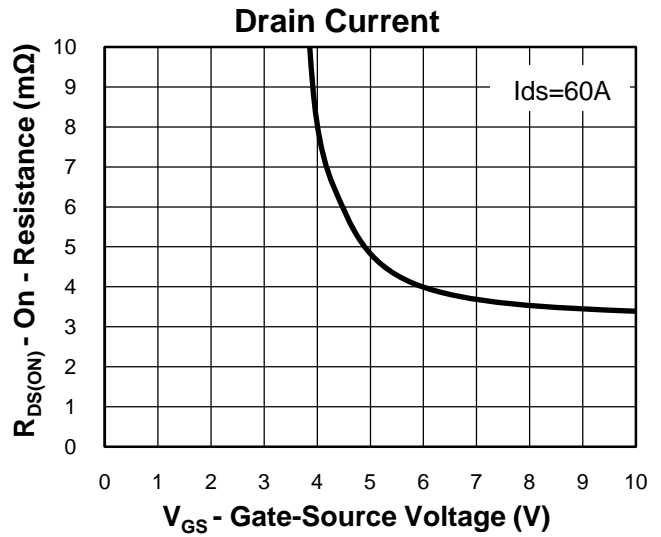
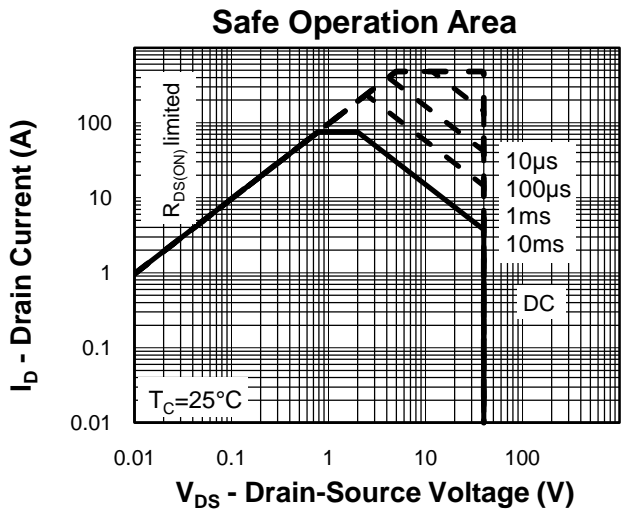
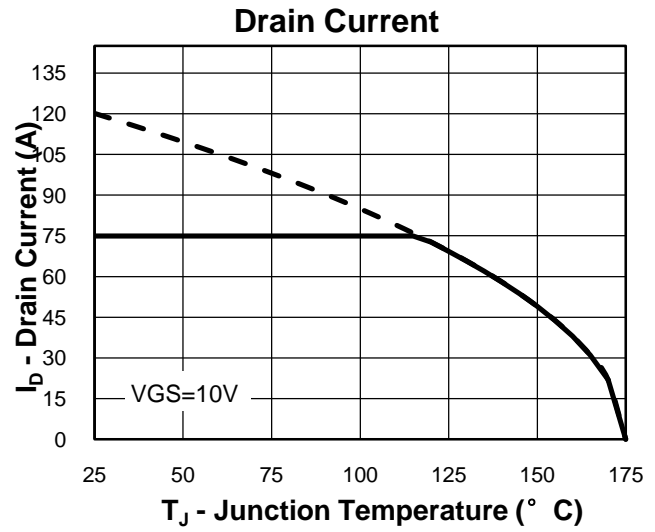
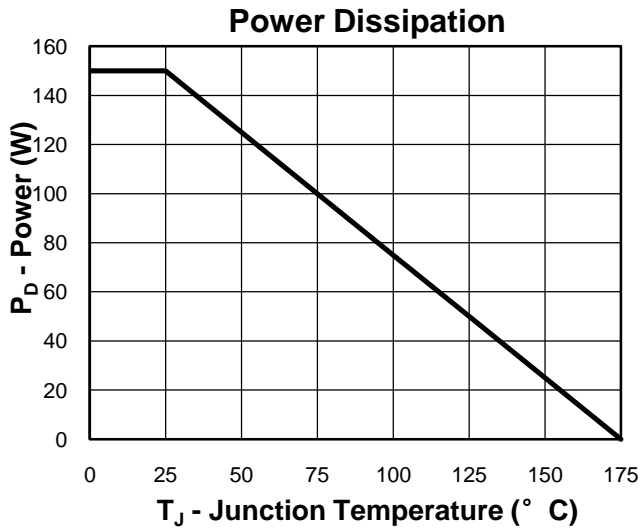
Symbol	Parameter	Test Condition	RU40120S			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	40			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V$			1	μA
		$T_J=125^\circ C$			30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	2		4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
$R_{DS(ON)}^{(4)}$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=60A$		3.5	4.5	m Ω
Diode Characteristics						
$V_{SD}^{(4)}$	Diode Forward Voltage	$I_{SD}=60A, V_{GS}=0V$			1.2	V
t_{rr}	Reverse Recovery Time	$I_{SD}=60A, di_{SD}/dt=100A/\mu s$		33		ns
Q_{rr}	Reverse Recovery Charge			30		nC
Dynamic Characteristics ⁽⁵⁾						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$		1.8		Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=20V,$ Frequency=1.0MHz		3700		pF
C_{oss}	Output Capacitance			680		
C_{riss}	Reverse Transfer Capacitance			345		
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=20V, I_{DS}=60A,$ $V_{GEN}=10V, R_G=4.7\Omega$		36		ns
t_r	Turn-on Rise Time			205		
$t_{d(OFF)}$	Turn-off Delay Time			85		
t_f	Turn-off Fall Time			45		
Gate Charge Characteristics ⁽⁵⁾						
Q_g	Total Gate Charge	$V_{DS}=32V, V_{GS}=10V,$ $I_{DS}=60A$		90		nC
Q_{gs}	Gate-Source Charge			32		
Q_{gd}	Gate-Drain Charge			37		

- Notes:
- ① Pulse width limited by safe operating area.
 - ② Calculated continuous current based on maximum allowable junction temperature. The package limitation current is 75A.
 - ③ Limited by T_{Jmax} , $I_{AS}=40A$, $V_{DD}=32V$, $R_G=50\Omega$, Starting $T_J=25^\circ C$.
 - ④ Pulse test; Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
 - ⑤ Guaranteed by design, not subject to production testing.

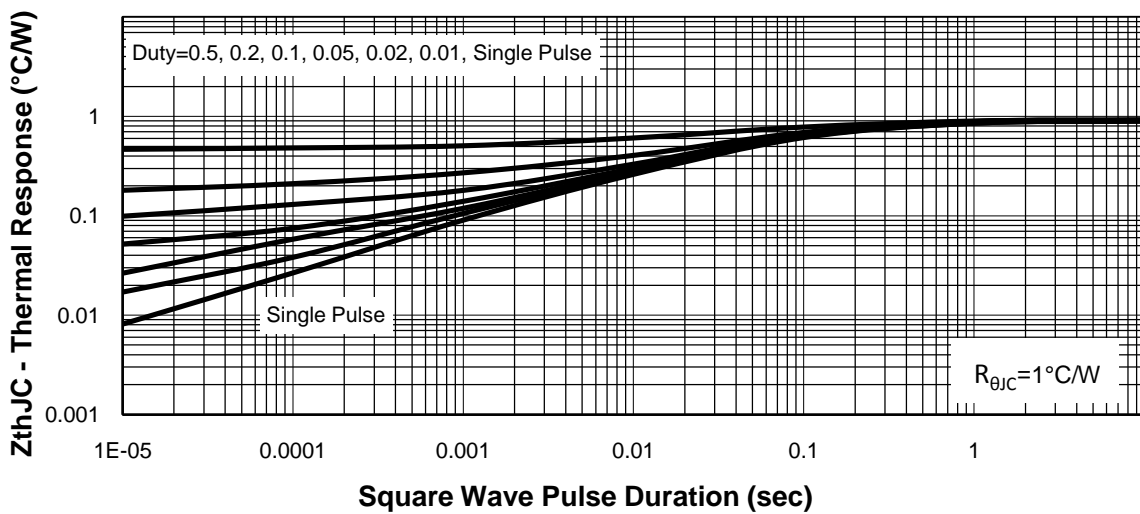
Ordering and Marking Information

Device	Marking	Package	Packaging	Quantity	Reel Size	Tape width
RU40120S	RU40120S	TO263	Tube	50	-	-

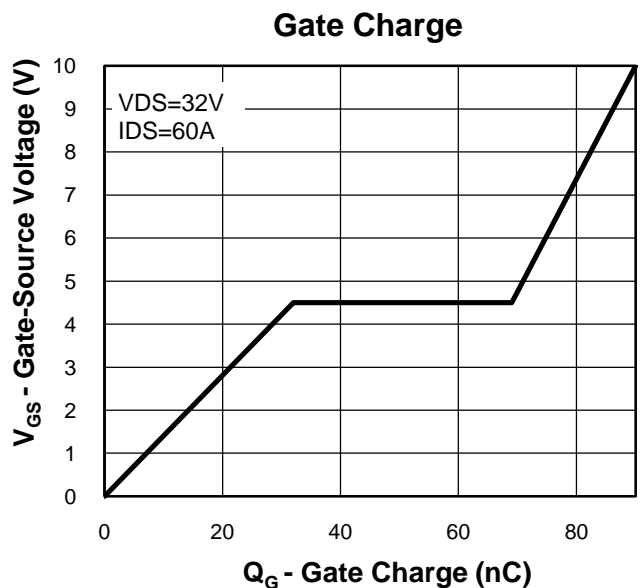
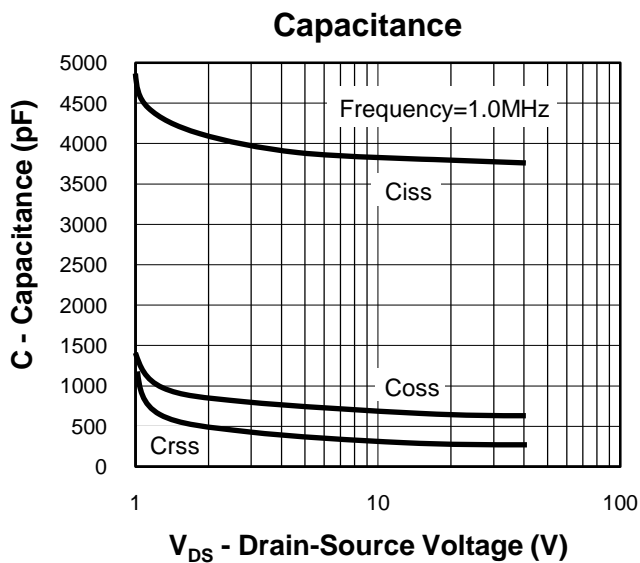
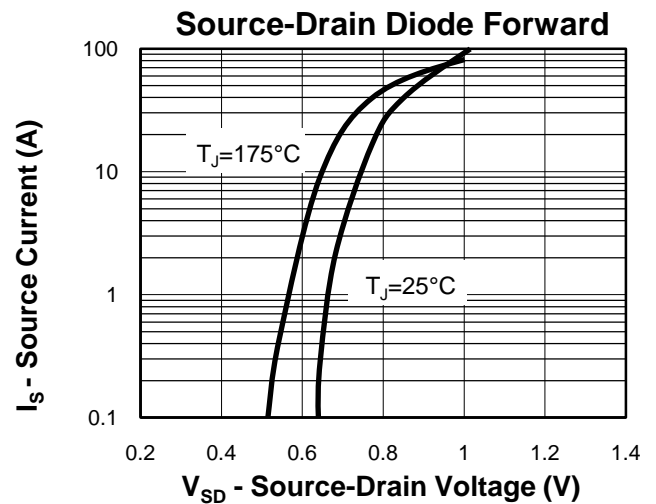
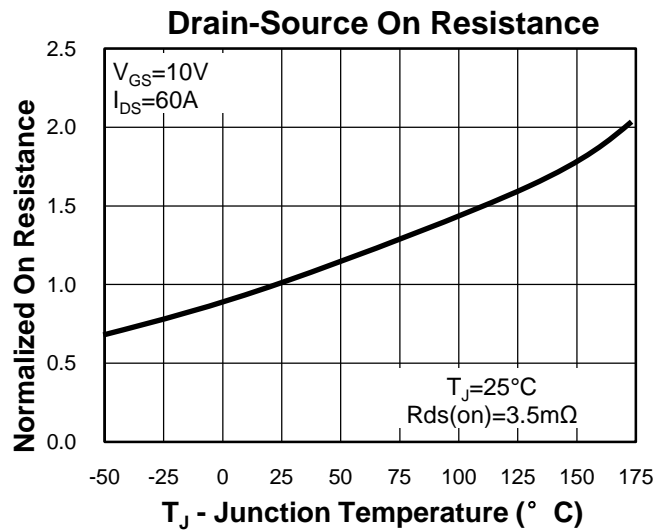
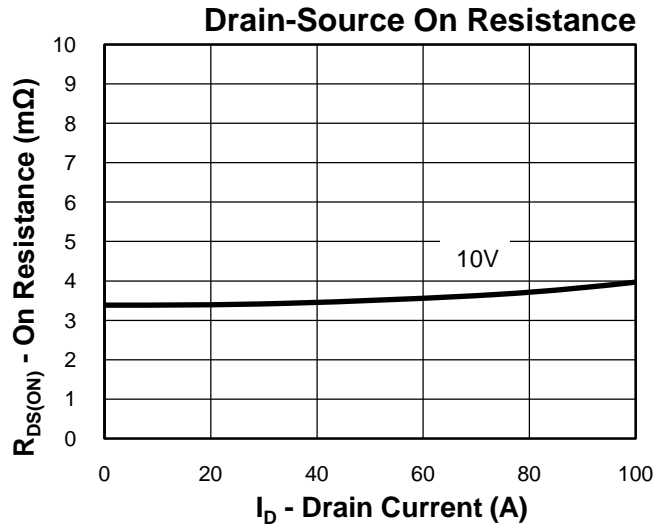
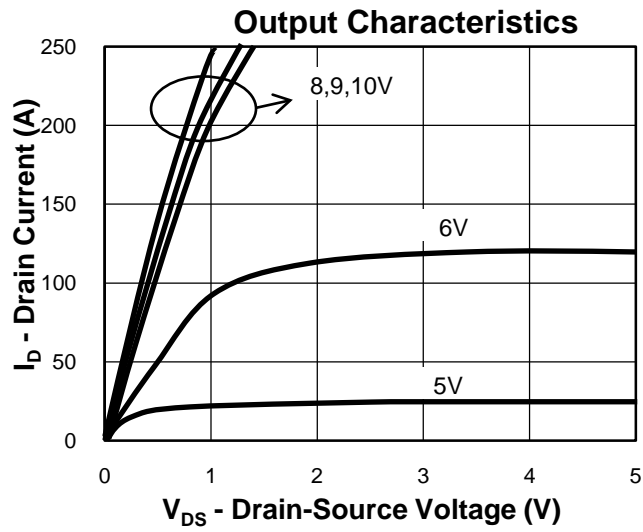
Typical Characteristics



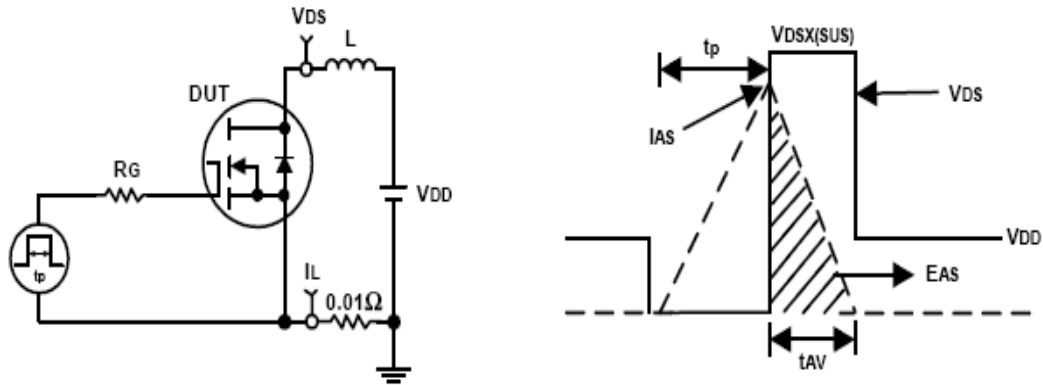
Thermal Transient Impedance



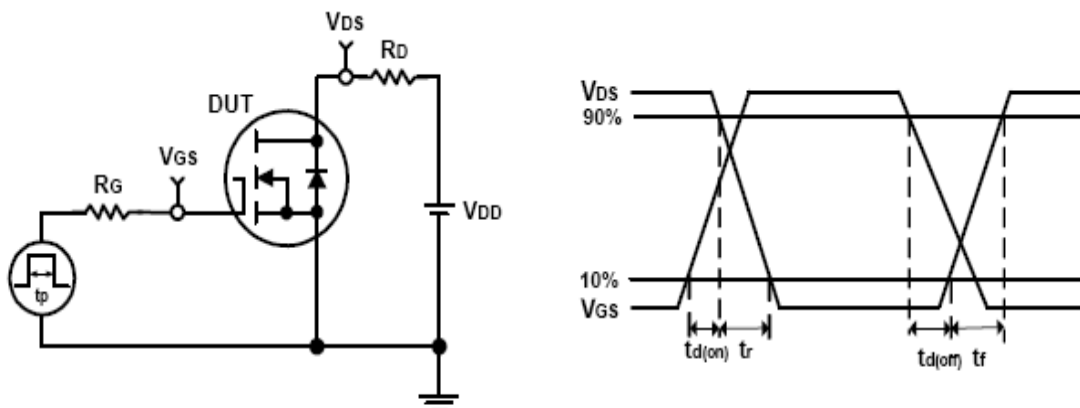
Typical Characteristics



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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