RU70E15L

N-Channel Advanced Power MOSFET

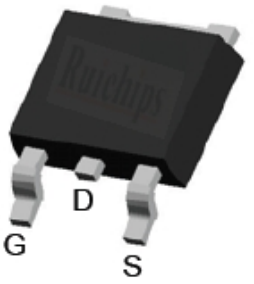
Features

- 70V/15A,
  $R_{DS(ON)}$ =90mΩ(Typ.)@V_{GS}=10V
  $R_{DS(ON)}$ =100mΩ(Typ.)@V_{GS}=4.5V
- Super High Dense Cell Design
- ESD protected
- Reliable and Rugged
- 100% avalanche tested
- Lead Free and Green Devices Available (RoHS Compliant)

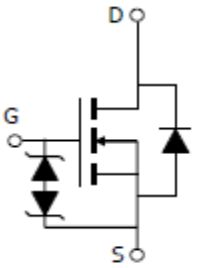
Applications

- Power Management

Pin Description



TO252



N-Channel MOSFET

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T_C=25°C Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		70	V
V_{GSS}	Gate-Source Voltage		±20	
T_J	Maximum Junction Temperature		175	°C
T_{STG}	Storage Temperature Range		-55 to 175	°C
I_S	Diode Continuous Forward Current	T_C=25°C	15	A
Mounted on Large Heat Sink				
I_{DP}	300μs Pulse Drain Current Tested	T_C=25°C	60①	A
I_D	Continuous Drain Current(V_{GS}=10V)	T_C=25°C	15②	A
		T_C=100°C	10.5	
P_D	Maximum Power Dissipation	T_C=25°C	40	W
		T_C=100°C	20	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		3.75	°C/W
Drain-Source Avalanche Ratings				
E_{AS}③	Avalanche Energy, Single Pulsed		12	mJ

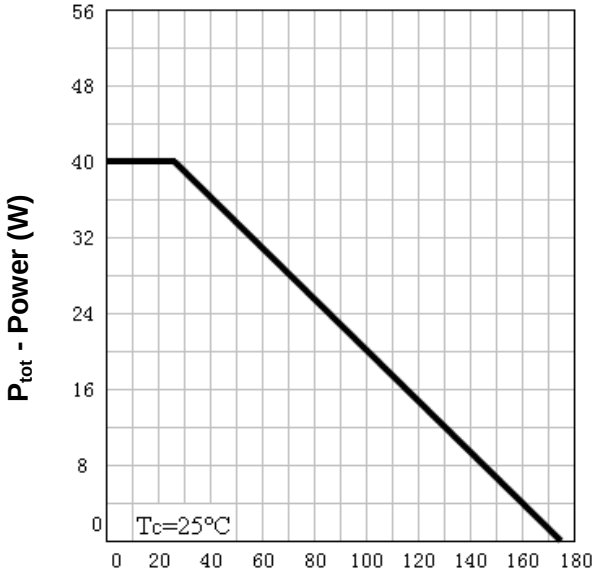
Electrical Characteristics (T_C=25°C Unless Otherwise Noted)

Symbol	Parameter	Test Condition	RU70E15L			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250μA	70			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}= 70V, V_{GS}=0V			1	μA
		T_J=85°C			30	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1.5	2	2.7	V
I_{GSS}	Gate Leakage Current	V_{GS}=±16V, V_{DS}=0V			±10	μA
$R_{DS(ON)}$ ④	Drain-Source On-state Resistance	V_{GS}= 10V, I_{DS}=7.5A		90	105	mΩ
		V_{GS}= 4.5V, I_{DS}=5A		100	125	mΩ
Diode Characteristics						
V_{SD} ④	Diode Forward Voltage	I_{SD}=7.5A, V_{GS}=0V			1.2	V
t_{rr}	Reverse Recovery Time	I_{SD}=7.5A, dI_{SD}/dt=100A/μs		39		ns
Q_{rr}	Reverse Recovery Charge			66		nC
Dynamic Characteristics ⑤						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz		1.3		Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}= 35V, Frequency=1.0MHz		480		pF
C_{oss}	Output Capacitance			70		
C_{rss}	Reverse Transfer Capacitance			40		
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=35V, R_L=4.7Ω, I_{DS}=7.5A, V_{GEN}= 10V, R_G=6Ω		14		ns
t_r	Turn-on Rise Time			19		
$t_{d(OFF)}$	Turn-off Delay Time			32		
t_f	Turn-off Fall Time			15		
Gate Charge Characteristics ⑤						
Q_g	Total Gate Charge	V_{DS}=56V, V_{GS}= 10V, I_{DS}=7.5A		14	18	nC
Q_{gs}	Gate-Source Charge			3		
Q_{gd}	Gate-Drain Charge			5		

Notes: ①Pulse width limited by safe operating area.
②Calculated continuous current based on maximum allowable junction temperature.
③Limited by T_{Jmax}, I_{AS} =7A, V_{DD} = 48V, R_G = 50Ω , Starting T_J = 25°C.
④Pulse test ; Pulse width≤300μs, duty cycle≤2%.
⑤Guaranteed by design, not subject to production testing.

Typical Characteristics

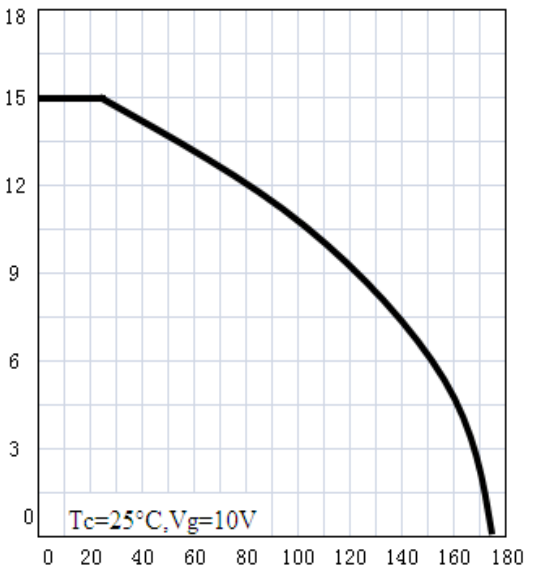
Power Dissipation



P_{tot} - Power (W)

T_j - Junction Temperature (°C)

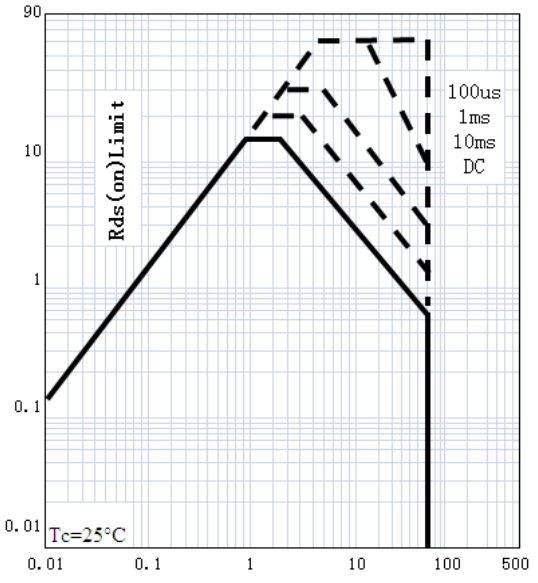
Drain Current



I_D - Drain Current (A)

T_j - Junction Temperature (°C)

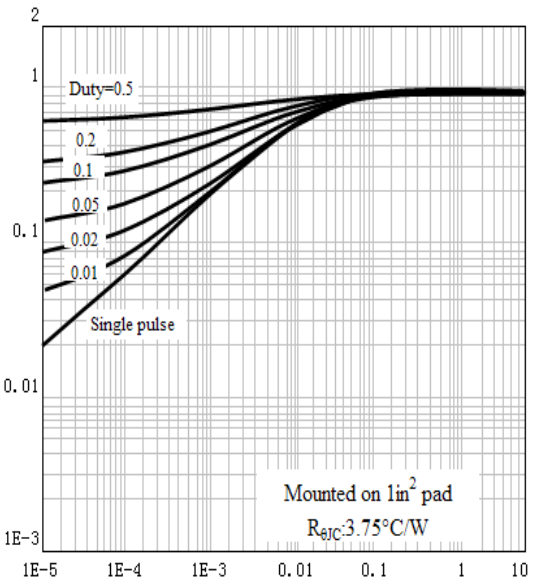
Safe Operation Area



I_D - Drain Current (A)

V_{DS} - Drain-Source Voltage (V)

Thermal Transient Impedance

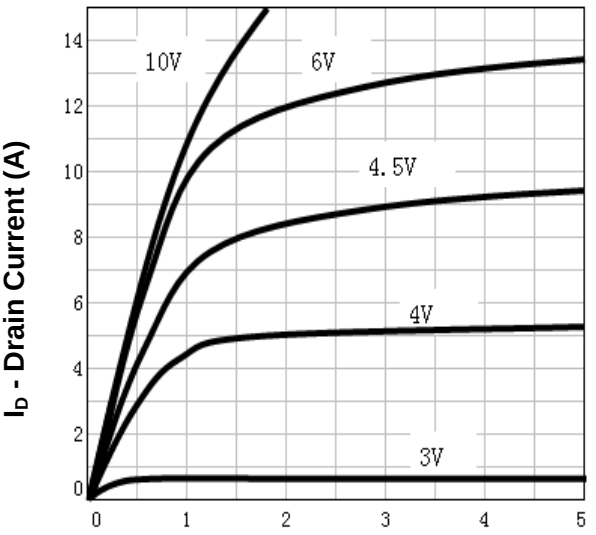


Normalized Effective Transient

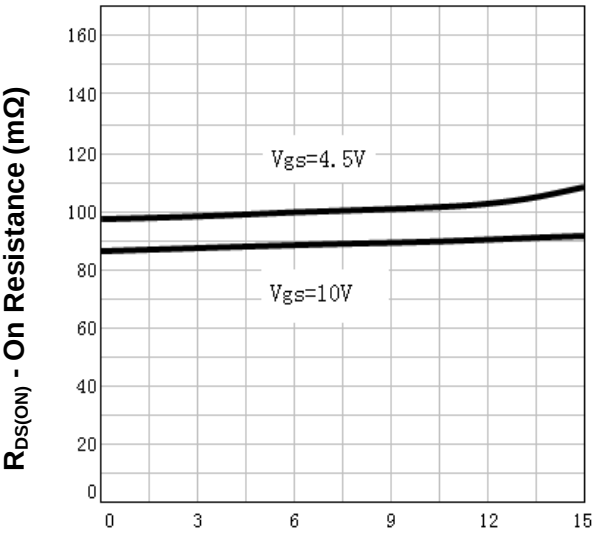
Square Wave Pulse Duration (sec)

Typical Characteristics

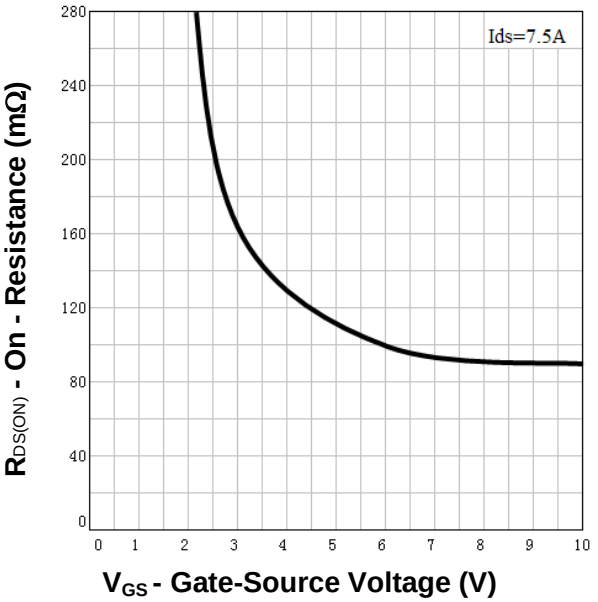
Output Characteristics



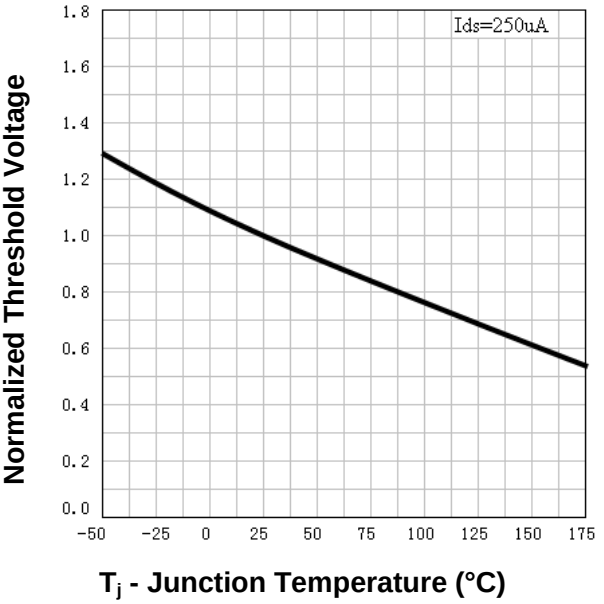
Drain-Source On Resistance



Drain-Source On Resistance

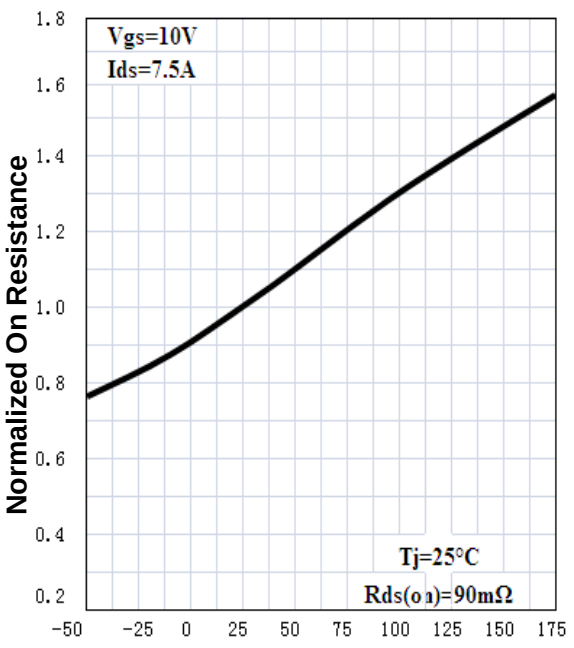


Gate Threshold Voltage



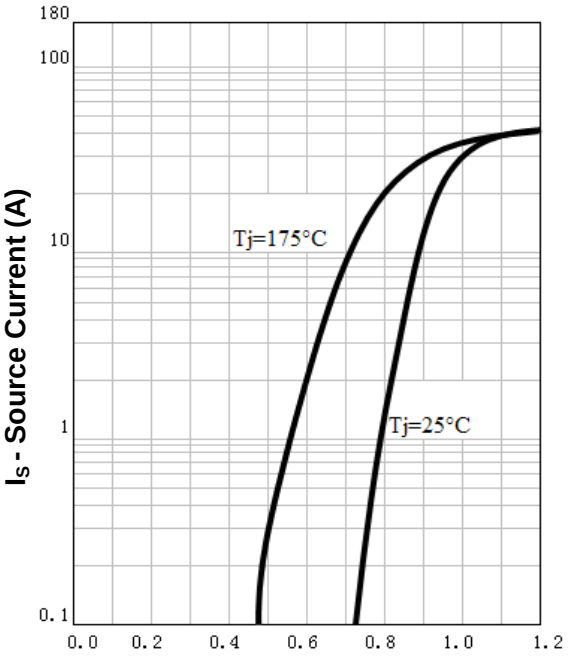
Typical Characteristics

Drain-Source On Resistance



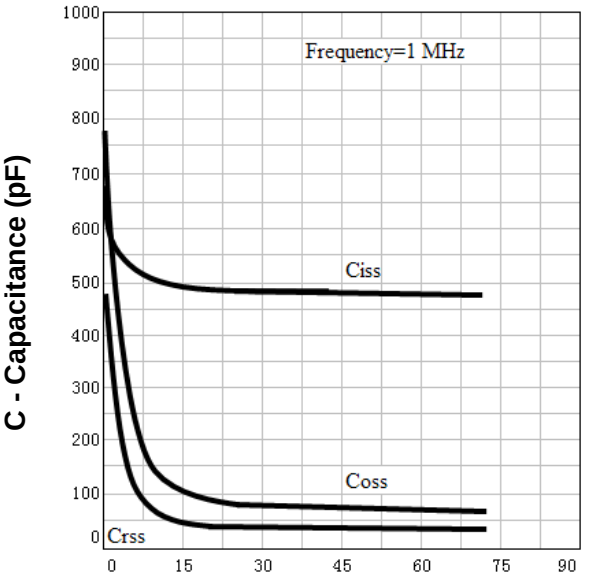
T_j - Junction Temperature (°C)

Source-Drain Diode Forward



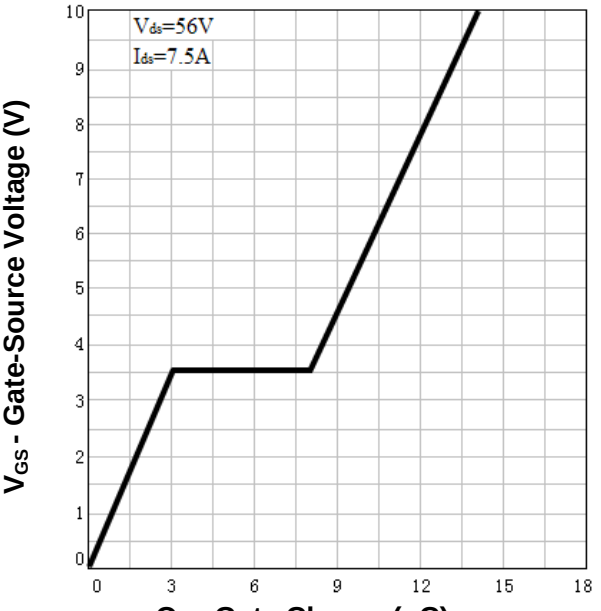
V_{SD} - Source-Drain Voltage (V)

Capacitance



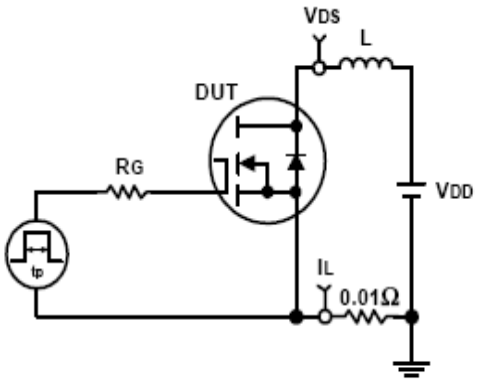
V_{DS} - Drain-Source Voltage (V)

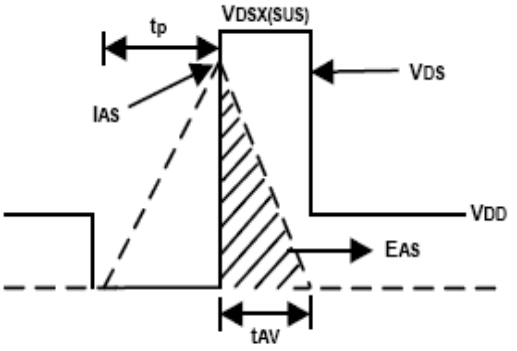
Gate Charge



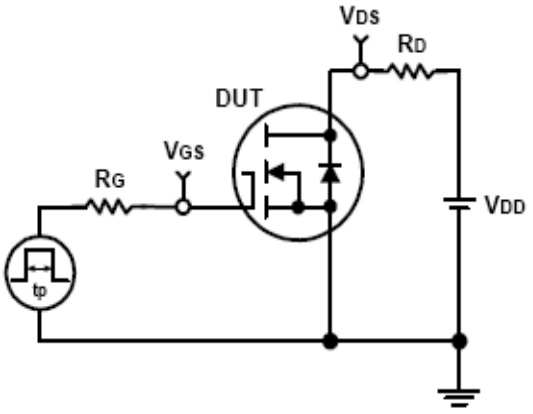
Q_G - Gate Charge (nC)

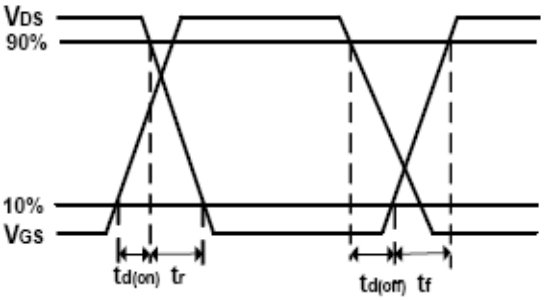
Avalanche Test Circuit and Waveforms





Switching Time Test Circuit and Waveforms



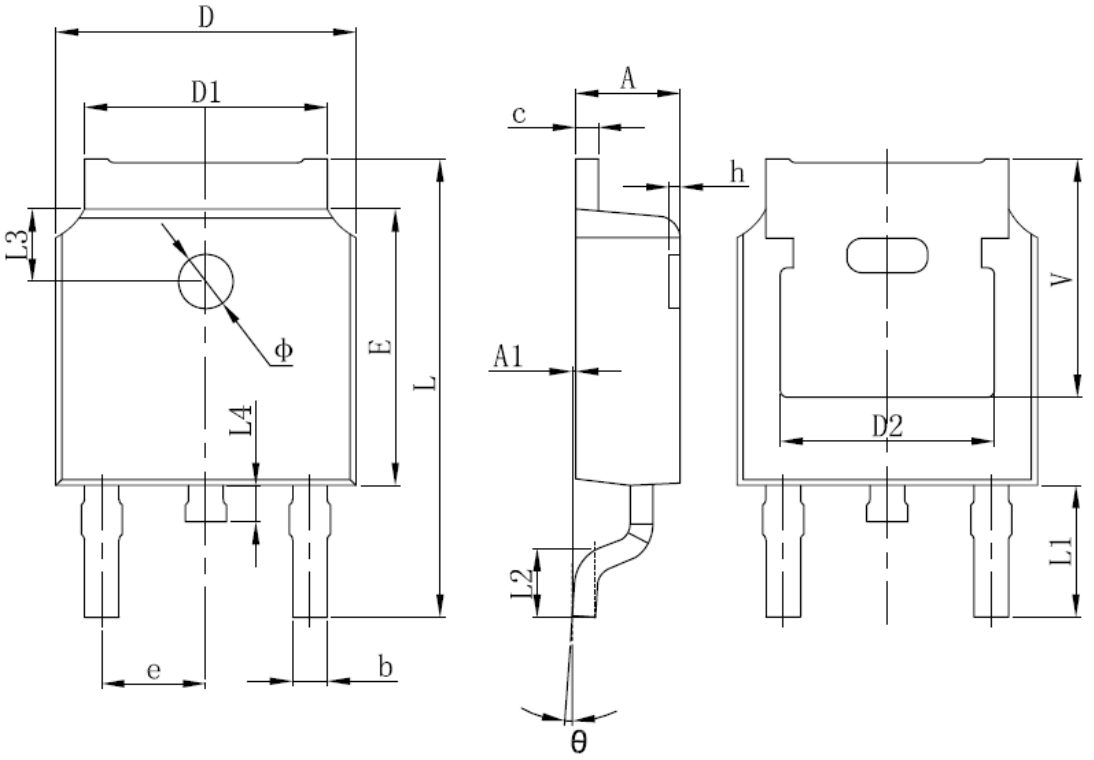


Ordering and Marking Information

Device	Marking	Package	Packaging	Quantity	Reel Size	Tape width
RU70E15L	RU70E15L	TO-252	Tape&Reel	2500	13''	16mm

Package Information

TO252-2L



SYMBOL	MM		INCH		SYMBOL	MM		INCH	
	MIN	MAX	MIN	MAX		MIN	MAX	MIN	MAX
A	2.200	2.400	0.087	0.094	L	9.800	10.400	0.386	0.409
A1	0.000	0.127	0.000	0.005	L1	2.900 REF.		0.114 REF.	
b	0.660	0.860	0.026	0.034	L2	1.400	1.700	0.055	0.067
C	0.460	0.580	0.018	0.023	L3	1.600 REF.		0.063REF.	
D	6.500	6.700	0.256	0.264	L4	0.600	1.000	0.024	0.039
D1	5.100	5.460	0.201	0.215	Φ	1.100	1.300	0.043	0.051
D2	4.830 REF.		0.190 REF.		θ	0°	8°	0°	8°
E	6.000	6.200	0.236	0.244	h	0.000	0.300	0.000	0.012
e	2.186	2.386	0.086	0.094	V	5.350 REF.		0.211 REF.	

ALL DIMENSIONS REFER TO JEDEC STANDARD
DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS

Customer Service

Worldwide Sales and Service:
Sales@ruichips.com

Technical Support:
Technical@ruichips.com

Investor Relations Contacts:
Investor@ruichips.com

Marcom Contact:
Marcom@ruichips.com

Editorial Contact:
Editorial@ruichips.com

HR Contact:
HR@ruichips.com

Legal Contact:
Legal@ruichips.com

Shen Zhen RUICHIPS Semiconductor CO., LTD
Room 501, the 5floor An Tong Industrial Building,
NO.207 Mei Hua Road Fu Tian Area Shen Zhen City, CHINA

TEL: (86-755) 8311-5334
FAX: (86-755) 8311-4278
E-mail: Sales-SZ@ruichips.com