

SDA

FOUT

/INT

Ц<u>н</u>

臣

LVEL

SHIFT

핅

Timer

Register

Interrupts

Controlle

Cloc

and Calende

FOUT

Controller

Divide

DTCXO

- Charge from VDD to backup battery connected to VBAT is switches to the backup battery.
- This circuit is optimal to backup with a secondary battery and a large capacitor.
- Alarm function
- Available readout temperature data from embedded temp



R Ş

EDLC

or

secondary #

battery

+ С

Battery backup connection example (2)

VBAT

Real time clock module

Signal Name	1/0	Function
T1(CE)	input	Use by the manufacture for testing. (Do not connect externally.)
SCL	input	Serial clock input pin.
FOUT	Output	The pin outputs the reference clock signal. (CMOS output)
VBAT	-	Battery supply. This pin has charge capability to backup battery.
Vdd	-	Connected to a positive power supply
FOE	input	The input pin for the FOUT output control.
/ INT	Output	Interrupt output (N-ch. open drain).
GND	-	Connected to a ground
T2(Vpp)	-	Use by the manufacture for testing. (Do not connect externally.)
SDA	I/O	Data input and output pin.

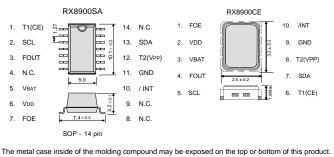
Specifications (characteristics)

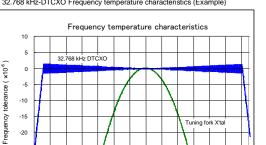
VBAT

	Electrical Characteristics										
	Item	Symb ol	Conditions		Min.	Тур.	Max.	Unit			
	Operating voltage	Vdd	VDD Interface voltage		e	2.5	3.0	5.5	V		
	Temp. compensated Voltage	oltage VTEM Temp. compensated voltage		oltage	2.0	3.0	5.5	V			
	Clock supply voltage	VCLK	Internal clock			1.6	3.0	5.5	V		
	Operating temperature	TOPR	No condensation		-40	+25	+85 ^{*1}	°C			
	Stability	Δf/f	UA	Ta = -40 °C to +85 °C		±3.4 *2			× 10 ⁻⁶		
			UB	Ta = -40 °C to +85 °C							
			UC	Ta = -30 °C to +70 °C		±5.0 *3					
	Current consumption (1)	loo1	fSCL=0Hz, /INT=V _{DD} , FOE =GND V _{DD=VBAT} FOUT: OFF Temp. Compensation interval 2.0 s.		Vdd = 5V	-	0.72	1.5	μA		
	Current consumption (2)	IDD2			Vdd = 3V	-	0.70	1.4	μΛ		

*¹) Please contact us about +85 °C < TOPR

*2) Equivalent to ±9 seconds of month deviation. *3) Equivalent to ±13 seconds of month deviation.





32.768 kHz-DTCXO Frequency temperature characteristics (Example)

Funing fork X

75 85

* Refer to application manual for details.

(Unit:mm)

SEIKO EPSON CORPORATION

- possible. VDD voltage drop(VDET3) detection and automatically
- Timer function
- Timer period is adjustable in 1/4096 second from 4095 minutes.
- Available dual-alarm, weekly and monthly.
- Temp. sensor function
 - sensor. (Bank.2_Add17h)

Terminal connection / External dimensions

-20

This purely cosmetic and does not have any effect on quality, reliability or electrical spece

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

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Explanation of the mark that are using it for the catalog

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ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

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