TOSHIBA RF POWER AMPLIFIER MODULE

S-AV34

ORF POWER AMPLIFIER MODULE for VHF BAND

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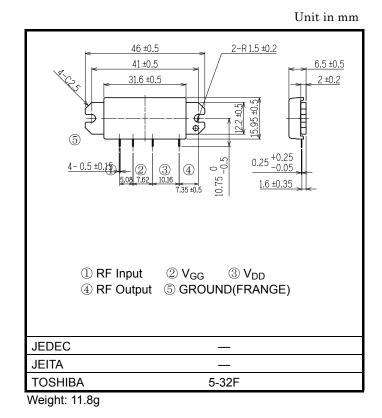
ABSOLUTE MAXIMUM RATINGS (Tc = 25° C, Z_G = Z_L = 50Ω)

CHARACTERISTIC	SYMBOL	TEST CONDITION	RATING	UNIT
DC Supply Voltage	V _{DD}	V _{GG} =0V, Pi =0mW	20	V
DC Supply Voltage	V _{GG}	V _{DD} ≦10.8V, Pi =0mW	8	V
Input Power	Pi	V _{DD} ≦10.8V	20	dBmW
Junction Temperature	T _{j MAX}		150	°C
Storage Temperature Range	T _{stg}		-40~110	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Caution: This absolute maximum rating given in a sheet guarantees each item independently. When two items or more of maximum rated items joins a device at once. It becomes the outside of a guarantee. Please design in circuit to make it always operate within this regulation also on the worst condition.



PACKAGE OUTLINE

ELECTRICAL CHARACTERISTICS (Tc = 25°C, Z_G = 50 Ω)

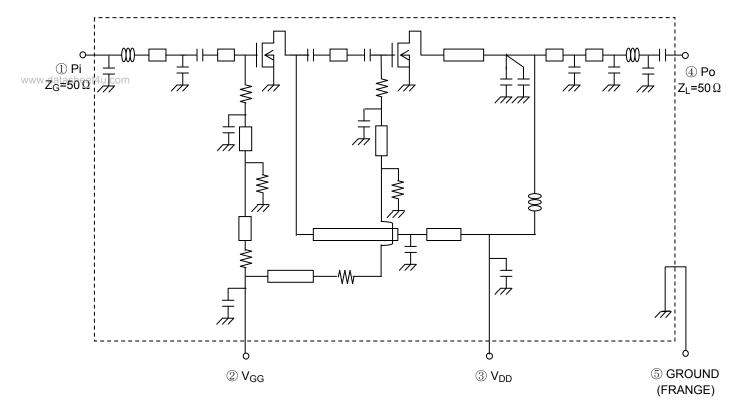
	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	Frequency Range	f _{range}	—	150	_	165	MHz
vww.datas	Input Power	Pi	V_{DD} = 10.8V, I_{DD} = 2.8A (V_{GG} = adjust) Po = 39dBmW, Z_L = 50 Ω	_	_	6	dBmW
	Output Power 1	Po1	V_{DD} = 10.8V, V_{GG} = 5V, Pi = 12dBmW Z_L = 50 Ω	43	_	_	dBmW
	Total Efficiency	η_{T}	V_{DD} = 10.8V, Po = 39dBmW (Pi= adjust) Z _L = 50 Ω	23			%
	Drain Current	I _{DD}				3	А
	Second Harmonic	2nd HRM	V_{DD} = 10.8V, I_{DD} = 2.8A (V_{GG} = adjust) Po = 39dBmW (Pi= adjust), Z_L = 50 Ω	_		-30	dB
	Harmonic	HRM				-30	dB
	Adjacent-Channel Power Ratio	ACP	$ \begin{array}{l} V_{DD} = 10.8V, \ I_{DD} = 2.8A \ (V_{GG} = adjust) \\ Po = 39dBmW \ (Pi= adjust), \ Z_L = 50 \ \Omega \\ Modulated \ Wave : \pi \ \pi / 4 \cdot DQPSK \\ (\alpha = 0.5, \ 32kbps) \\ Band \ Width : \ 16kHz \\ Frequency \ Offset : \ 25kHz \end{array} $		_	-34	dB
	Rate of Adjustment for Input Load	VSWRin	Input VSWR (When RF output pin connects 50 Ω Load)	_	_	3	_
	Gate Bias Current	I _{GGBias}	V_{DD} = 10.8V, I_{DD} = 2.8A (V_{GG} = adjust) Po = 39dBmW (Pi= adjust), Z_L = 50 Ω After that Pi OFF	_	_	5	mA
	Output Power 2	Po2	V_{DD} = 8.7V, V_{GG} = 5V, Pi = 5dBmW Z_L = 50 Ω	36	_	_	dBmW
	Ralative Phase Variation			_	_	20	٥
	Load Mismatch	_	V_{DD} = 10.8V, I _{DD} = 2.8A (V _{GG} = adjust) Po = 39dBmW (Pi= adjust, Z _L = 50 Ω) VSWR LOAD 20: 1 ALL PHASE	No Degradation			_
s	Stability	_	V _{DD} = 8.7 to 13.0V, V _{GG} = 0 to 5V Pi = -40 to 39 dBmW VSWR LOAD 2.5: 1 ALL PHASE	All spurious output than 60dB below desired signal			_

Caution

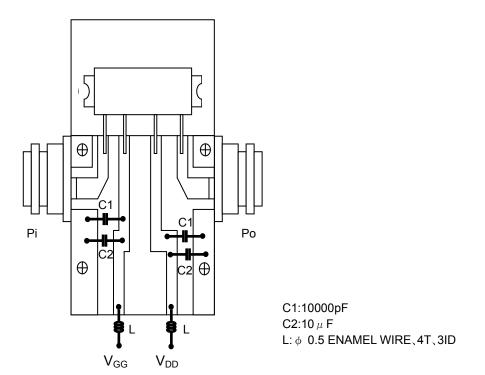
- This product has intersetting cap. Please pay attention for exceeding stress and foreign matter in your application. And not to take away the cap.
- Do not break, cut, crush or dissolve chemically. Dispose of this product properly according to law. Do not intermingle with normal industrial or domestic waste.
- This product is electrostatic sensitivity, please handle with caution.

TOSHIBA

SCHEMATIC



TEST FIXTURE



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RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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