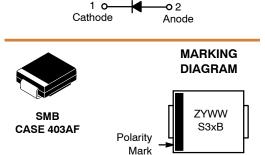


Rectifiers, Surface Mount, 3 A, 50 V-1000 V

S3AB-S3MB

Features

- Glass Passivated Chip Junction
- High Surge Current Capacity
- Low Forward Voltage: 1.15 V Maximum
- UL Flammability 94V-0 Classification
- MSL 1 per J-STD-020
- RoHS Compliant / Green Molding Compound
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free and Halide Free Devices



Ζ WW S3xB

= Assembly Plant Code = Year

= Work Week = Specific Device Code

x = A, B, D, G, J, K, M

ORDERING INFORMATION

See detailed ordering and shipping information on page 3 of this data sheet.

ABSOLUTE MAXIMUM RATINGS Values are at $T_A = 25$ °C unless otherwise noted.

		Value							
Symbol	Parameter	S3AB	S3BB	S3DB	S3GB	S3JB	S3KB	S3MB	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	50	100	200	400	600	800	1000	V
V _{RMS}	RMS Reverse Voltage	35	70	140	280	420	560	700	V
V _R	DC Blocking Voltage	50	100	200	400	600	800	1000	Α
I _{F(AV)}	Average Forward Rectified Current	3			Α				
I _{FSM}	Peak Forward Surge Current: 8.3 ms Single Half Sine-Wave Superimposed on Rated Load	80			Α				
T_J	Operating Junction Temperature Range	-55 to 150			°C				
T _{STG}	Storage Temperature Range	–55 to 150			°C				

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS Values are at T_A = 25°C unless otherwise noted. (Note 1)

	Symbol	Parameter	Value	Unit
	$R_{\theta JA}$	Typical Thermal Resistance, Junction-to-Ambient	148	°C/W
Ī	$\Psi_{\sf JL}$			°C/W

^{1.} Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm per JESD51-3.

ELECTRICAL CHARACTERISTICS Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _F	Instantaneous Forward Voltage (Note 2)	I _F = 3 A	-	_	1.15	V
I _R	Reverse Current at Rated V _R	T _J = 25°C	-	_	10	μΑ
		T _J = 125°C	-	_	250	
t _{rr}	Reverse Recovery Time	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A	-	1.5	-	μs
CJ	Junction Capacitance	V _R = 4 V, f = 1 MHz	-	40	-	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1

2. Pulse test with PW = 300 μ s, 1% duty cycle.

TYPICAL PERFORMANCE CHARACTERISTICS

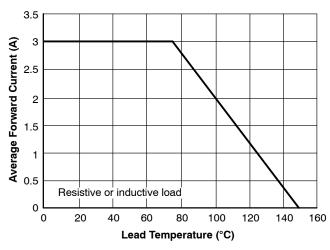


Figure 1. Forward Current Derating Curve

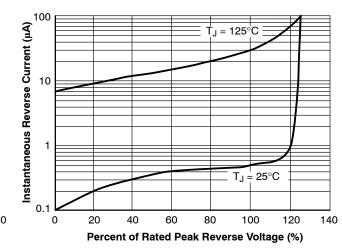


Figure 2. Typical Reverse Characteristics

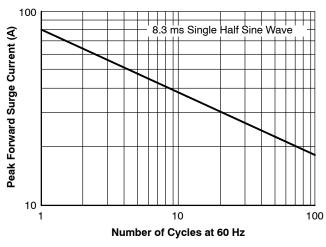


Figure 3. Maximum Non-Repetitive Forward Surge Current

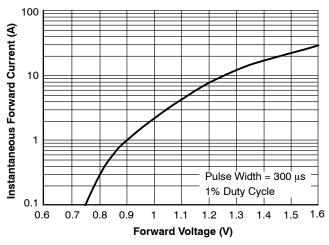


Figure 4. Typical Forward Characteristics

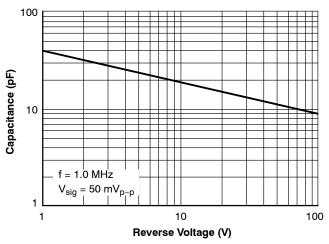


Figure 5. Typical Junction Capacitance

S3AB-S3MB

TEST CIRCUIT DIAGRAM

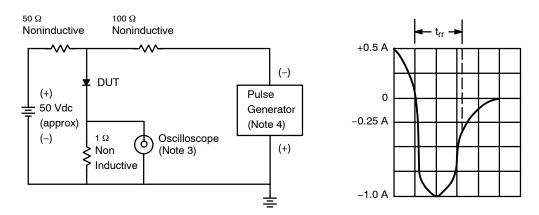


Figure 6. Reverse Recovery Time Characteristic and Test Circuit Diagram

NOTES:

- 3. Rise Time = 7 ns max. Input Impedance = 1 M Ω , 22 pF
- 4. Rise Time = 10 ns max. Source Impedance = 50 Ω , 22 pF

ORDERING INFORMATION

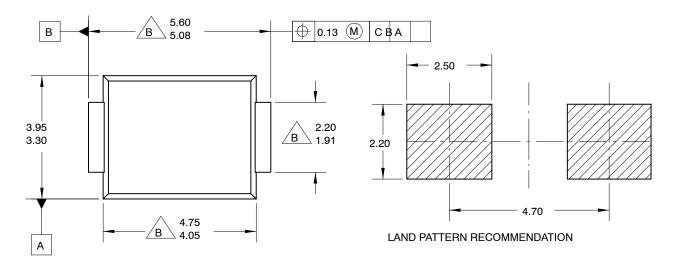
Part Number	Device Code Marking	Package	Shipping [†]		
S3AB, NRVS3AB*	S3AB	SMB 3000 / Tape &			
S3BB, NRVS3BB*	S3BB	(Pb-Free, Halide-Free)			
S3DB, NRVS3DB*	S3DB				
S3GB, NRVS3GB*	S3GB				
S3JB, NRVS3JB*	S3JB				
S3KB, NRVS3KB*	S3KB	1			
S3MB, NRVS3MB*	S3MB	1			

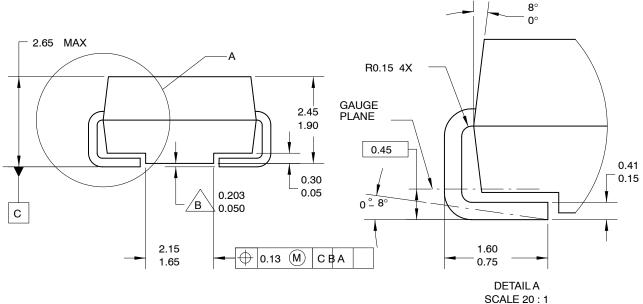
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable

SMB CASE 403AF ISSUE O

DATE 31 AUG 2016





NOTES:

A. EXCEPT WHERE NOTED CONFORMS TO

√ JEDEC DO214 VARIATION AA.

 $/\mathsf{B}\setminus\mathsf{DOES}$ NOT COMPLY JEDEC STD. VALUE.

- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DIMENSION AND TOLERANCE AS PER ASME
- Y14.5-1994.
- F. LAND PATTERN STD. DIOM5336X240M.

DOCUMENT NUMBER:	98AON13441G	Electronic versions are uncontrolled except when accessed directly from the Document Repositor Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	SMB		PAGE 1 OF 1		

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales