

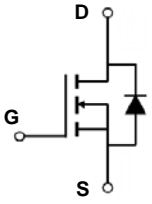
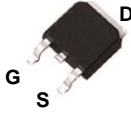
Features

- $V_{DS}=65V, I_D=71A$
 $R_{ds(on)}(typ)=6.8m\Omega @ V_{GS}=10V$
- 100% Avalanche Tested
- 100% Rg Tested
- Lead-Free (RoHS Compliant)

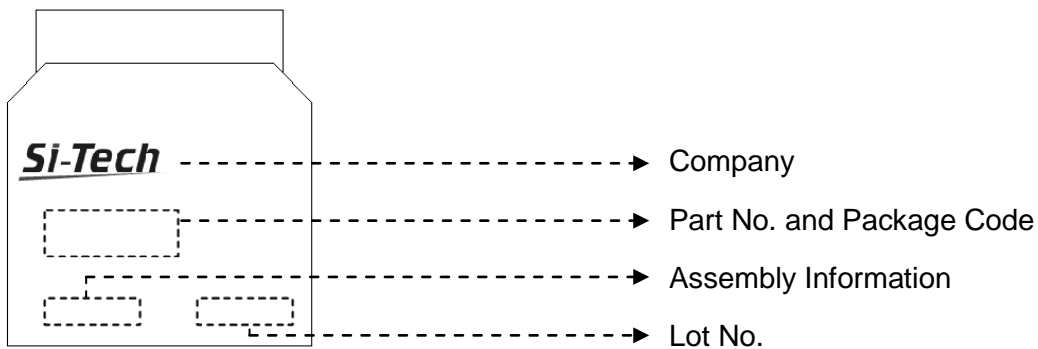
Applications

- DC Motor Control
- DC-DC Converters
- BMS
- SMPS
- Automotive Environment

Internal Circuit and Pin Description

	
Package	TO-252
Package Code	M

Package Marking



Absolute Maximum Ratings ($T_C=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DSS}	Drain-Source Voltage	65	V
I_D	Continuous Drain Current ($T_C=25^{\circ}C$)	71	A
	Continuous Drain Current ($T_C=100^{\circ}C$)	45	A
I_{DM}	Pulsed Drain Current (Note 1)	284	A
V_{GS}	Gate-Source Voltage	± 25	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	240	mJ
P_D	Maximum Power Dissipation ($T_C=25^{\circ}C$)	83	W
	Derating Factor above $25^{\circ}C$	0.66	W/ $^{\circ}C$
T_J	Operating Junction Temperature Range	-55 to +150	$^{\circ}C$
T_{STG}	Storage Temperature Range	-55 to +150	$^{\circ}C$

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{th j-c}	Thermal Resistance, Junction to case	1.5	°C/W

Electrical Characteristics (T_C=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	65	-	-	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =61.75V, V _{GS} =0V	-	-	1	uA
I _{GSS}	Gate Leakage Current, Forward	V _{GS} =25V, V _{DS} =0V	-	-	100	nA
	Gate Leakage Current, Reverse	V _{GS} =-25V, V _{DS} =0V	-	-	-100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2.4	3	3.6	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =40A	5.4	6.8	8	mΩ
Q _g	Total Gate Charge	V _{DD} =40V	-	72	-	nC
Q _{gs}	Gate-Source Charge	V _{GS} =10V	-	18	-	nC
Q _{gd}	Gate-Drain Charge	I _D =40A (Note 3)	-	22	-	nC
t _{d(on)}	Turn-on Delay Time	V _{DD} =37.5V, V _{GS} =10V	-	27	-	ns
t _r	Turn-on Rise Time	I _D =45A, R _G =4.7Ω	-	25	-	ns
t _{d(off)}	Turn-off Delay Time	T _C =25°C	-	55	-	ns
t _f	Turn-off Fall Time	(Note 3)	-	50	-	ns
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	-	1.1	-	Ω
C _{iss}	Input Capacitance	V _{DS} =25V	-	4168	-	pF
C _{oss}	Output Capacitance	V _{GS} =0V	-	366	-	pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz	-	272	-	pF

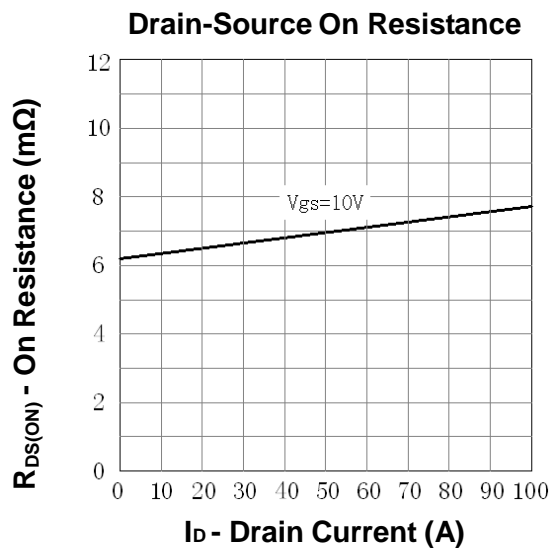
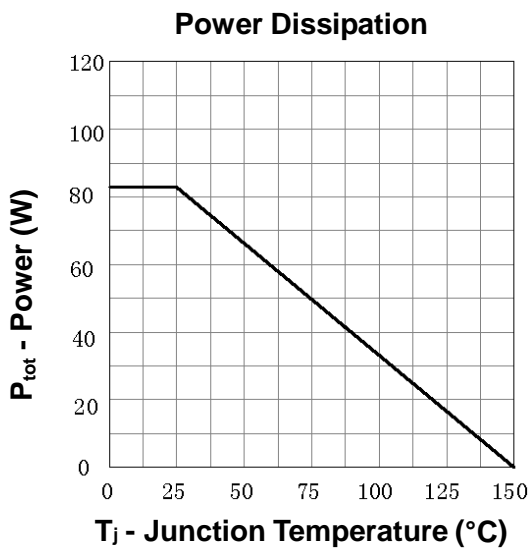
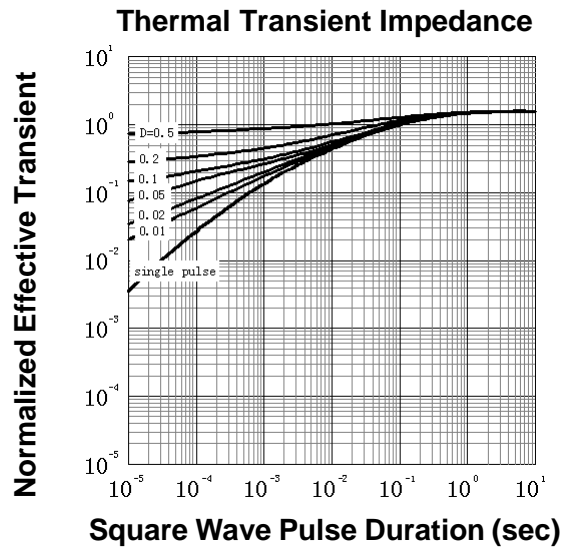
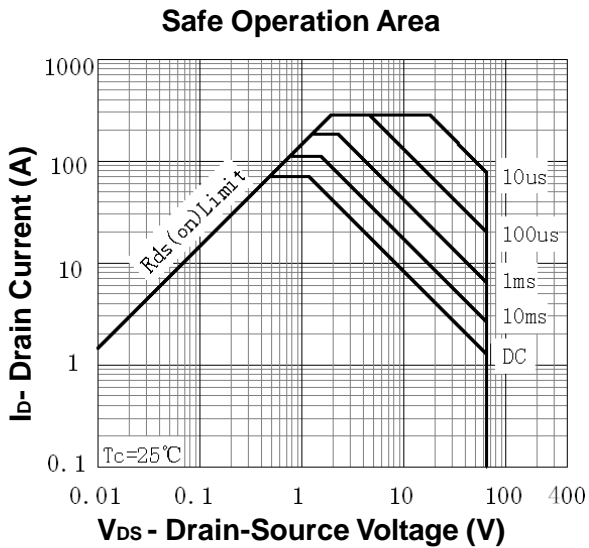
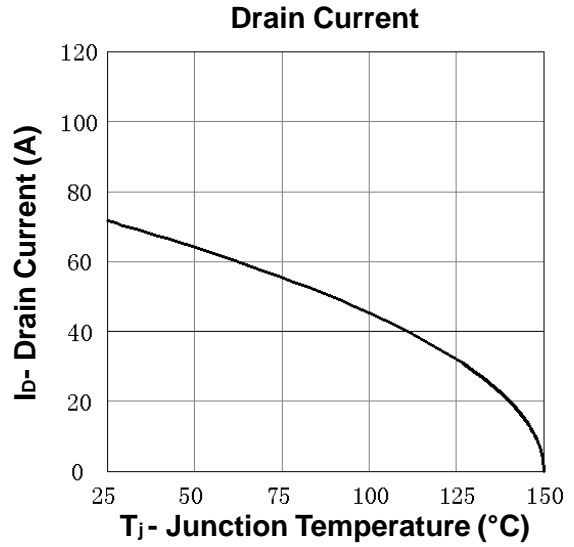
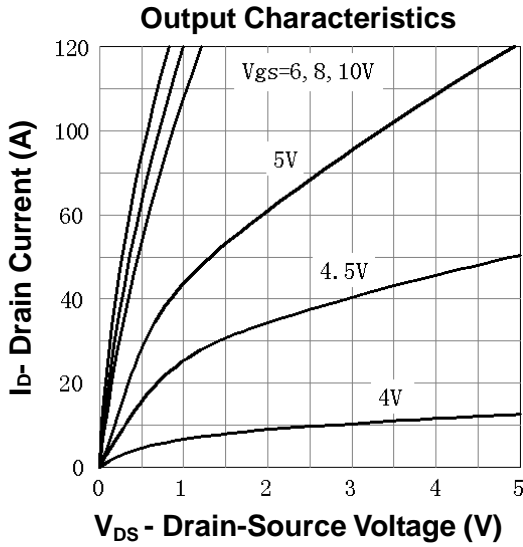
Source-Drain Diode Characteristics (T_C=25°C unless otherwise noted)

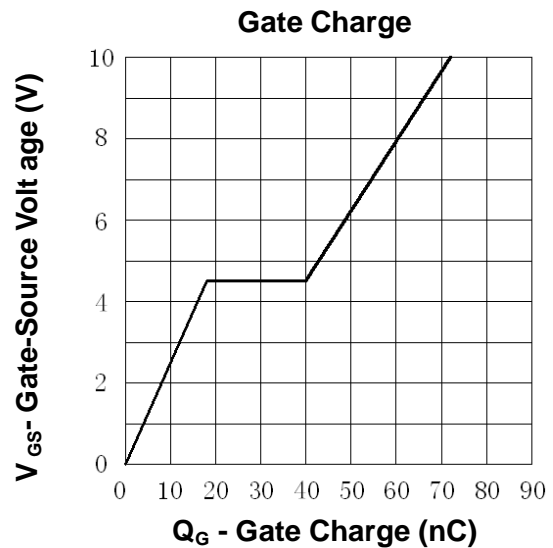
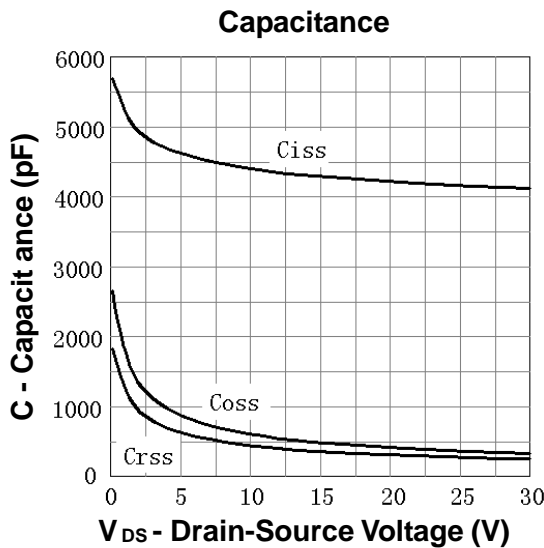
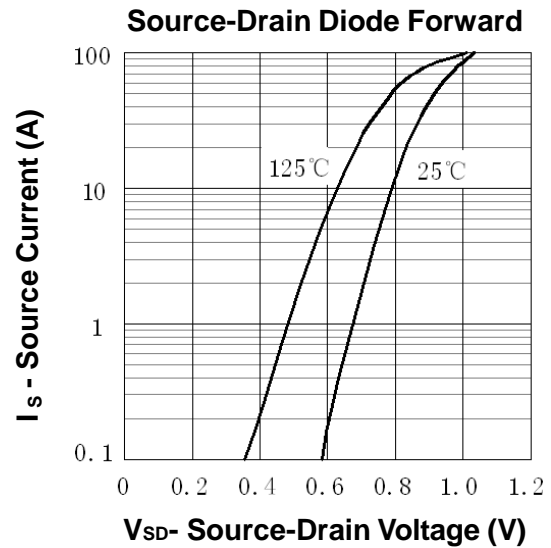
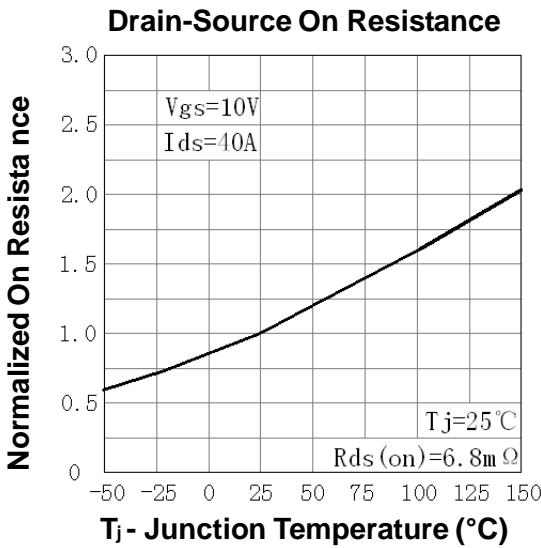
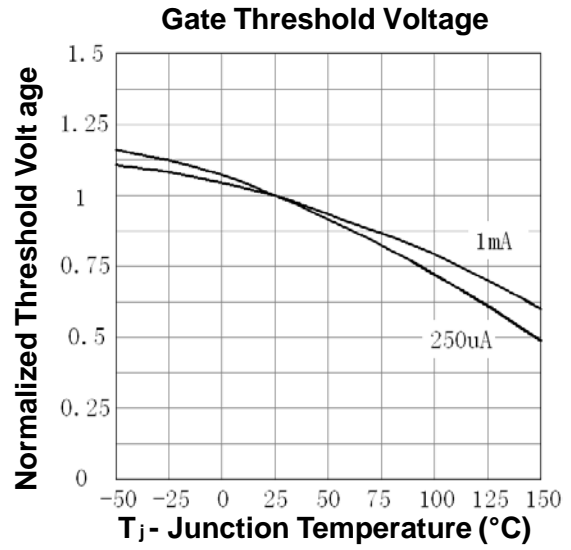
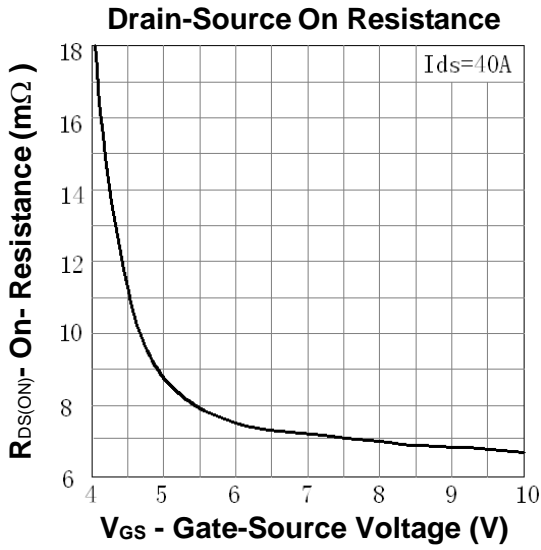
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I _S	Continuous Source Diode Forward Current		-	-	71	A
I _{SM}	Pulsed Source Diode Forward Current (Note 1)		-	-	284	A
V _{SD}	Forward On Voltage	V _{GS} =0V, I _S =45A	-	0.89	1	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =45A	-	35	-	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt = 100A/us	-	54	-	nC

Notes:

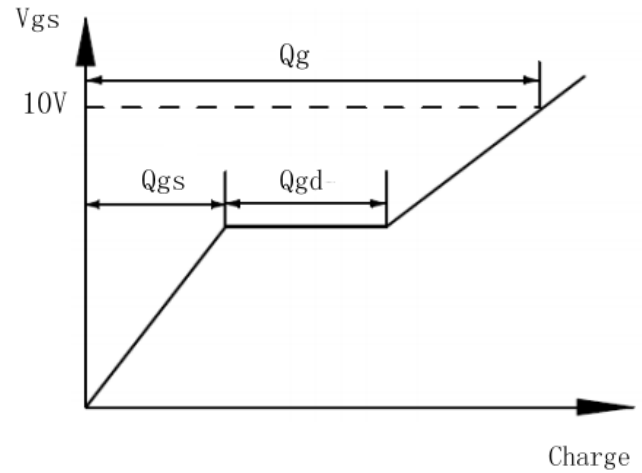
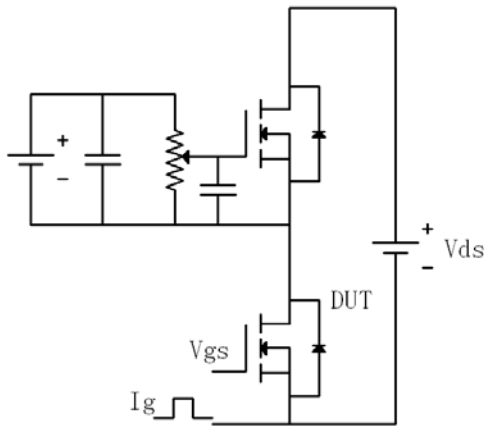
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=0.5mH, V_{DD}=48V, R_G=25Ω, Starting T_J=25°C
3. Pulse Width ≤ 300 us; Duty Cycle ≤ 2%

Typical Characteristics

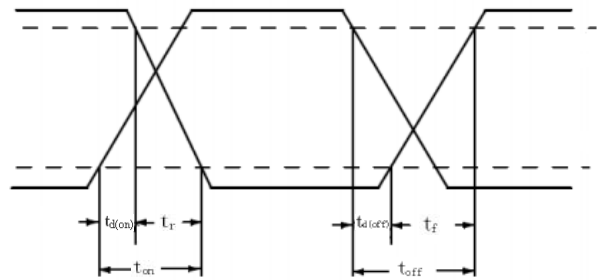
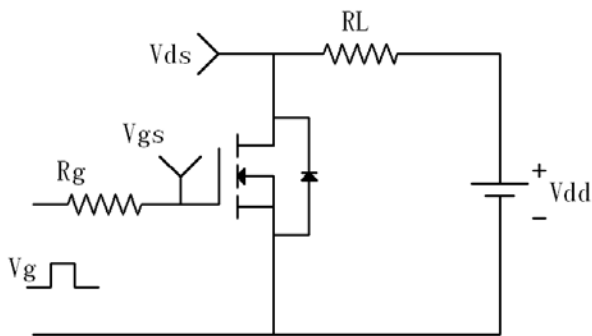




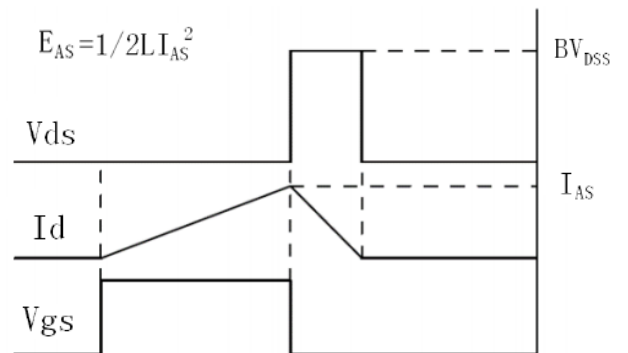
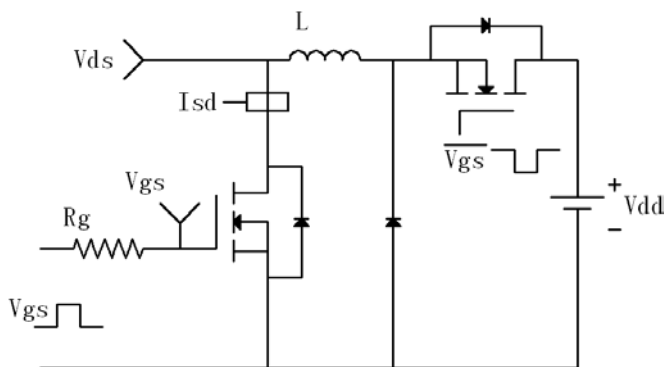
Gate Charge Test Circuit and Waveforms



Switching Time Test Circuit & Waveforms

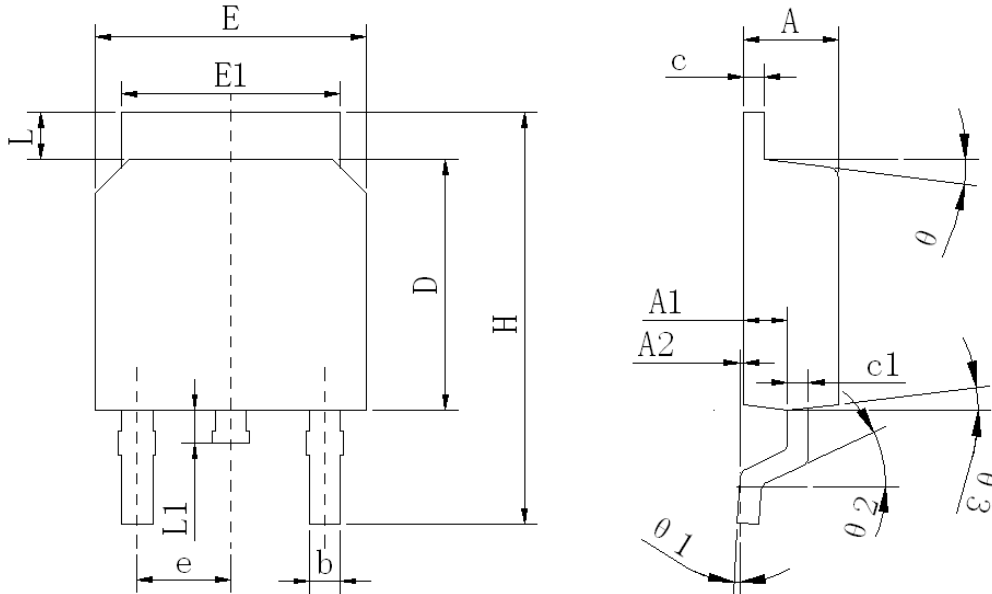


Avalanche Test Circuit & Waveforms

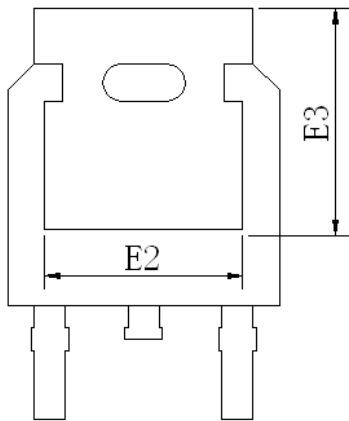


Package Outline

TO252



UNIT:mm



SYMBOL	MIN	NOM	MAX
A	2.25	2.30	2.35
A1	1.02	1.07	1.12
A2	0.05	0.1	0.15
b	0.71	0.76	0.81
c	0.46	0.51	0.56
c1	0.46	0.51	0.56
D	6.05	6.10	6.15
E	6.55	6.60	6.65
E1	5.23	5.33	5.43
E2	4.73	4.83	4.93
E3	5.30	5.40	5.50
e	2.286 BSC		
H	9.82	10.02	10.22
L	0.96	1.01	1.06
L1	0.7	0.8	0.9
θ	5°	7°	9°
θ_1	1°	3°	5°
θ_2	23°	25°	27°
θ_3	5°	7°	9°