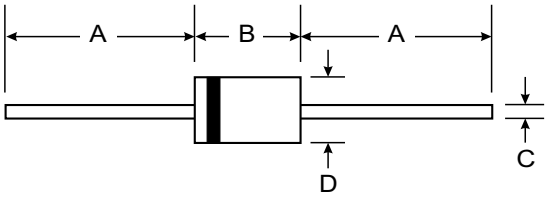
SB240 - SB260

2.0A SCHOTTKY BARRIER RECTIFIER

ADVANCE INFORMATION

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- Surge Overload Rating to 50A Peak
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Plastic Material - UL Flammability Classification 94V-0



DO-41 Plastic		
Dim	**Min**	**Max**
A	25.40	—
B	4.06	5.21
C	0.71	0.864
D	2.00	2.72
All Dimensions in mm		

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.4 grams (approx.)
- Mounting Position: Any
- Marking: Type Number

Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	SB240	SB260	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	40	60	V
RMS Reverse Voltage	$V_{R(RMS)}$	28	42	V
Average Rectified Output Current (Note 1) @ T_A = 25°C	I_O	2.0		A
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50		A
Forward Voltage @ I_F = 2.0A	V_{FM}	0.50	0.70	V
Peak Reverse Current at Rated DC Blocking Voltage @T_A = 25°C	I_{RM}	0.5		mA
Typical Junction Capacitance (Note 2)	C_j	190		pF
Operating and Storage Temperature Range	T_j, T_{STG}	-55 to +125		°C

Notes: 1. Pulse width ≤300 μs, duty cycle ≤2%.
2. Measured at 1.0 MHz and applied reverse voltage of 5.0V DC.

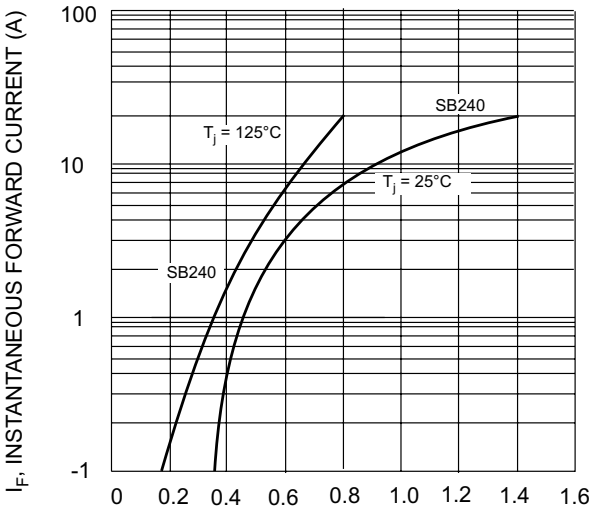


Fig. 1 Typical Fwd. Characteristics

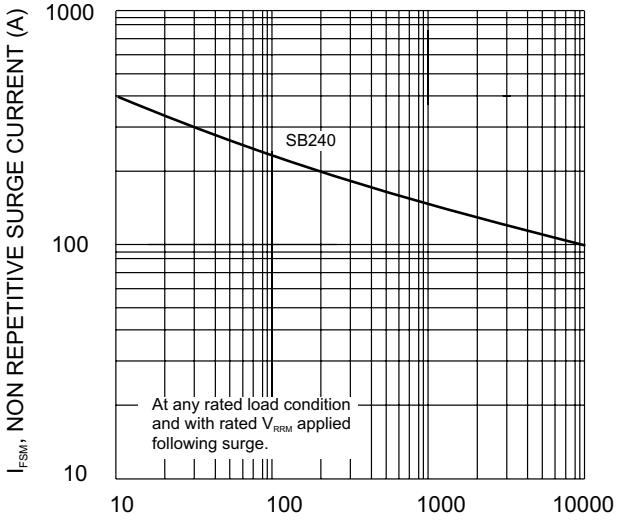


Fig. 2 Max Non-Repetitive Surge Current

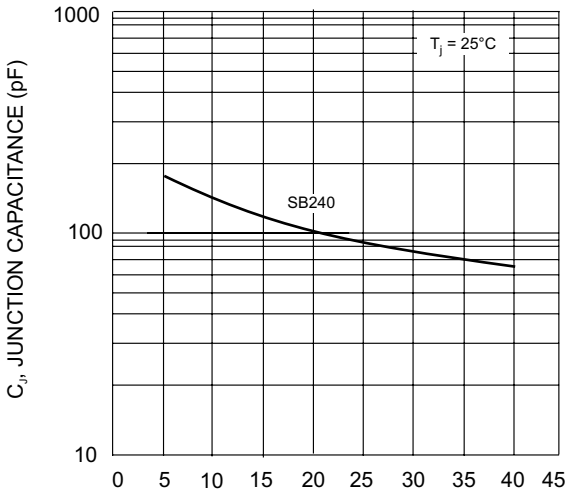


Fig. 3, Typ. Junction Capacitance vs Reverse Voltage