Zibo Seno Electronic Engineering Co., Ltd.

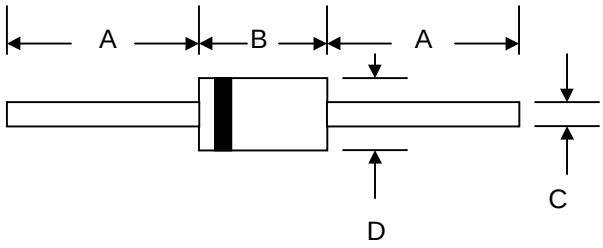
淄博圣诺

SB520 – SB5200

5.0A SCHOTTKY BARRIER DIODE

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications



Mechanical Data

- Case: DO-201AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version,**

DO-201AD		
Dim	**Min**	**Max**
A	25.4	—
B	7.20	9.50
C	1.20	1.30
D	4.80	5.30
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	SB520	SB530	SB540	SB550	SB560	SB580	SB5100	SB5150	SB5200	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	20	30	40	50	60	80	100	150	200	V
RMS Reverse Voltage	$V_{R(RMS)}$	14	21	28	35	42	56	70	105	140	V
Average Rectified Output Current @T_L = 95°C (Note 1)	I_O	5.0									A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	150									A
Forward Voltage @I_F = 5.0A	V_{FM}	0.5			0.75		0.85		0.92		V
Peak Reverse Current @T_A = 25°C At Rated DC Blocking Voltage @T_A = 100°C	I_{RM}	0.5 20							0.02 10		mA
Typical Junction Capacitance (Note 2)	C_j	250									pF
Typical Thermal Resistance (Note 1)	$R_{\theta JA}$	20									°C/W
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150									°C

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

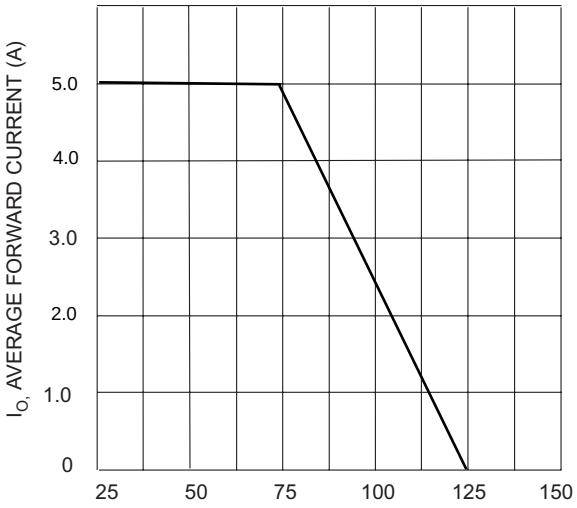


Fig. 1 Forward Current Derating Curve

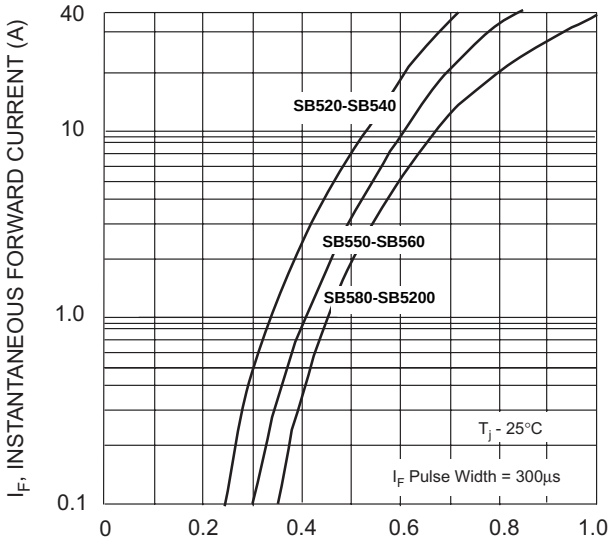


Fig. 2 Typical Forward Characteristics

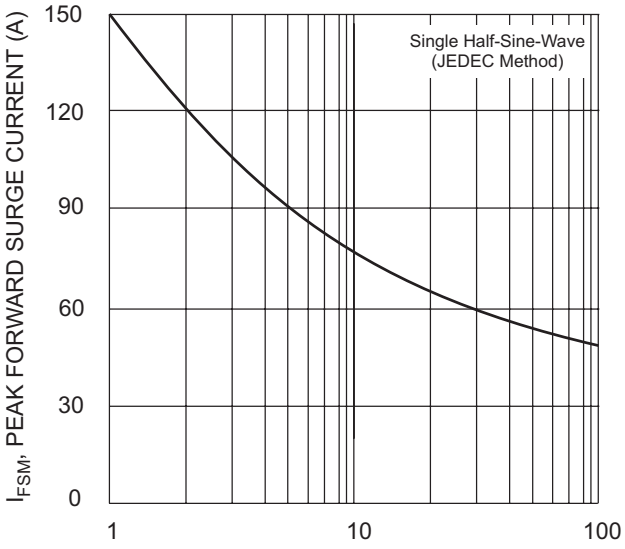


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

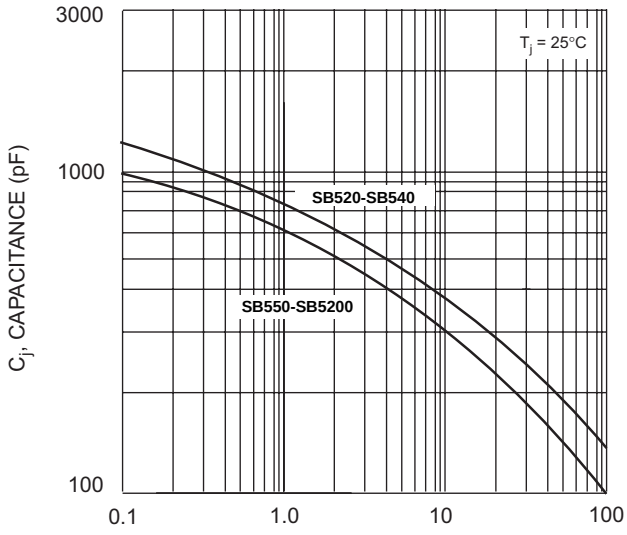


Fig. 4 Typical Junction Capacitance

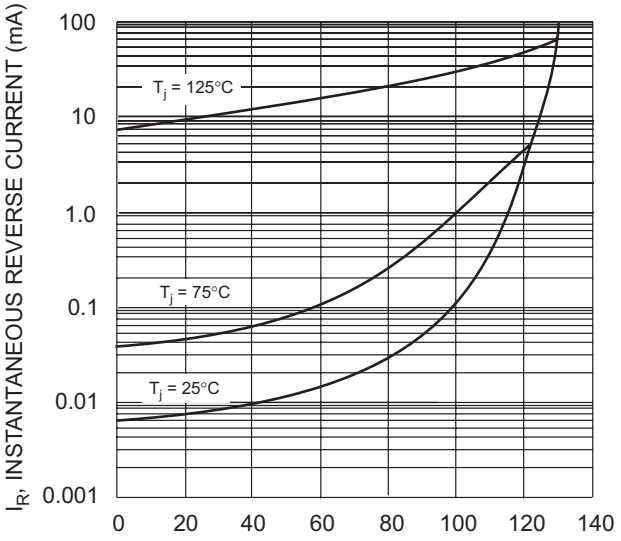


Fig. 5 Typical Reverse Characteristics