SB80W06T

ON Semiconductor®

http://onsemi.com

Schottky Barrier Diode

60V, 8A, Low IR, Monolithic Dual TP Common Cathode

Applications

· High frequency rectification (switching regulators, converters, choppers)

Features

- Low forward voltage (VF max=0.6V)
- · Low switching noise
- · Halogen free compliance

- · Fast reverse recovery time
- · High reliability due to highly reliable planar structure

Specifications

Absolute Maximum Ratings at Ta=25°C

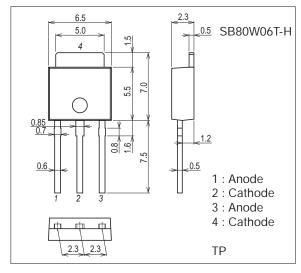
Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	VRRM		60	V
Nonrepetitive Peak Reverse Surge Voltage	V _{RSM}		66	V

Continued on next page.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

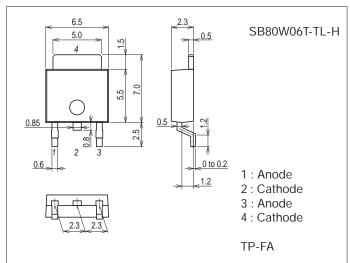
Package Dimensions

unit: mm (typ) 7518-001



Package Dimensions

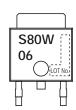
unit: mm (typ) 7003-002



Product & Package Information

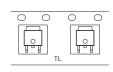
- Package: TP
- JEITA, JEDEC: SC-64, TO-251, SOT-553, DPAK
- Minimum Packing Quantity: 500 pcs./bag

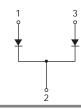
Marking (TP, TP-FA)



- Package: TP-FA
- JEITA, JEDEC: SC-63, TO-252, SOT-428, DPAK
- Minimum Packing Quantity: 700 pcs./reel

Packing Type (TP-FA): TL





Semiconductor Components Industries, LLC, 2013

September, 2013

Electrical Connection

SB80W06T

Continued from preceding page.

Parameter	Symbol	Conditions	Ratings	Unit
Average Output Current	I _O *2	50Hz resistive load, sine wave Tc=130°C	4	А
Average Output Current	I ₀ *1	50Hz resistive load, sine wave Tc=83°C	8	А
Surge Forward Current	I _{FSM}	50Hz sine wave, 1 cycle	40	А
Junction Temperature	Tj		-55 to +150	°C
Storage Temperature	Tstg		-55 to +150	°C

Note) *1. Indicates the total value

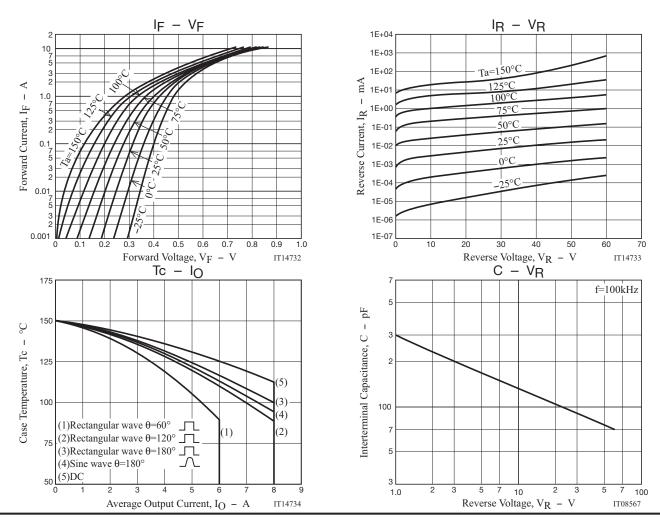
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Parameter	Symbol		min	typ	max	Offic
Reverse Voltage	VR	I _R =1mA, Tj=25°C *2				V
Forward Voltage	VF	I _F =3.0A, Tj=25°C *2			0.6	V
Reverse Current	IR	V _R =30V, Tj=25°C *2			0.1	mA
Interterminal Capacitance	С	V _R =10V, Tj=25°C *2		130		pF
Transient Thermal Resistance	Rth(j-c)	Junction-Case : Smoothed DC		6		°C / W

Note) *2. Value per element

Ordering Information

Device	Package	Shipping	memo
SB80W06T-H	TP	500pcs./bag	Dh Fron and Halagan Fron
SB80W06T-TL-H	TP-FA	700pcs./reel	Pb Free and Halogen Free

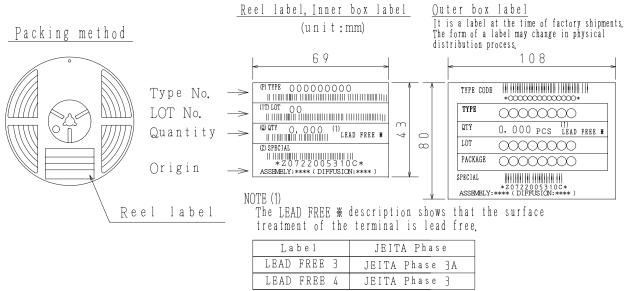


^{*2.} Value per element

Taping Specification SB80W06T-TL-H

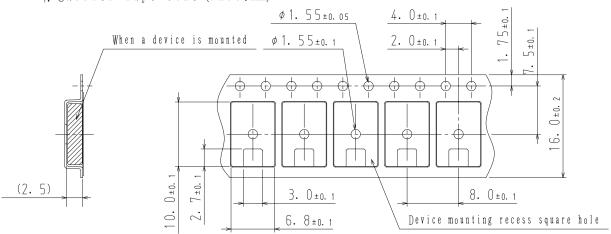
Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	format
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
TP-FA	TP	700	2, 100	12, 600	3 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210

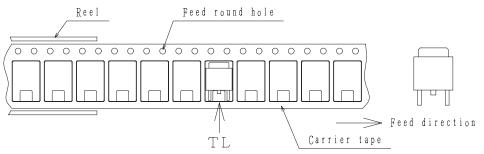


Taping configuration

1. Carrier tape size (unit:mm)



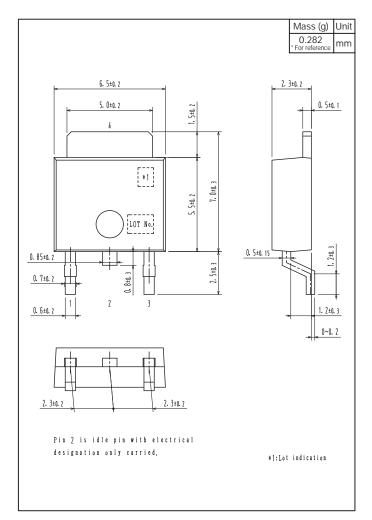
7. Device placement direction

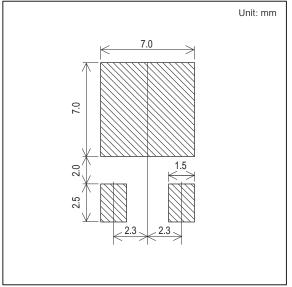


Those with one electrode terminal on the feed hole side · · · · · TL

Outline Drawing SB80W06T-TL-H

Land Pattern Example



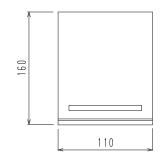


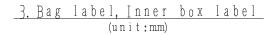
Bag Packing Specification SB80W06T-H

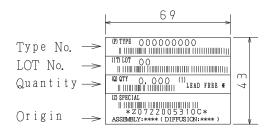
1. Packing Format

Package Name		Maximum Number of devices contained (pcs)					
1 4 0 11 4 5 0 1 1 4 4 11 0	Bag	Inner box	Outer box				
TP		B-1	A-1	A-2			
11	500	10,000	50,000	30,000			
		Packing fo	rmat (Dimensions:m	m (external))			
		Inner box	Outer	bох			
	B-1		A-1	A-2			
		445×225×55	470×250×300	470×250×190			

2. Bag dimensions (unit:mm)







4. Outer box label (unit:mm)

It is a label at the time of factory shipments, The form of a label may change in physical distribution process,

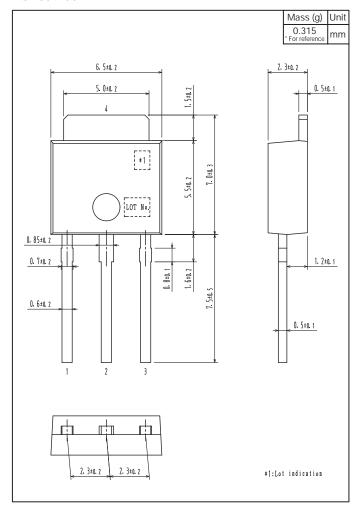


	Label			JEITA Phase
	LEAD	FREE	3	JEITA Phase 3A
ĺ	LEAD	FREE	4	JEITA Phase 3

	TYPE CODE ************************************
	TYPB OOOOOO
	QTY 0, 000 PCS LEAD FREE #
8	LOT OCCOOCOO
	PACKAGE OCCOO
	20722005310C ASSEMBLY:**** (DIFFUSION:****)
	108
	108

Outline Drawing

SB80W06T-H



SB80W06T

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa