

# UNISONIC TECHNOLOGIES CO., LTD

SB8U60 Preliminary DIODE

# 8A SCHOTTKY BARRIER RECTIFIER

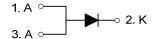
#### DESCRIPTION

The UTC **SB8U60** is a 8A schottky barrier rectifier, it uses UTC's advanced technology to provide the customers with sort, fast switching capability and low forward voltage drop, etc.

### **■** FEATURES

- \* Sort, fast switching capability \* High efficiency
- \* Low forward voltage drop

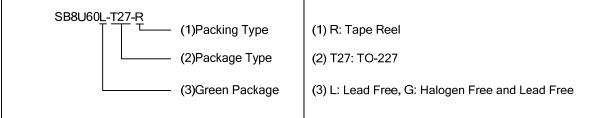




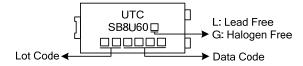
### ■ ORDERING INFORMATION

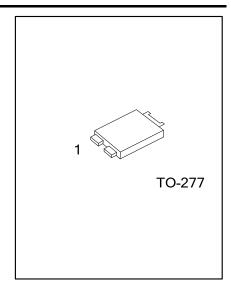
Ordering Number		Deales	Pin Assignment			Daaliina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
SB8U60L-T27-R	SB8U60G-T27-R	TO-277	Α	K	Α	Tape Reel	

Note: Pin Assignment: A: Anode K: Common Cathode



#### MARKING





# ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT	
Peak Repetitive Reverse Voltage	$V_{RRM}$	60	V	
Working Peak Reverse Voltage	$V_{RWM}$	60	V	
DC Blocking Voltage	$V_{RM}$	60	V	
Average Rectified Output Current	I <sub>O</sub>	8	Α	
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	280	А	
Operating Junction Temperature	TJ	-65~ +150	°C	
Storage Temperature	T <sub>STG</sub>	-65~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	$\theta_{JA}$	60	°C/W	
Junction to Case (Note)	$\theta_{JC}$	13	°C/W	

Note: Polymide PCB, 2 oz. Copper.

## ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub> =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> =1.0A, T <sub>J</sub> =25°C		0.30	0.35	V
		I <sub>F</sub> =8A, T <sub>J</sub> =25°C		0.46	0.53	V
		I <sub>F</sub> =8A, T <sub>J</sub> =125°C			0.5	V
Reverse Current (Note 2)	l <sub>D</sub>	V <sub>R</sub> = 60V, T <sub>J</sub> =25°C		0.12	0.6	mA
		V <sub>R</sub> = 60V, T <sub>J</sub> =125°C			100	mA

Notes: 1. Theoretical  $\theta_{JS}$  calculated from the top center of the die straight down to the PCB cathode tab solder junction.

2. Short duration pulse test used to minimize self-heating effect.

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