

CHENMKO ENTERPRISE CO., LTD

SURFACE MOUNT

Lead free devices SCHOTTKY BARRIER RECTIFIER VOLTAGE RANGE 20 - 60 Volts CURRENT 5.0 Amperes



FEATURES

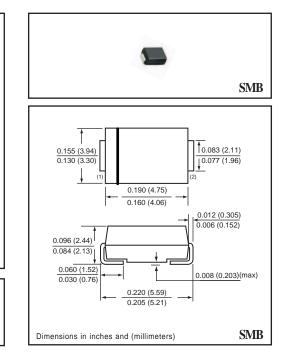
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- For surface mounted applications
- Low profile package
- Built-in strain relief
- Metal silicon junction, majority carrier conduction
- Low power loss, high efficiency
- High current capability, low forward voltage drop
- High surge capability
- For use in low voltage high frequency inverters, free wheeling, and polarity protection applications High temperature soldering guaranteed : 260°C/10 seconds at terminals

MECHANICAL DATA

Case: JEDEC SMB molded plastic Terminals: Solder plated, solderable per MIL-STD-750, Method 2026 Polarity: Color band denotes cathode end Weight: 0.003 ounce 0.093 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%.



MAXIMUM RATINGES (At TA = 25°C unless otherwise noted)

RATINGS	SYMBOL	SBM52PT	SBM53PT	SBM54PT	SBM55PT	SBM56PT	UNITS
Maximum Recurrent Peak Reverse Voltage	Vrrm	20	30	40	50	60	Volts
Maximum RMS Voltage	Vrms	14	21	28	35	42	Volts
Maximum DC Blocking Voltage	VDC	20	30	40	50	60	Volts
Maximum Average Forward Rectified Current	lo	5.0					
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	IFSM	125					Amps
Typical Junction Capacitance (Note 2)	CJ	300					pF
Typical Thermal Resistance (Note 1)	RθJL	12					°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +125					°C

ELECTRICAL CHARACTERISTICS (At TA = 25°C unless otherwise noted)

CHARACTERISTICS		SYMBOL	SBM52PT	SBM53PT	SBM54PT	SBM55PT	SBM56PT	UNITS
Maximum Instantaneous Forward Voltage at 5.	A DC	VF	0.55 0.70		70	Volts		
Maximum Average Reverse Current	@ TA = 25°C		0.5					mAmps
at Rated DC Blocking Voltage	@ TA = 100°C	l IR	20					mAmps
NOTES 1 Thermal Resistance (Junction to Lead) - PC Roard Mounted on 0.55 X 0.55" (14 X 14mm) conner nad area								2002-5

NOTES: 1. Thermal Resistance (Junction to Lead): PC Board Mounted on 0.55 X 0.55" (14 X 14mm) copper pad area. 2. Measured at 1.0 MHz and applied reverse voltage of 4.0 volts.

