



SURFACE MOUNT SUPER BARRIER RECTIFIER

Features

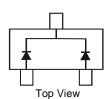
- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Device, (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SC-59
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish annealed over Copper leadframe Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)







Device Schematic

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	20	V
RMS Reverse Voltage	V _{R(RMS)}	14	V
Average Rectified Output Current (See Figure 1)	l ₀	500	mA
Non-Repetitive Peak Forward Surge Current	I _{FSM}	3	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance Thermal Resisitance Junction to Ambient (Note 4)	$R_{ heta JA}$	134	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

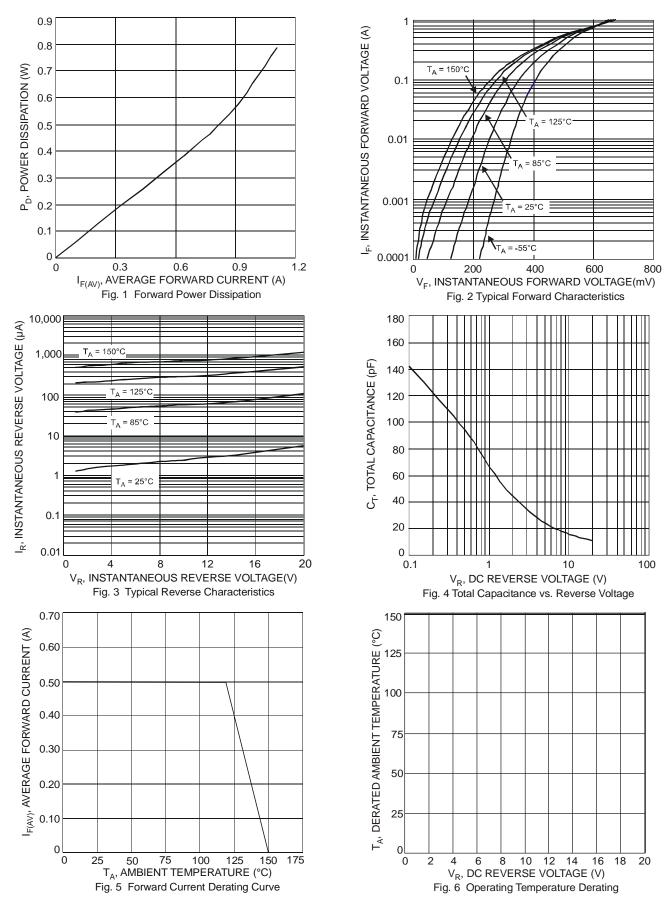
Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
Reverse Breakdown Voltage (Note 3)	$V_{(BR)R}$	20	-	-	V	$I_R = 100 \mu A$		
Forward Voltage Drop	V _F	-	0.51 0.48	0.56 0.53	V	I _F = 0.5A, T _J = 25°C I _F = 0.5A, T _J = 125°C		
Leakage Current (Note 3)	I _R	-	6 0.5	100 20	μA mA	$V_R = 20V, T_J = 25^{\circ}C$ $V_R = 20V, T_J = 125^{\circ}C$		

Notes:

- 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- 3. Short duration pulse test used to minimize self-heating effect.
- 4. Polymide PCB, 2oz, copper minimum recommended pad layout per http://www.diodes.com





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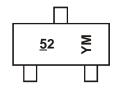


Ordering Information (Note 4)

Part Number	Case	Packaging		
SBR05U20SN-7	SC-59	3000/Tape & Reel		

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

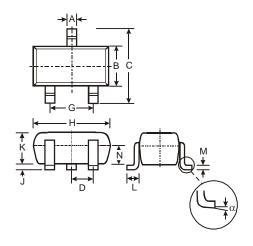


52 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

Date Code Key

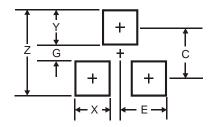
Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	X		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Package Outline Dimensions



SC-59					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
C	2.70	3.00	2.80		
D	-	-	0.95		
G	-	-	1.90		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	4.0
G	1.2
Х	0.9
Y	1.4
С	2.6
E	0.95



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