



10A SBR[®]
SUPER BARRIER RECTIFIER
PowerDI[®]5

Features

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for 200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- Low Forward Voltage Drop
- Excellent High Temperature Stability
- Lead Free Finish, RoHS Compliant (Note 2)





Top View Bottom View

Mechanical Data

- Case: PowerDI[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.093 grams (approximate)

LEFT PIN OBOTTOMSIDE RIGHT PIN OBOTTOMSIDE

Note: Pins Left & Right must be electrically connected at the printed circuit board.

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	45	V
RMS Reverse Voltage	V _{R(RMS)}	32	V
Average Rectified Output Current	I ₀	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	180	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Typical Thermal Resistance Thermal Resistance Junction to Lead Thermal Resistance Junction to Ambient (Note 3 Thermal Resistance Junction to Ambient (Note 4		R _{θJL} R _{θJA} R _{θJA}	3 102 60	°C/W
Operating Temperature Range	$V_R \le 80\% V_{RRM}$ $V_R \le 50\% V_{RRM}$ DC Forward Mode	T_J	-65 to +150 ≤180 ≤200	°C
Storage Temperature Range		T _{STG}	-65 to +175	°C

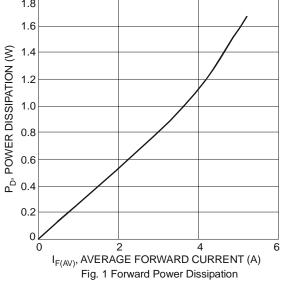
Electrical Characteristics @T_A = 25°C unless otherwise specified

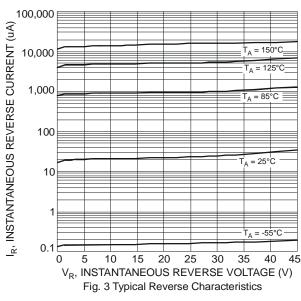
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	45	-	-	V	$I_R = 0.5 \text{mA}$
Forward Voltage Drop		-	-	0.51		$I_F = 8A, T_J = 25^{\circ}C$
	VF	-	0.49	0.55	V	$I_F = 10A, T_J = 25^{\circ}C$
		-	0.47	0.53		$I_F = 10A, T_J = 125^{\circ}C$
		-	0.03	0.45		$V_R = 45V, T_J = 25^{\circ}C$
Leakage Current (Note 1)	I_R	-	-	18	mA	$V_R = 45V, T_J = 100^{\circ}C$
		-	17	100		$V_R = 45V, T_J = 150^{\circ}C$

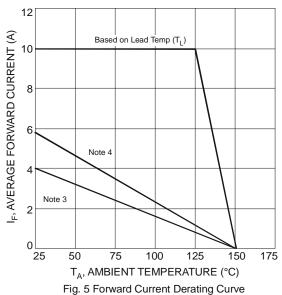
Notes:

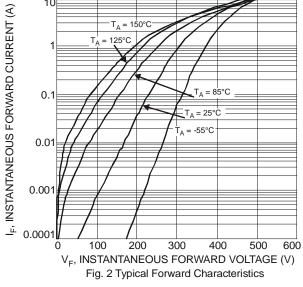
- 1. Short duration pulse test used to minimize self-heating effect.
- 2. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
- 3. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Polymide PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.











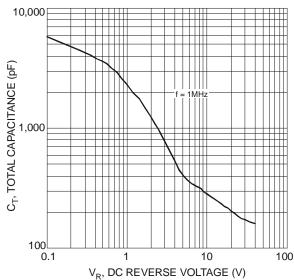


Fig. 4 Total Capacitance vs. Reverse Voltage



Ordering Information (Note 5)

Part Number	Case	Packaging
SBR1045SP5-13	PowerDI [®] 5	5000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

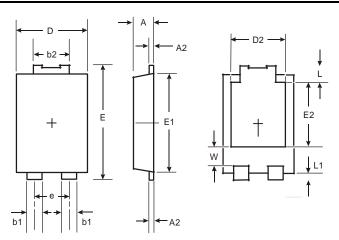
Marking Information



S1045S = Product Type Marking Code

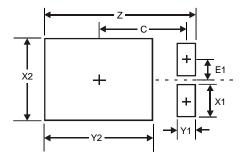
Old = Manufacturers' code marking K = Factory designator YYWW = Date Code Marking YY = Last two digits of year (ex: 08 for 2008) WW = Week code 01 to 52

Package Outline Dimensions



PowerDI [®] 5				
Dim	Min	Max		
Α	1.05	1.15		
A2	0.33	0.43		
b1	0.80	0.99		
b2	1.70	1.88		
D	3.90	4.05		
D2	3.054 Typ			
Е	6.40	6.60		
е	1.84 Typ			
E1	5.30	5.45		
E2	3.549 Typ			
L	0.75	0.95		
L1	0.50	0.65		
W	1.10	1.41		
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	6.6
X1	1.4
X2	3.6
Y1	0.8
Y2	4.7
С	3.87
E1	0.9

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