

Product Summary (@ T_A = +25°C)

V _{RRM} (V)	I _o (A)	V _F Max (V)	I _R Max (mA)
45	10	0.53	0.4

Features and Benefits

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for +200°C maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier (SBR[®]) Technology
- Low-Forward Voltage Drop
- Excellent High-Temperature Stability
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The SBR10A45SP5Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

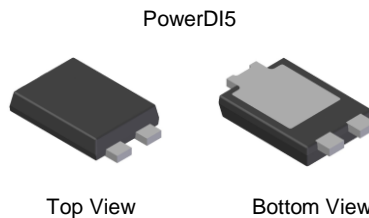
<https://www.diodes.com/quality/product-definitions/>

Applications

- DC/DC converters
- AC/DC adaptors
- Bypass diodes

Mechanical Data

- Package: PowerDI[®]5
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.093 grams (Approximate)



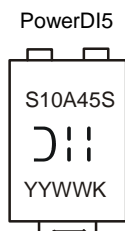
LEFT PIN ○ ○ RIGHT PIN ○ ○ BOTTOM SIDE HEAT SINK

Note: Pins Left & Right must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
SBR10A45SP5-13	PowerDI5	5,000	Tape & Reel
SBR10A45SP5-7	PowerDI5	1,500	Tape & Reel
SBR10A45SP5Q-13	PowerDI5	5,000	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information


S10A45S = Product Type Marking Code
)|| = Manufacturer's Marking
 K = Factory Designator
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 23 for 2023)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _R RM	45	V
Working Peak Reverse Voltage	V _R WM		
DC Blocking Voltage	V _R M		
Average Rectified Output Current	I _O	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	180	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance	—	—	—
Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	102	°C/W
Thermal Resistance Junction to Ambient (Note 6)	R _{θJA}	60	
Operating Temperature Range	V _R ≤ 80% V _R RM	-65 to +150	°C
	V _R ≤ 50% V _R RM	≤180	
	DC Forward Mode (Note 7)	≤200	
Storage Temperature Range	T _{STG}	-65 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V _{(BR)R}	45	—	—	V	I _R = 0.5mA
Forward Voltage Drop	V _F	—	0.39	—	V	I _F = 5A, T _J = +25°C
		—	0.46	0.53		I _F = 10A, T _J = +25°C
Leakage Current (Note 8)	I _R	—	—	0.4	mA	V _R = 45V, T _J = +25°C

- Notes:
5. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.
 6. Polyimide PCB, 2oz. Copper, minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.
 7. Max junction temperature guaranteed for 2 hours.
 8. Short duration pulse test used to minimize self-heating effect.

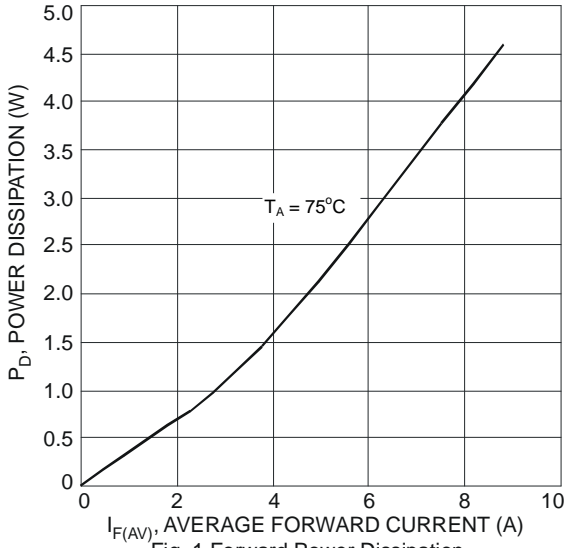


Fig. 1 Forward Power Dissipation

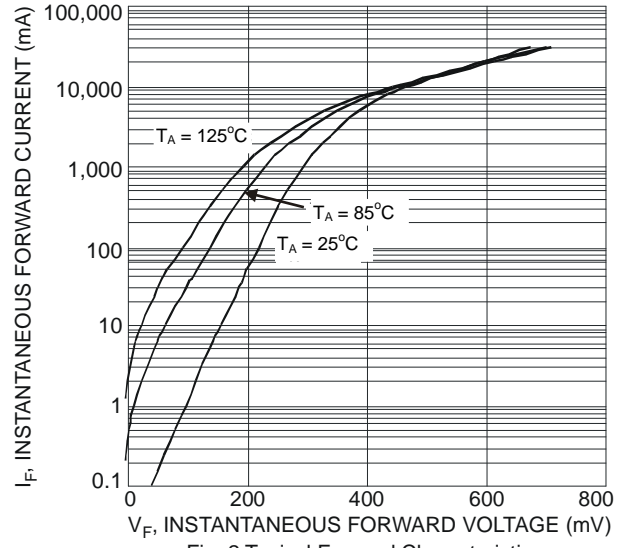


Fig. 2 Typical Forward Characteristics

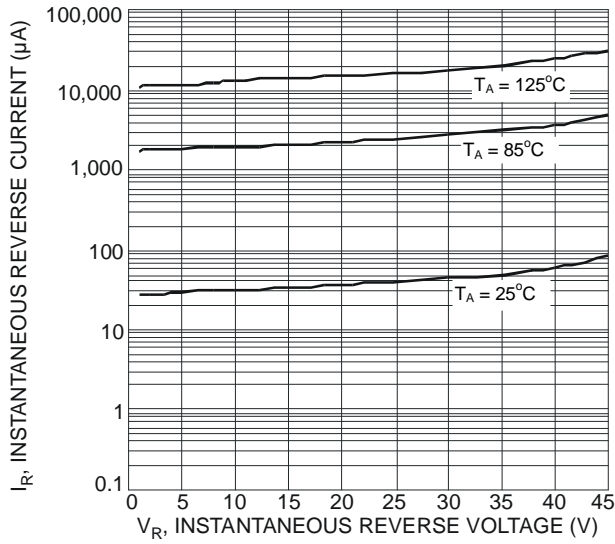


Fig. 3 Typical Reverse Characteristics

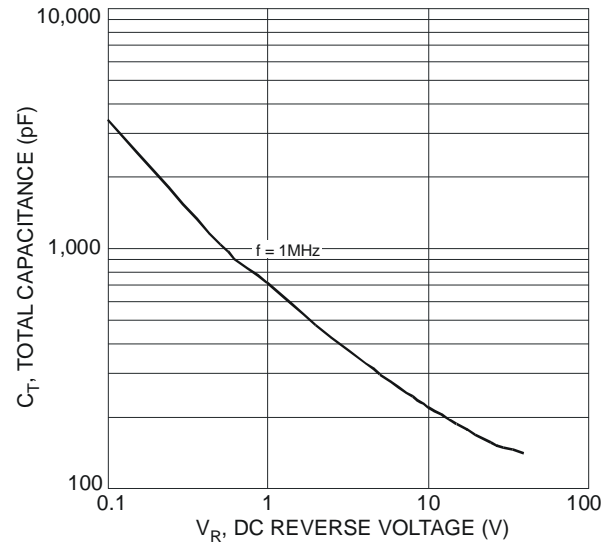


Fig. 4 Total Capacitance vs. Reverse Voltage

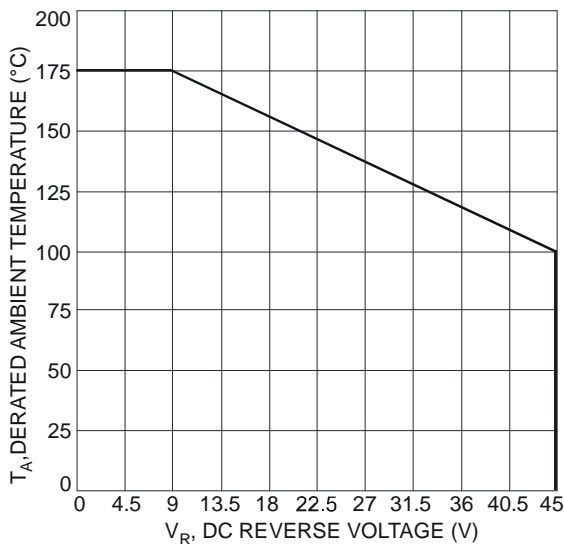


Fig. 5 Operating Temperature Derating

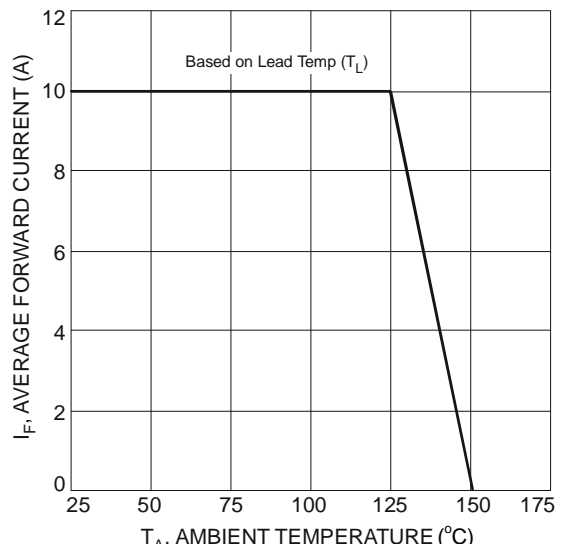
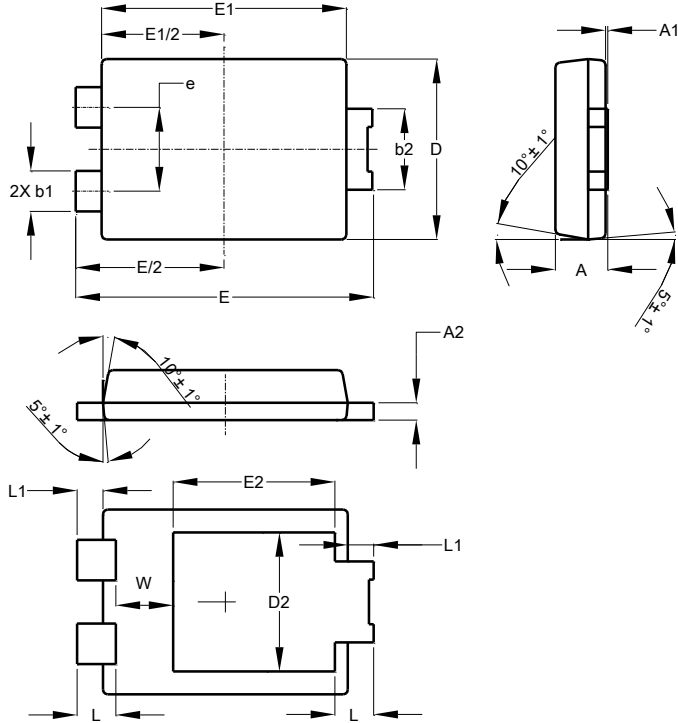


Fig. 6 Forward Current Derating Curve

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5

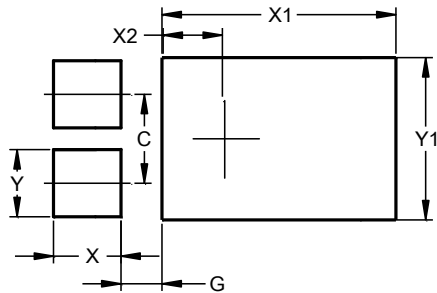


PowerDI5			
Dim	Min	Max	Typ
A	1.05	1.15	1.10
A1	0.00	0.05	--
A2	0.33	0.43	0.381
b1	0.80	0.99	0.89
b2	1.70	1.88	1.78
D	3.90	4.05	3.966
D2	--	--	3.054
E	6.40	6.60	6.51
e	--	--	1.84
E1	5.30	5.45	5.37
E2	--	--	3.549
L	0.75	0.95	0.85
L1	0.50	0.65	0.57
W	1.10	1.41	1.255
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5



Dimensions	Value (in mm)
C	1.840
G	0.852
X	1.400
X1	4.860
X2	1.310
Y	1.390
Y1	3.360

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