# APD Semiconductor, Inc.

# Super Barrier Rectifier TM

Using state-of-the-art SBR IC process technology, the following features are made possible in a single device:

#### Major ratings and characteristics

Characteristics	Values	Units
I <sub>F(AV)</sub> Rectangular Waveform	1.0 *	A
V <sub>RRM</sub>	20	V
V <sub>F</sub> @1A, T <sub>J</sub> =75°C	0.34	V, typ
T <sub>J</sub> (operating/storage)	-65 to 125	°C

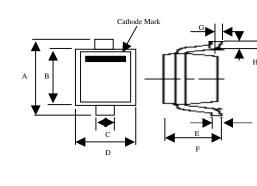
\*Note: Device monuted on a glass epoxy board, Board size: 50mm x 50m, Land size: 6mm x 6mm

### ELECTRICAL:

- \* Low Forward Voltage Drop
- \* Low Reverse Leakage
- \* Reliable High Temperature Operation
- \* Super Barrier Design
- \* Softest, fast switching capability
- \* 125°C Operating Junction Temperature

### MECHANICAL:

\* Molded Plastic SOD-323 package



SOD-323				
Di	Min	Max		
А	2.30	2.70		
В	1.60	1.80		
С	0.25	0.40		
D	1.15	1.45		
Е	0.10	0.18		
F	0.85	1.05		
G	-	0.10		
Н	0.20	0.40		
All Dimensions in mm				

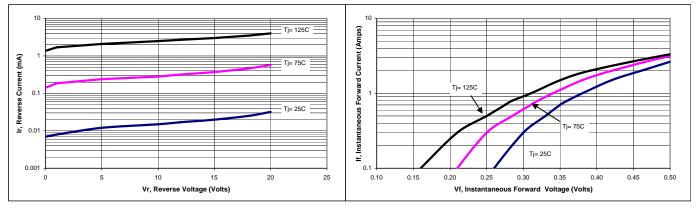
	SYMBOL			UNITS
DC Blocking Voltage Working Peak Reverse Voltage Peak Repetitive Reverse Voltage	V <sub>RM</sub> V <sub>RWM</sub> V <sub>RRM</sub>	20		Volts
Average Rectified Forward Current (Rated V <sub>R</sub> -20Khz Square Wave) - 50% duty cycle	I <sub>O</sub> <sup>(1)</sup>	1		Amps
Peak Forward Surge Current - 1/2 60hz	I <sub>FSM</sub>	18		Amps
Instantaneous Forward Voltage $I_F = 0.7A; T_J = 25^{\circ}C$ $I_F = 1A; T_J = 25^{\circ}C$ $I_F = 0.7A; T_J = 75^{\circ}C$	V <sub>F</sub>	Тур  0.38 	Max 0.40  0.34	Volts
Maximum Reverse Current at Rated $V_{RM}$ T <sub>J</sub> = 25°C T <sub>J</sub> = 75°C	I <sub>R</sub> <sup>(2)</sup>	Тур  	Max 0.2 2	mA mA
Operating and Storage Junction Temperature	TJ	-65 to +125		°C

(1) We recommend that the worst case current be no greater than 80% of the maximum rating of I  $_{\rm O}$ 

(2) Pulse width < 300 uS, Duty cycle < 2%

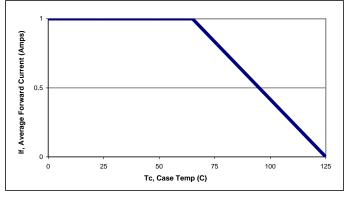
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**Figure 1: Typical Reverse Current** 

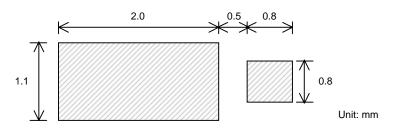
Figure 2: Typical Forward Voltage



#### Figure 3: Current Derating, Case\*

\*Device mounted on a 50mm x 50mm glass epoxy board, 50% duty cycle





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APD Semiconductor, Inc.

1 Lagoon Drive, Suite 410, Redwood City, CA 94065, USA Ph: 650 508 8896 FAX: 650 508 8865 Homepage: www.apdsemi.com email: info@apdsemi.com