



SBR20U50SLP

20A SBR SUPER BARRIER RECTIFIER PowerDI5060-8

Product Summary

VRRM (V)	lo (A)	VF max (V)	I _{R max} (mA)
50	20	0.52	0.5

Description and Applications

Packaged in the compact thermally efficient PowerDI5060-8 package, the DIODES™ SBR20U50SLP provides very low V_F and excellent reverse leakage stability at high temperatures. It is ideal for use as a rectifier, freewheel diode or blocking diode in:

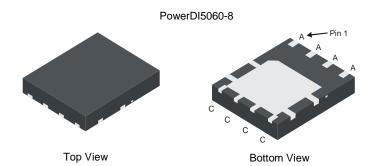
- DC-DC converters
- AC-DC adaptors

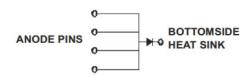
Features and Benefits

- Patented SBR[®] Technology Provides Superior Avalanche Capability Versus Schottky Diodes, Ensuring More Rugged and Reliable End Applications
- Reduced Ultra-Low Forward Voltage Drop (V_F); Better Efficiency and Cooler Operation
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure in High Temperature Operation
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at
 - https://www.diodes.com/products/automotive/automotive-products/.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe Solderable per MIL-STD-202, Method 208 (§3)
- Polarity: See Below
- Weight: 0.097 grams (Approximate)





Note: All four anode pins must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

Part Number	Pookage	Packing		
Part Number	Package	Qty.	Carrier	
SBR20U50SLP-13	PowerDI5060-8	2,500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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Marking Information

PowerDI5060-8



] | |= Manufacturer's Marking SBR20U50S = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 22 = 2022) WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VRM	50	V
RMS Reverse Voltage	V _R (RMS)	35	V
Average Rectified Output Current	lo	20	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	100	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	RθJA	30	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	Rejc	5	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +125	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

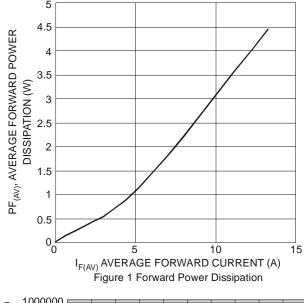
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop (Note 6)	VF		_	0.48	V	IF = 10A, T _J = +25°C
			0.46	0.52		$I_F = 20A, T_J = +25^{\circ}C$
Leakage Current (Note 6)	I _R	_	_	0.5	mΑ	$V_R = 50V, T_J = +25^{\circ}C$
Leakage Current (Note 6)		_	_	100		V _R = 50V, T _J = +125°C
Reverse Recovery Time	trr	_	57		ns ns	IF = 0.5A, IR = 1.0A
Reverse Recovery Time						$I_{RR} = 0.25A$
Total Capacitance	Ст		400		pF	V _R = 40V, f = 1MHz

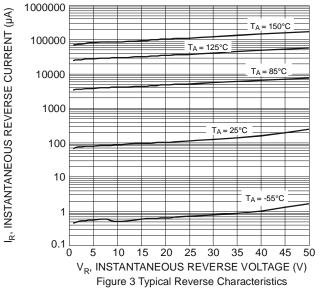
Notes: 5. Device mounted on FR4 substrate PC board with 10cm x 10cm copper pad.

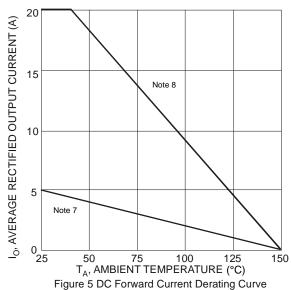
6. Short duration pulse test used to minimize self-heating effect.

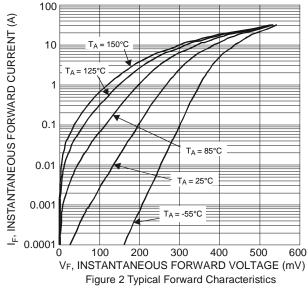


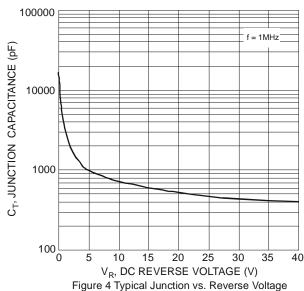


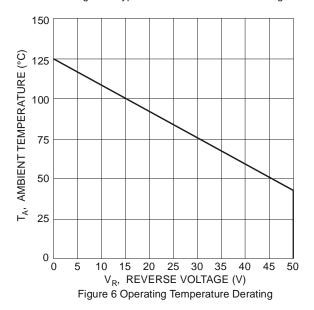












Notes: 7. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.

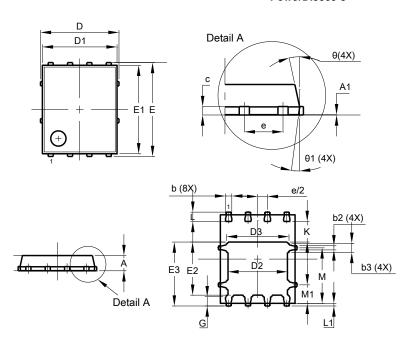
8. Device mounted on aluminum substrate PC board with 2-inch sq. copper pad + additional heatsink (Al 48mm x 35mm x 80mm).



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

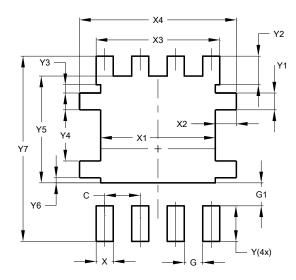


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
C	0.230	0.330	0.277		
D	į,	5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	6.15 BSC				
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	0.755		
Х3	4.420		
X4	5.610		
Y	1.270		
Y1	0.600		
Y2	1.020		
Y3	0.295		
Y4	1.825		
Y5	3.810		
Y6	0.180		
Y7 6.610			



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