

Super Barrier Rectifier ™

Using state-of-the-art SBR IC process technology, the following features are made possible in a single device:

Major ratings and characteristics

Characteristics	Values	Units
I _{F(AV)} Rectangular Waveform	30	Α
V_{RRM}	45	V
V _F @15A, Tj=125 ^O C	0.48	V, typ
Tj (operating/storage)	-65 to 150	°C

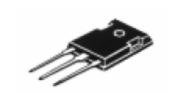
ELECTRICAL:

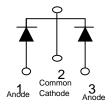
- * Low Forward Voltage Drop
- * Reliable High Temperature Operation
- * Super Barrier Design
- * Softest, fast switching capability
- * 150°C Operating Junction Temperature

Device optimized for low forward voltage drop to maximize efficiency in Power Supply applications

MECHANICAL:

* Molded Plastic TO-247 package





Maximum Ratings and Electrical Characteristics (at 25°C unless otherwise specified)

(dt 25 °C dhiess otherwise specified)						
	SYMBOL			UNITS		
DC Blocking Voltage Working Peak Reverse Voltage Peak Repetitive Reverse Voltage	$egin{array}{c} oldsymbol{V}_{RM} \ oldsymbol{V}_{RRM} \end{array}$	4	45			
Average Rectified Forward Current (Rated V _R -20Khz Square Wave) - 50% duty cycle	I _o	30		Amps		
Peak Forward Surge Current - 1/2 60hz	I _{FSM}	200		Amps		
Peak Repetitive Reverse Surge Current (2uS-1Khz)	I _{RRM}	2		Amps		
Instantaneous Forward Voltage (per leg) $I_F = 15A$; $T_J = 25^{\circ}C$ $I_F = 15A$; $T_J = 125^{\circ}C$	V_{F}	Тур 	Max 0.55 0.50	Volts		
Maximum Instantaneous Reverse Current at Rated V_{RM} $T_{\text{J}} = 25^{\circ}\text{C}$ $T_{\text{J}} = 125^{\circ}\text{C}$	I _R *	Typ 	Max 0.5 100	uA mA		
Maximum Rate of Voltage Change (at Rated $V_{\mbox{\tiny R}}$)	dv/dt	10,000		V/uS		
Maximum Thermal Resistance JC (per leg) Package = TO-247	R⊕ _{JC}	2		°C/W		
Operating and Storage Junction Temperature	T」	-65 to +150		°C		

^{*} Pulse width < 300 uS, Duty cycle < 2%

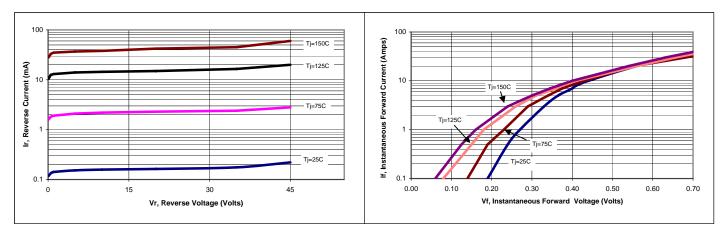


Figure 1: Typical Reverse Current

Figure 2: Typical Forward Voltage

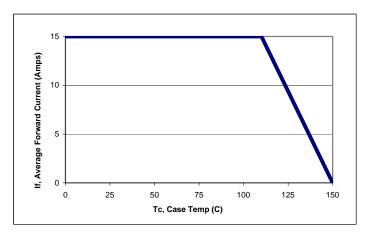


Figure 3: Current Derating, Case

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APD Semiconductor, Inc.

1 Lagoon Drive, Suite 410, Redwood City, CA 94065, USA Ph: 650 508 8896 FAX: 650 508 8865 Homepage: www.apdsemi.com email: info@apdsemi.com